ASTRONICS
TEST SYSTEMS

Racal Instruments™

1257A-D

Developmental RF Interface Unit

User Manual

Publication No. 980991   Rev. D

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Before undertaking any troubleshooting, maintenance or exploratory procedure, read carefully the **WARNINGS** and **CAUTION** notices.

CAUTION
RISK OF ELECTRICAL SHOCK
DO NOT OPEN

This equipment contains voltage hazardous to human life and safety, and is capable of inflicting personal injury.

If this instrument is to be powered from the AC line (mains) through an autotransformer, ensure the common connector is connected to the neutral (earth pole) of the power supply.

Before operating the unit, ensure the conductor (green wire) is connected to the ground (earth) conductor of the power outlet. Do not use a two-conductor extension cord or a three-prong/two-prong adapter. This will defeat the protective feature of the third conductor in the power cord.

CAUTION
SENSITIVE ELECTRONIC DEVICES
DO NOT SHIP OR STORE NEAR STRONG ELECTROSTATIC, ELECTROMAGNETIC, MAGNETIC OR RADIOACTIVE FIELDS

Maintenance and calibration procedures sometimes call for operation of the unit with power applied and protective covers removed. Read the procedures and heed warnings to avoid "live" circuit points.

Before operating this instrument:

1. Ensure the proper fuse is in place for the power source to operate.

2. Ensure all other devices connected to or in proximity to this instrument are properly grounded or connected to the protective third-wire earth ground.

If the instrument:

- fails to operate satisfactorily
- shows visible damage
- has been stored under unfavorable conditions
- has sustained stress

Do not operate until performance is checked by qualified personnel.

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CE Declaration of Conformity

We

Astronics Test Systems Inc.
4 Goodyear St.
Irvine, CA 92618

declare under sole responsibility that:

1257A-D2 (P/N 408437)
1257A-D4 (P/N 408441)
1257A-D6 (P/N 408426)

conform to the following Product Specifications:

Safety: EN/IEC 61010-1, 3rd Edition: Safety Requirements, Electrical
Equipment for Measurement, Control, and Laboratory Use

EMC:
Radiated Emissions: EN 55011:2009 (Amended by A1:2010) Class B
Conducted Emissions: EN 55011:2009 (Amended by A1:2010) Class B
Harmonics: EN 61000-3-2:2006 (Amended by A1:2009 & A2:2009)
Flicker: EN 61000-3-3:2008
Radiated Immunity: EN 61000-4-3:2006 (Amended by A1:2008)
EFT: EN 61000-4-4:2004 (Amended by A1:2010)
ESD: EN 61000-4-2:2009
Surge: EN 61000-4-5:2006
Conducted Immunity: EN 61000-4-6:2009
Magnetic Field Immunity: EN 61000-4-8:2010
Voltage Interruptions: EN 61000-4-11:2004
Voltage Dips: EN 61000-4-11:2004

Irvine, CA, May 30, 2013

David Johnston, Engineering Manager

DOCUMENT CHANGE HISTORY

Revision	Date	Description of Change
A	3/15/2013	Document Control release
B	6/6/2013	ECN02634. Update specifications in Chapter 1 for power and dimensional changes. Update figures in Chapter 2 to show latest usable mounting areas on front and rear panels. Update Appendix B with new graphics and added part numbers. Add appendix for new Configurator software tool.
C	8/19/2014	ECN04034. Adds information on Power-On Recall, Confidence Mode, and Event System. Also adds information on establishing a connection over USB. Add new Agilent switch information.
D	7/6/2015	ECN05930. Adds additional information to clarify Confidence Mode, Route:Verify, and Event System in Chapter 5.

Chapter 1
Overview and Features

The 1257A-D Developmental RF Interface Unit is a platform for in-house development with software and hardware building blocks to customize the RF system to meet your specific requirements.

When you ordered the system, you either referred to the product datasheet or received assistance from your Astronics Test Systems sales representative to determine the unit and accessories you needed. This would have included the appropriate-sized system chassis, the driver and adapter boards, and any other system hardware you need to install your components into the 1257A-D.

Chapter 2, **Component Installation,** assists you in installing the boards, hardware, and components into the chassis. **Chapter 3, Getting Started,** and **Chapter 4, LXI Web-Page Controls,** helps you set up and configure your customized system. **Appendix D, Component Configuration Example,** provides step-by-step instructions to set up and configure several specific components.

As needed, refer to the other chapters and appendices in this manual.

The chapters and appendices in this manual include information on:

- Overview and features

- Component installation and configuration

- System setup

- LXI web-page controls

- SCPI command basics

- SCPI command reference

- Troubleshooting

- Mounting hardware and cables

- Adapter board configurations

- Component configuration example

- Rack-mount installation

Overview

The 1257A-D Developmental RF Interface Units are high-performance switching and control systems available in 2U-, 4U-, and 6U-height configurations to accommodate a variety of unique requirements.

This development unit has 48-channel driver boards that can be scaled up to 480 channels to meet even the most complex designs with I/O available in TTL, open-collector, and pulse latching to control a variety of RF components.

The driver boards are customized with adapter modules that translate into the connector types used on the components. This makes it possible for manufactured cable assemblies to be used with commercially available RF components that mount on mechanical bracket options to easily build up a RFIU system.

The 1257A comes standard with a LXI/ Ethernet, USB, and GPIB control interface as well as a rich SCPI command set and IVI drivers for ease of use and compatibility with all software environments.

The 1257A series allows for

- Quick and easy device configuration through a web page interface
- LXI™ compliant remote control and monitoring through your Ethernet connection
- Simplified programming and greater software reuse with other leading text software through our IVI™ drivers
- Fast setup with no configuration jumpers or switches to set
- Rack-mount or benchtop use

The 1257A-D (**Figure 1-1** and **1-2**) includes the following features:

- 2U, 4U, and 6U height
- Expandable I/O up to 480 control, indicator, and LED channels
- Open-collector, TTL, latching, and pulsed-latching relay support
- Modular designed to simplify custom development
- Rails to drop in many different microwave components
- Custom brackets that adapt many different manufacturers' products
- Front and rear panels easily removable for custom fabrication
- Optional internal mounting rails and shelves
- Scalable software to support user designs
- Relay counters with end-of-life warning

Figure 1-1, 1257A-D – Front View (2U and 4U Assembly Configuration)

Figure 1-2, 1257A-D – Rear View (2U and 4U Assembly Configuration)

Web-Page Interface

The 1257A has a built-in web-page interface to allow full access and control of the instrument and driver boards. The system controls require you to have a recent version of the Java™ engine installed on your computer.

The web-page interface provides information regarding VISA resource strings, Refer to **Chapter 4, LXI Web-Page Controls** for further information.

Software Control

The 1257A provides extensive software capabilities, including:

- Exclude Lists. Allows you to prevent simultaneous closure of switches in a specified exclude group. The switches are then mutually exclusive.

- Include Lists. Reduces programming effort. When two or more modules are on an include list, they respond simultaneously to a command sent to any of them.

- Verification. Allows you to verify the status of all switches with proper hardware configuration. This can be either through drive feedback or indicator feedback, depending on the relay type.

- Path-Level Switching. Allows complex paths to be defined with open and close elements, stored in non-volatile memory, and be controlled remotely.

- Non-Volatile Memory. Stores and recalls switch configurations and 1257A user preferences (such as GPIB address).

- Power-Up Recall. Automatically recalls a complete switching system configuration from non-volatile memory at power-up.

- Self-Test. Ensures that critical system components are functioning correctly. This test executes automatically at power-up.

- Relay Counters. Records each relay activation. Notification given of relay warning and end-of-life limits.

- Latching and fail-safe relay operation

Additional Features

Ease of Use

The 1257A Developmental RF Interface Unit has many design features to make it easy and convenient to use:

- Fast Setup. There are no hardware configuration jumpers or switches to set.

- Flexible Controller Interface. The controller (host computer) may communicate with the 1257A using Ethernet, USB, or GPIB interfaces. This allows an external computer to automate the operation of the 1257A.

- Command Set Commonality. The 1257A supports the Standard Commands for Programmable Instruments (SCPI) Language. This command set is used in

other switching platforms such as the 1256, 1257, 1260 and 1830 series VXI. For a description of the SCPI command set and its usage, see **Chapter 5, SCPI Command Basics**. For a listing of all SCPI commands used with the 1257A, see **Chapter 6, SCPI Command Reference**.

LAN Reset Button

The LAN Reset button is used to reset the LAN settings of the 1257A to a factory default setting which includes automatic IP address configuration and password reset.

To activate the LAN reset, press the button for a minimum of ten seconds. When released, the LAN status light on the front panel momentarily turns off and then turns back on to indicate the LAN status.

You may need to rediscover the LAN IP address (see **Chapter 3, Getting Started**).

For initial password setting information, see **Setting the Password** in **Chapter 4, LXI Web-Page Controls**.

Power On Recall

The 1257A provides a feature to automatically recall "State 0" (known as the Reset State) at power up. To do so, enable the **Power On Recall** check box on the **System>Setup** page. (Refer to **Setup Tab** in **Chapter 4, LXI Web-Page Controls.**)

Note: *Store a "State 0" (Reset State) using the "*SAV 0" command prior to enabling and using this functionality.*

You may use Power On Recall to recall a start-up state on the 1257A. Doing so synchronizes the internal state of the 1257A system with the stored relay state on the next system power on.

Recalling a start-up state is particularly useful when you are using **latching relays with read backs**. Enabling the Power On recall ensures that at the following power-up, the 1257A internal state will synchronize with the start-up relay state set up in "State 0."

Note: *For latching relays, if a "close" path that exists in "State 0" is already closed, the path will not be opened during the "Reset" command that is automatically executed during "Power On Recall."*

Recall State (0) after a '*RST' (or SYSTem::PRESet) Command

This feature is available only if the **Power On Recall** feature is selected. Enabling this feature causes "State 0" to be recalled after a '*RST' or (SYSTem::PRESet) command execution. For additional information, refer to the preceding section, **Power On Recall**, and **Chapter 4, SCPI Command Basics**.

1257A-D Front and Rear Panels

Front and real panels of the 1257A-D (**Figure 1-3** and **Figure 1-4**) can be removed and modified to your configuration based on the available usable space in the 1257A-D and on the panel size. (The panels on the 6U-chassis are similar to those on the 4U chassis shown in **Figure 1-4**.) Refer to **Figure 2-1** and **Figure 2-2** in Chapter 2 for usable space specifications.
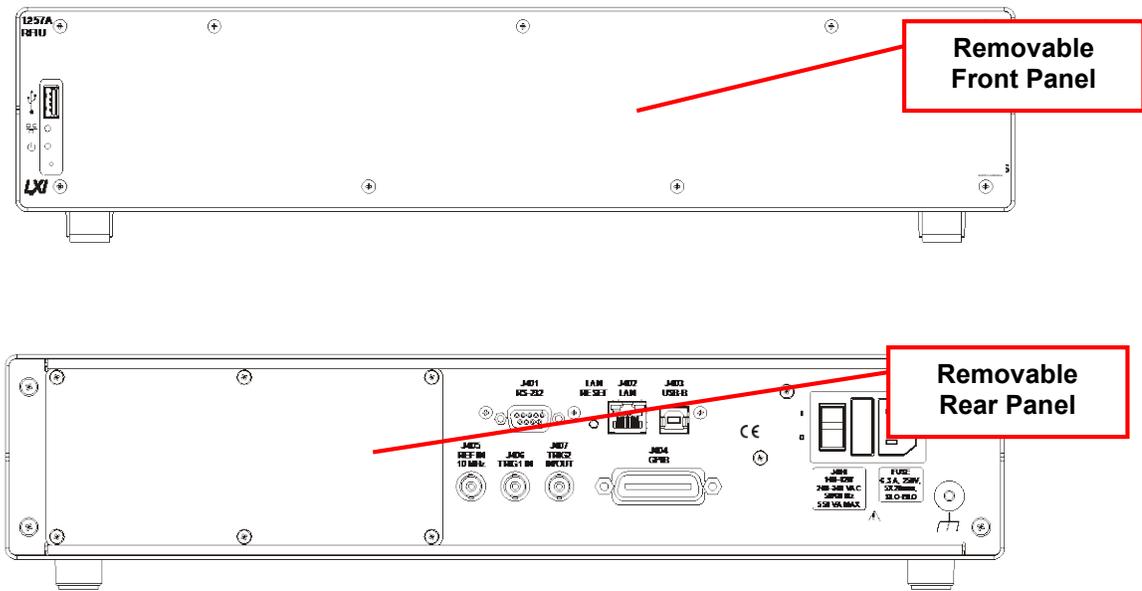
Figure 1-3, 1257A-D, 2U - Front and Rear Views

Removable Front Panel

Removable Rear Panel
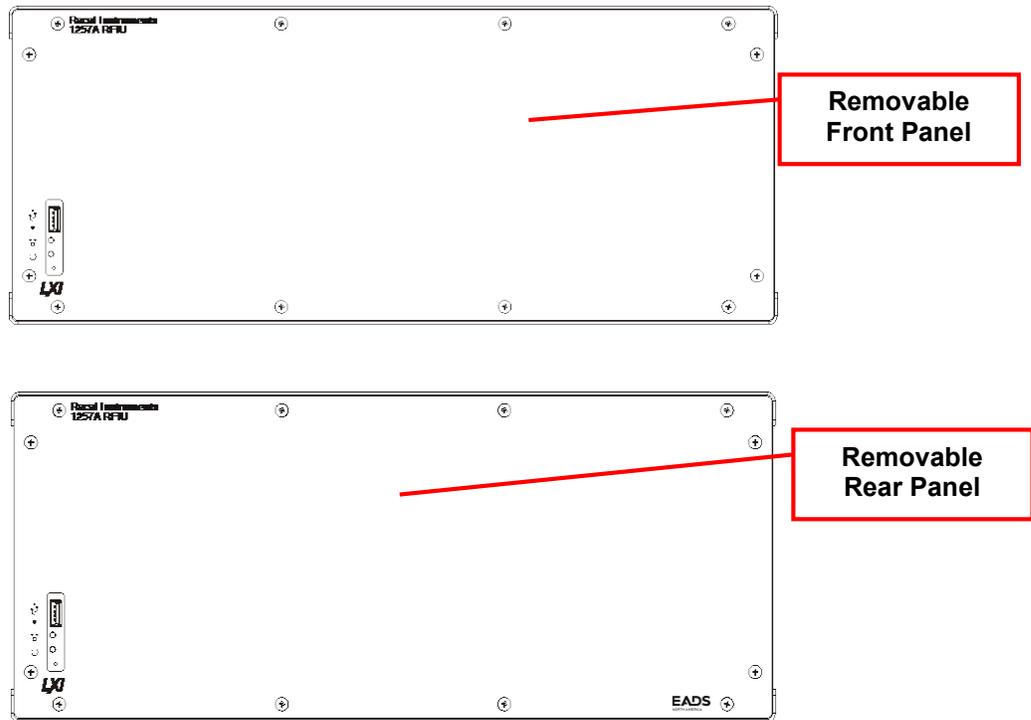
Figure 1-4, 1257A-D, 4U - Front and Rear View

Figures 1-3 and **1-4** shows the front and rear views of the 1257A-D base chassis. **Figures 1-5** through **1-7** detail the connectors, LEDs, switches, and fuses.

USB A Connector

LAN Status LED

System Reset Switch Access Hole

System Power Indicator
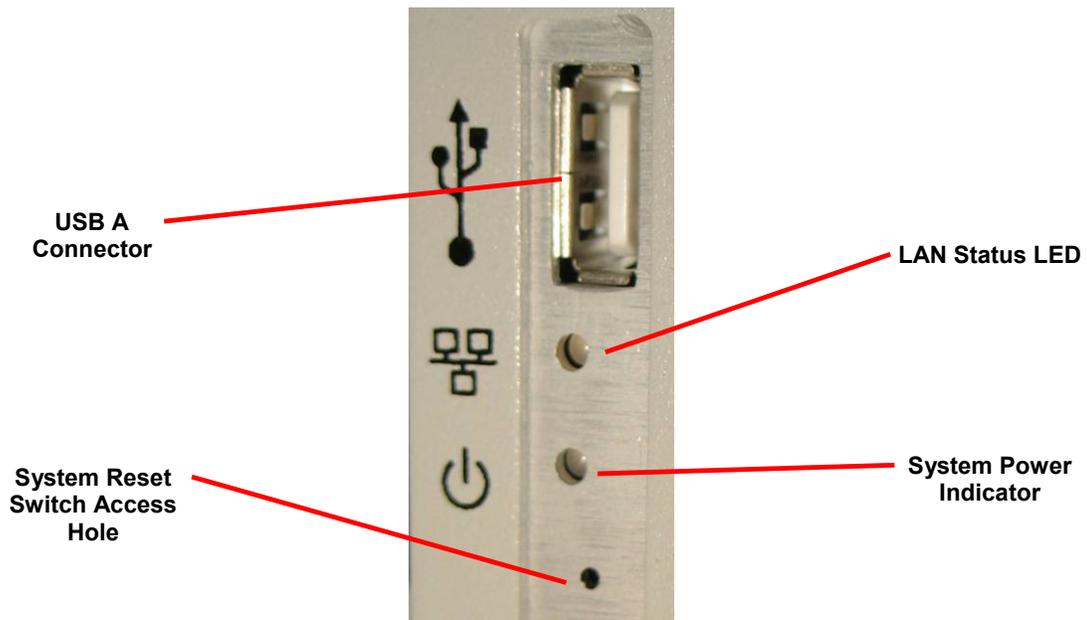
Figure 1-5, 1257A-D Front View Detail
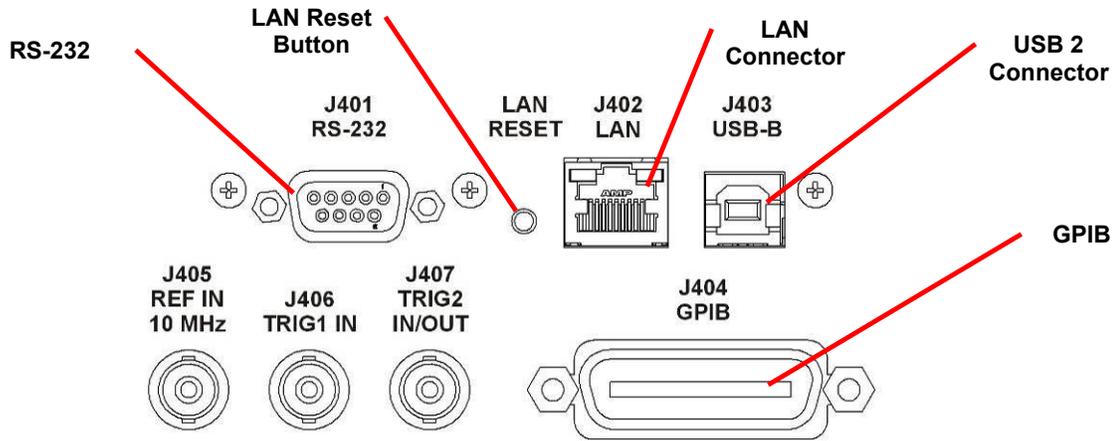
Figure 1-6, 1257A-D Rear View Detail – Connector Area

Note: *At this time, the Trigger and 10 MHz connectors on the rear of the 1257A are not supported by the operating software.*

The 2U connector area is shown in the figure above. But the appearance and components are typical of all 1257A configurations.
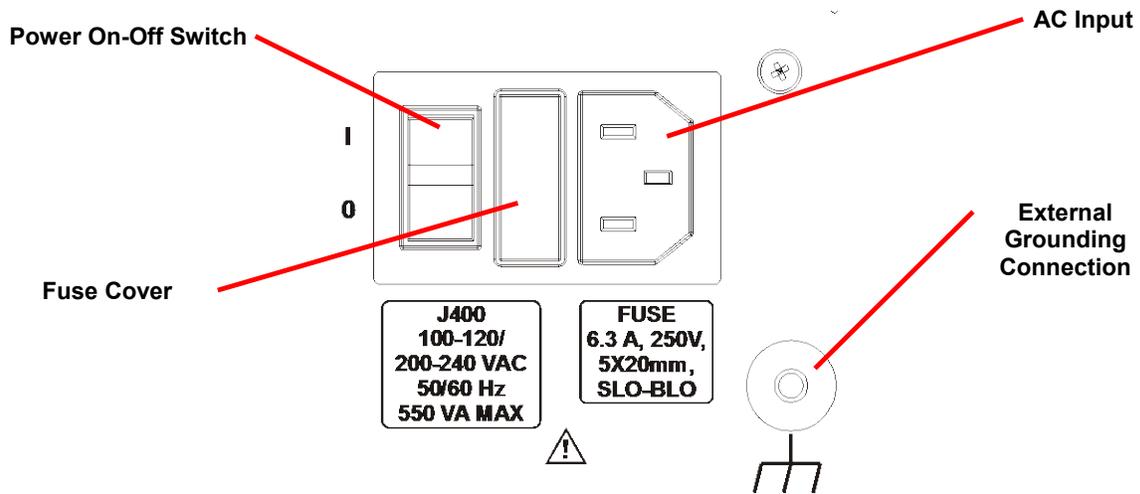
Figure 1-7, 1257A-D Rear View Detail – Power Control Area (Appearance Typical)

The 2U power control area is shown in the figure above. But the appearance and components are typical of all 1257A configurations.

Note: *Use of the Ground Connection requires a 6-32 screw of approximately ½" length.*

Chassis Identification Label

The identification label (**Figure 1-8**) on the side or rear of the 1257A chassis includes the part number (P/N) and build revision level, the serial number (S/N), and the chassis Media Access Control (MAC) address.

Astronics Test Systems Inc.

MODEL: *1257A-D RF IU

P/N: ¹*1257A-D4-1 REV. 1.10*

S/N: *00001251*

MAC ID: 00:0D:2B:00:06:AB   FSCM 21793
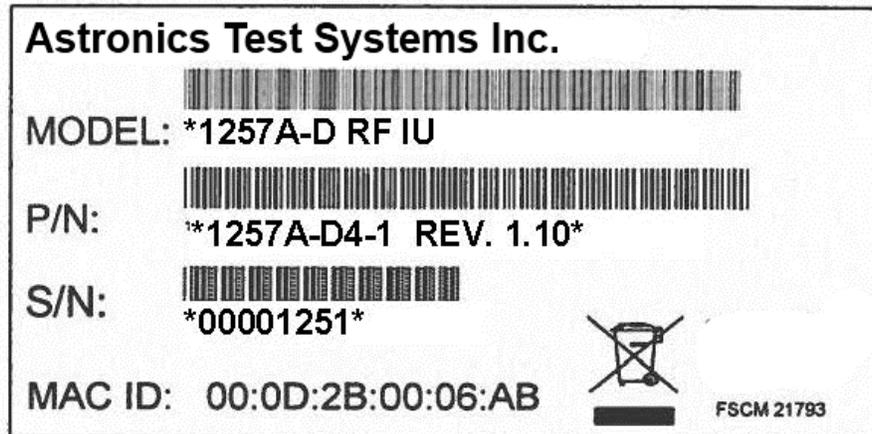
Figure 1-8, Chassis Indentification Label (Appearance Typical)

Specifications

Power Supply

Input Voltage	100 – 120/200 – 240 VAC
Input Frequency	50/60 Hz
Power Consumption	550 VA (maximum)
Fuse	250 V IEC SLO-BLO, 5x20 mm, 6.3 A (qty 2)

Power

Maximum Available Power (Total)	**2U (75 W)** -12 V @ 3.0 A +3.3 V @ 4.0 A +5 V @ 4.0 A +12 V @ 3.0 A +24 V @ 3.0 A
	4U, 6U (170 W) -12 V @ 3 A +3.3 V @ 4 A +5 V @ 4 A +12 V @ 3 A +24 V @ 5 A

Maximum Internal Power Dissipation (Ethernet and USB)	**2U** 75 W @ 45° C
	4U, 6U 170 W @ 45° C

General

System Management	Firmware upgrades: via LAN port Data file I/O: via USB interface

Software

Drivers	IVI-C and IVI-COM LabVIEW™ (version 9.0)
User Interfaces	Web pages SCPI & SCPI scripting

Front Panel I/O and Controls

USB Interface	USB 2.0 full-speed Type A port
Indicators	LAN status System power
Reset	System reset switch

Rear Panel I/O and Controls

Control Port Interfaces	USB 1.1 full-speed Type B port GPIB port (IEEE-488.2) LAN port: RJ-45 (LXI Class C) RS-232: (Factory debug port)
Indicators	LAN status
Reset	LAN reset switch
Power	Main system power
Fuses	Access cover

Note: At this time, the Trigger and 10 MHz connectors on the rear of the 1257A are not supported by the operating software.

Driver Board

Up to 10 driver boards per system	
No. of Banks	3
No. of Outputs/Bank	16
No. of Inputs/Bank	16
No. of LED Drivers Bank	16
Types	
Open Collector (408429-001) *	Maximum Voltage: 30 V Off Leakage Current: 50 µA On Currrent (Max): 600 mA Driver On Voltage (Max): 0.4 V @ 600 mA

TTL (408455-001) *	Hi Output Voltage:
	    4.8 V @ I_{out} = 90 µA
	    3 V @ I_{out} = 1 mA
	Lo Output Voltage: 0.4 V @ 600 mA

Inputs	Lo Input (Max): 0.8 V Hi Input (Min): 2.5 V Maximum Input Voltage: 30 V Input Leakage Current: 600 µA @ 30 V

LED Drivers	LED Driver current:
	    5 mA nominal
	    1–20 mA programmable
	Driver Compliance Voltage: 0.8 V

Note: * *For adapter boards that connect outputs to inputs, the output leakage current will be the sum of the input and output leakage currents.*

Conformance Testing

Emissions/Immunity	EN61326: 2006, Class B
Safety	EN61010-1: 2010-06
LXI Conformance	Class C
Mechanical Chassis Weight (without switches)	**1257A-D2-1 (2U):** 21.5 lbs (9.8 kg) **1257A-D4-1 (4U):** 24.0 lbs (10.9 kg) **1257A-D6-1 (6U):** 26.5 lbs (12.0 kg)
Dimensions	**1257A-D2-1 (2U)** 3.47" H (w/feet 3.91") x 17.00" W x 16.00" D (8.82 cm (w/feet 9.93 cm) x 43.18 cm x 40.64 cm) **1257A-D4-1 (4U)** 6.90" H x 16.63" W x 24.34" D (17.53 cm x 42.24 cm x 61.82 cm) **1257A-D6-1 (6U)** 10.40" H x 16.63" W x 24.34" D (26.42 cm x 42.24 cm x 61.82 cm) Dimensions are without handles, ears, or cable connectors.

Environmental

Temperature	Operating: 0° C to 50° C (Ethernet and USB) Storage: -40° C to 71° C
Relative Humidity	80% RH at 40° C

Reliability

Relay Operations Counter	Running total of operations stored in on-board non-volatile memory

Ordering and Accessory Information

Table 1-1, Model and Part Number Information

Part Number	Description
1257A-D2-1	Racal Instruments 1257A-D 2U Developmental RF Interface Unit
1257A-D4-1	Racal Instruments 1257A-D 4U Developmental RF Interface Unit
1257A-D6-1	Racal Instruments 1257A-D 6U Developmental RF Interface Unit

Table 1-2, Included Accessories

Part Number	Description
980991	Users manual
600620	AC power cord
922676	IVI driver
922670	LXI Discovery Browser software
922725	Configurator Software Tool

Table 1-3, Optional Accessories

Part Number	Description
602269	European power cord (unterminated)
602269-001	Africa power cord
602269-003	UK power cord
602269-008	China power cord
500310-001	GPIB cable, 1 meter
500310-002	GPIB cable, 2 meter
407813	Rack-mount slides (pair), 4U or 6U
408415	Rack-mount brackets (ears) (pair), 2U
408491-001	Rack-mount brackets (ears) (pair), 4U
408428-001	Rack-mount brackets (ears) (pair), 6U

Table 1-4, Orderable Parts

Part Number	Description	For additional information, refer to
Various	Driver boards	Chapter 2
Various	Mounting panels and plates	Appendix B
Various	Cables	Appendix B
Various	Adapter boards	Appendix B & C
408010-001	Front panel, 2U, Blank*	Chapter 2
458003	Rear panel, 2U, Blank*	Chapter 2
458030	Front panel, 4U, Blank*	Chapter 2
458039	Rear panel, 4U, Blank*	Chapter 2
458257	Front panel, 6U, Blank*	Chapter 2
458262	Rear panel, 6U, Blank*	Chapter 2
408450-001	Accessory Mounting Plate Kit	Chapter 2
408449-001	Vertical Mounting Rails (qty 4)	Chapter 2

A blank front and rear panel are included on your 1257A-D unit. They may be removed and customized if required. The blank-panel part numbers are listed in case you wish to order another panel to replace your original.

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Chapter 2
Component Installation and Configuration

This chapter will assist you in installation and configuring the various components that you install into the 1257A-D unit.

Sections in this chapter include:

- Mounting considerations and limitations

- Installing driver boards

- Installing adapter boards

- Attaching components to mounting brackets and plates

- Installing components

- Cabling components to the front and rear panels

Configuring the system to recognize installed components is included in **Chapter 3, Getting Started.**

Appendix D, Component Configuration Example, provides step-by-step instructions to set up and configure several specific components.

Warning

Before you open the case to install any components, remove AC power to the 1257A. Failure to do so may result in personal injury and/or equipment damage.

Caution

Use standard ESD precautions including ground straps and static-safe work surfaces whenever working inside the 1257A and handling sensitive electronics such as the gateway or driver boards.

Mounting Considerations and Limitations

Figures 2-1 and **2-2** show the usable mounting space on the various external panels and inside rails and panels.

Figures 2-3 through **2-5** show the internal configurable space that can be used to mount various components.

Figures 2-6 through **2-8** provide information on installing driver boards and their power and control cables.

Figure 2-9 shows an example of how driver boards can be stacked and mounted over each other. When you stack driver boards, cables and components must be taken into account for space and access considerations. The maximum driver board capacity shown in **Table 2-1** may be reduced depending on how you complete your installation.

The remainder of the chapter deals with installing adapter boards, components, panels, and cabling.

The front and rear panels can be used to mount connectors for outside access. **Figure B-1** in **Appendix B, Mounting Hardware and Cables** includes the bracket-mounting specifications to cut out holes on the panels.

Appendix B, Mounting Hardware and Cables, shows a list of supported components and associated adapter cards and brackets that you will need to mount the component into the 1257A-D chassis.

Table 1-4. Orderable Parts, in **Chapter 1**, lists part number information in case you need additional front or rear blank replacement panels.

Note: Before you drill any holes in the panels, be sure to remove the panels from the 1257A chassis so as not to leave any metal shavings inside the box.
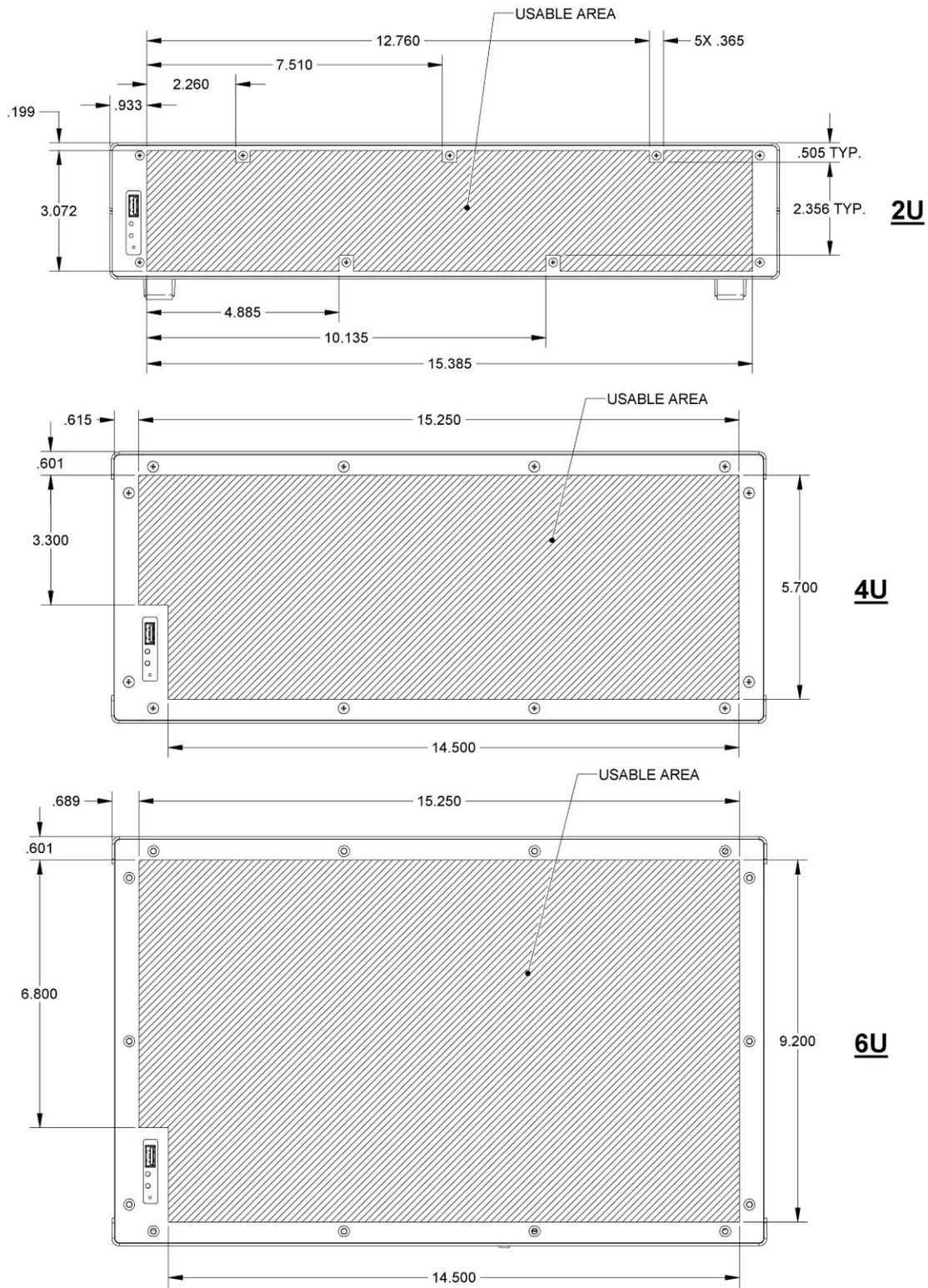
Figure 2-1, External Panel Usable Mounting Area – Front Views
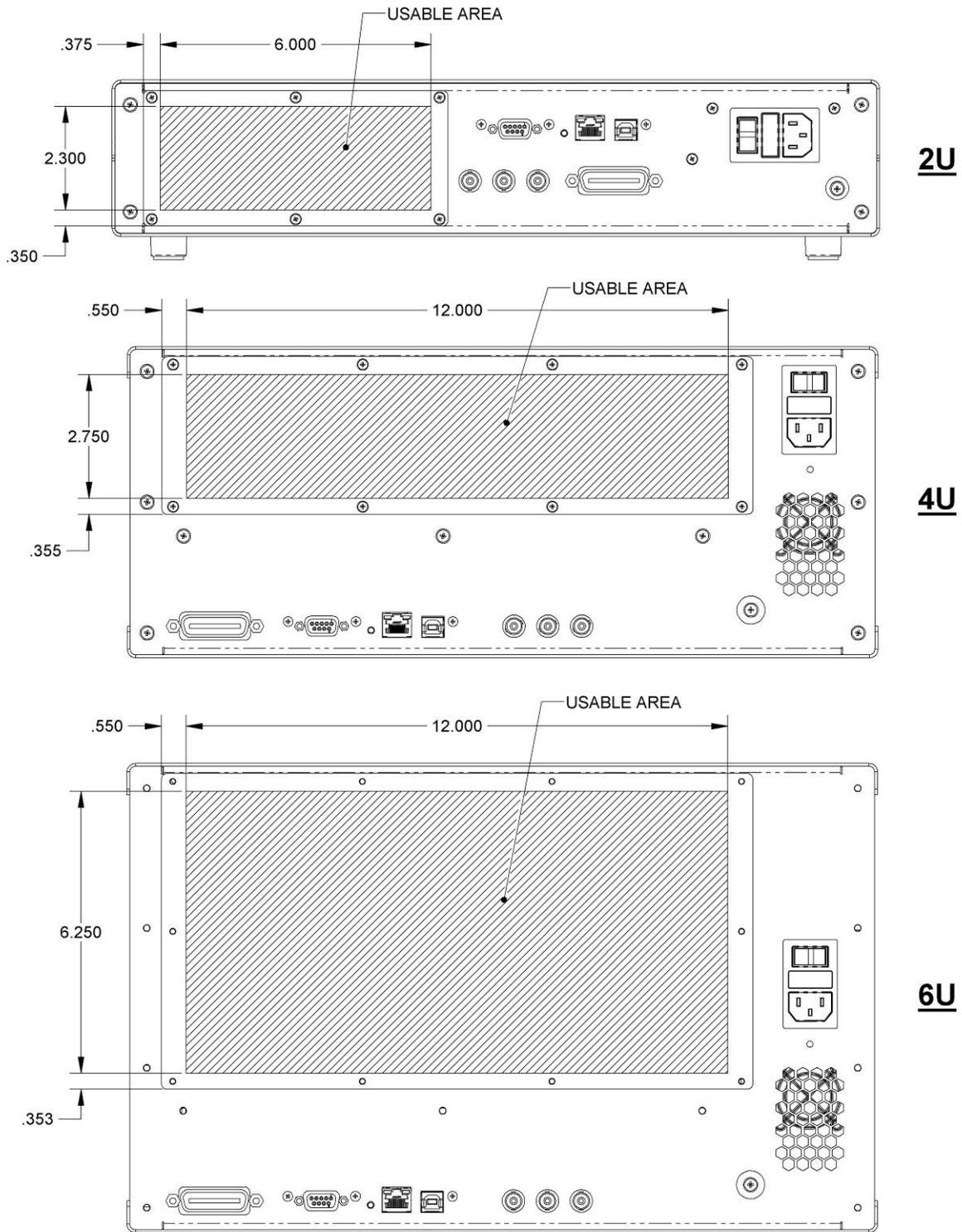
Figure 2-2, External Panel Usable Mounting Area – Rear Views
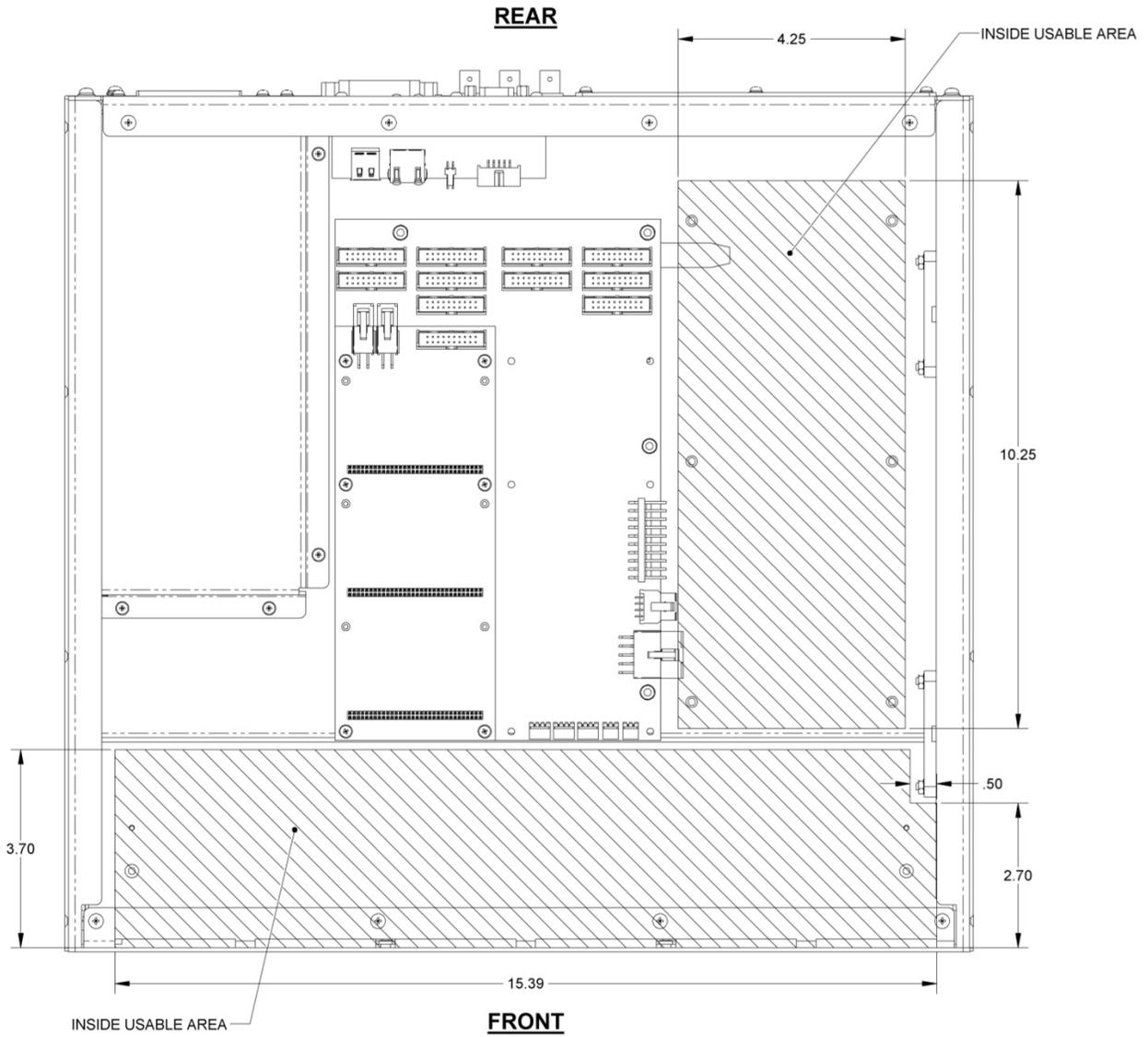
Figure 2-3, Internal Usable Mounting Area – 2U

REAR

INSIDE USABLE AREA

12.81

9.70

11.89

14.25

FRONT

INSIDE USABLE AREA
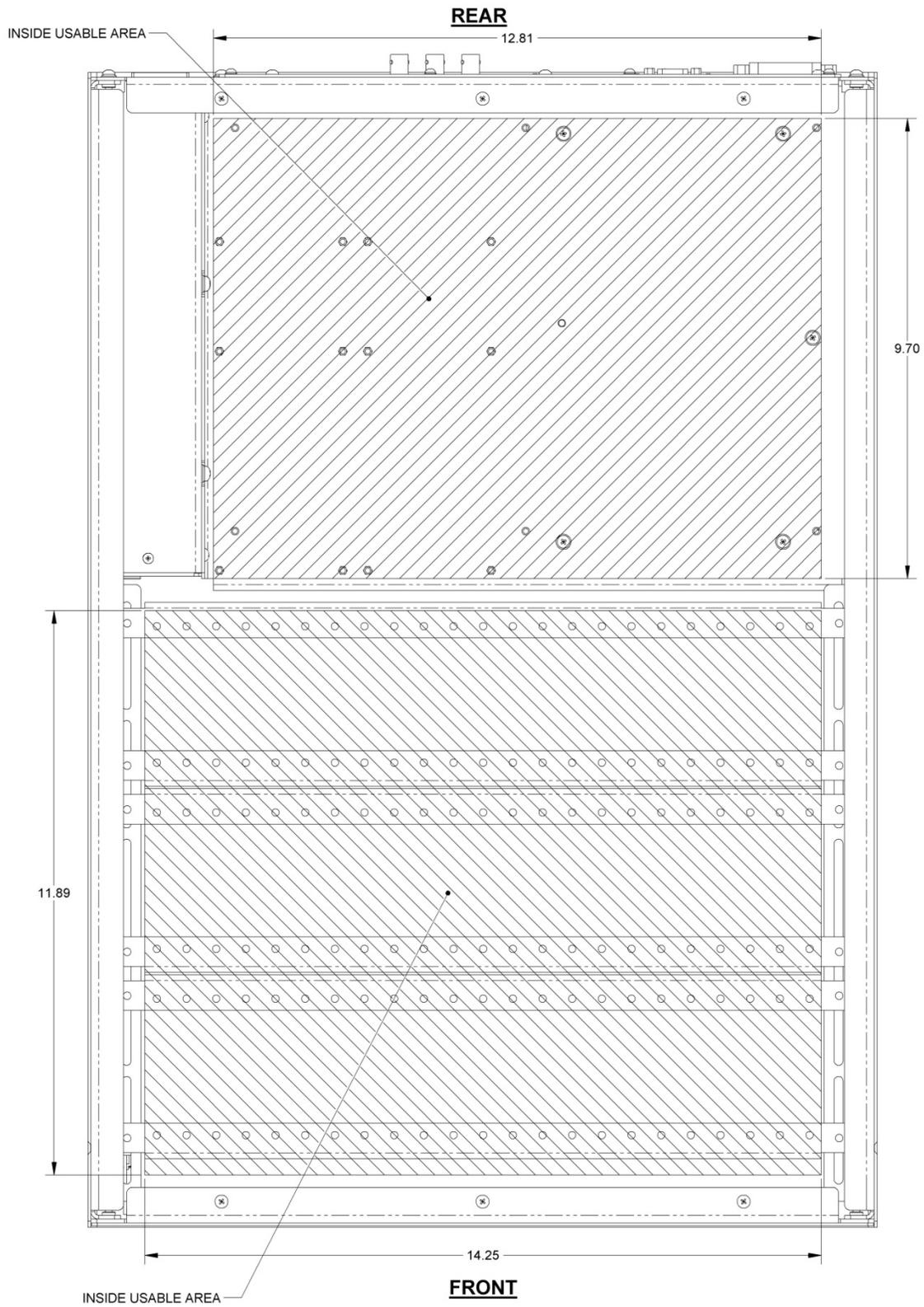
Figure 2-4, Internal Usable Mounting Area (Top View) – 4U and 6U

INSIDE USABLE AREA FOR MOUNTING EXTRA DRIVER BOARDS, SINGLE OR DOUBLE STACKED, SIDE BY SIDE

REAR

3.00

6.13

INSIDE USABLE AREA FOR MOUNTING EXTRA DRIVER BOARD, SINGLE.

7.75

8.17

FRONT
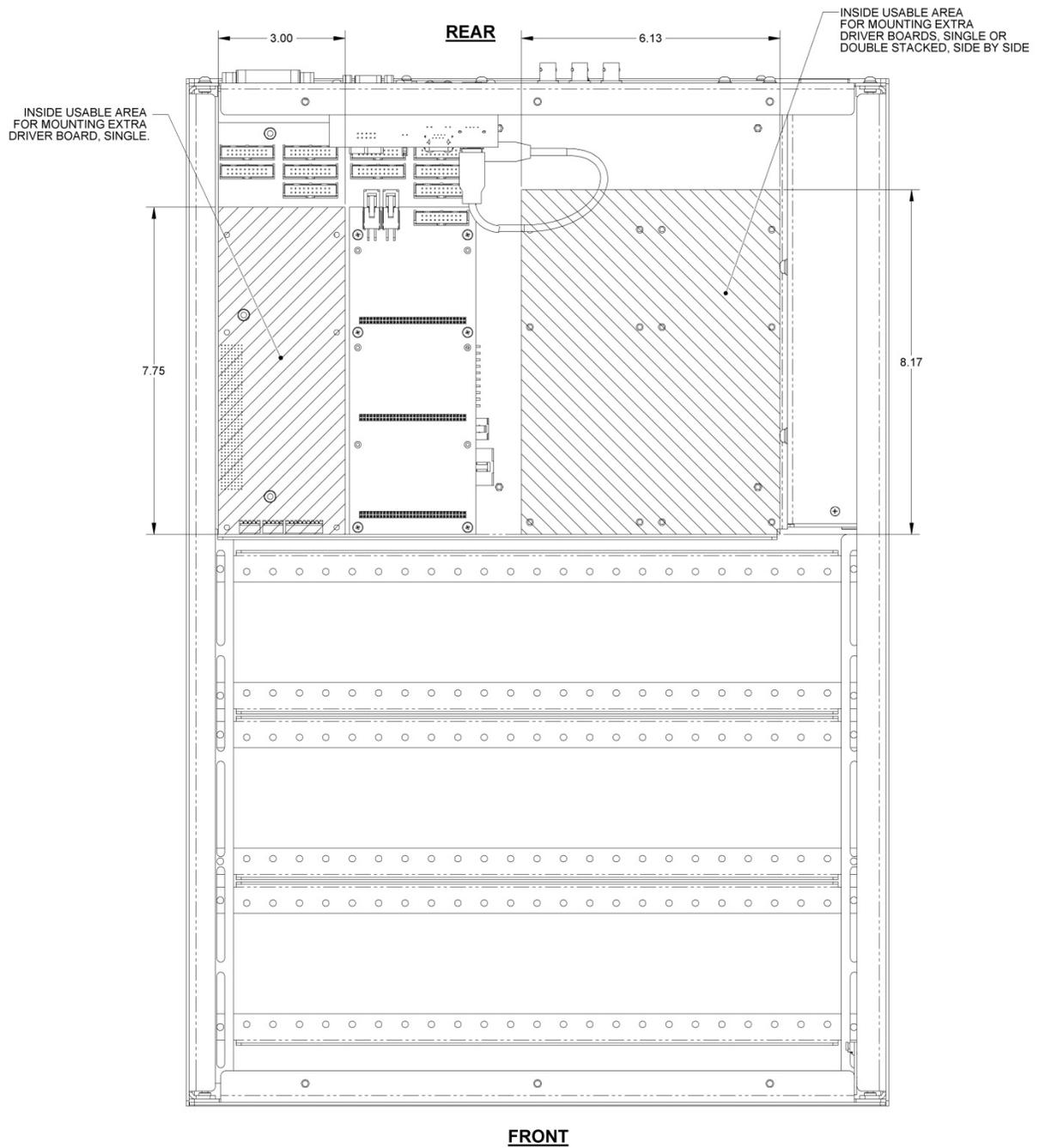
Figure 2-5, Internal Usable Mounting Area (Bottom View) – 4U and 6U

Installing Driver Boards

Table 2-1, **Driver Board Capacity**

Model	Maximum Number of Driver Boards
2U	2
4U	6
6U	10

Table 2-2, Driver Board Types

Kit Part Number	Type
408429-001	Open collector, power provided through V_{ext}
408455-001	TTL

Driver Board Types
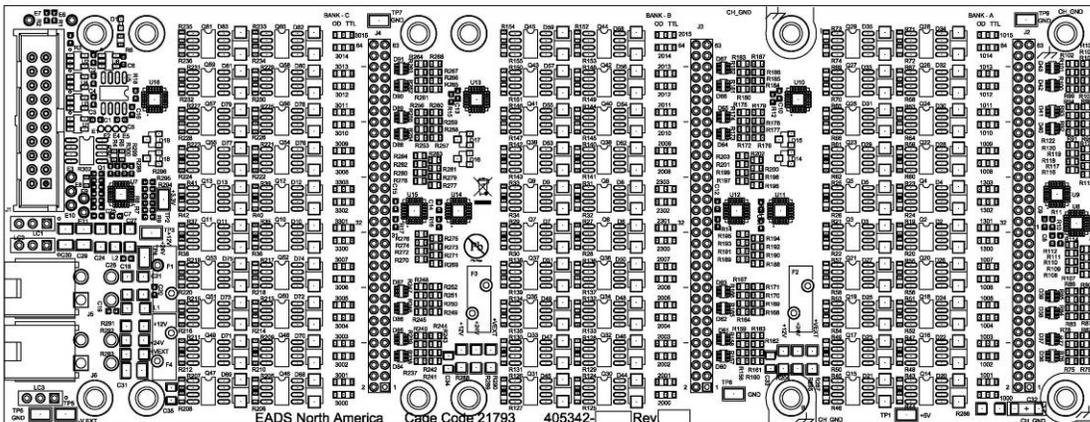
Figure 2-6, Driver Board: 405342-001 (Appearance Typical)

Installing the Driver Board

Refer to **Figure 2-7** and **Figure 2-8** for driver board installation and cabling.

1. Determine location for driver board on the main gateway board or on the shelf next to it. (In some models, you can also use the space on the Accessory Mounting Plate, discussed later in this chapter.)

2. Use included standoffs to mount the driver board (6 locations).

3. Connect control ribbon cable from the gateway board to the driver board.

4.   Connect power cable from the power supply to the driver board.
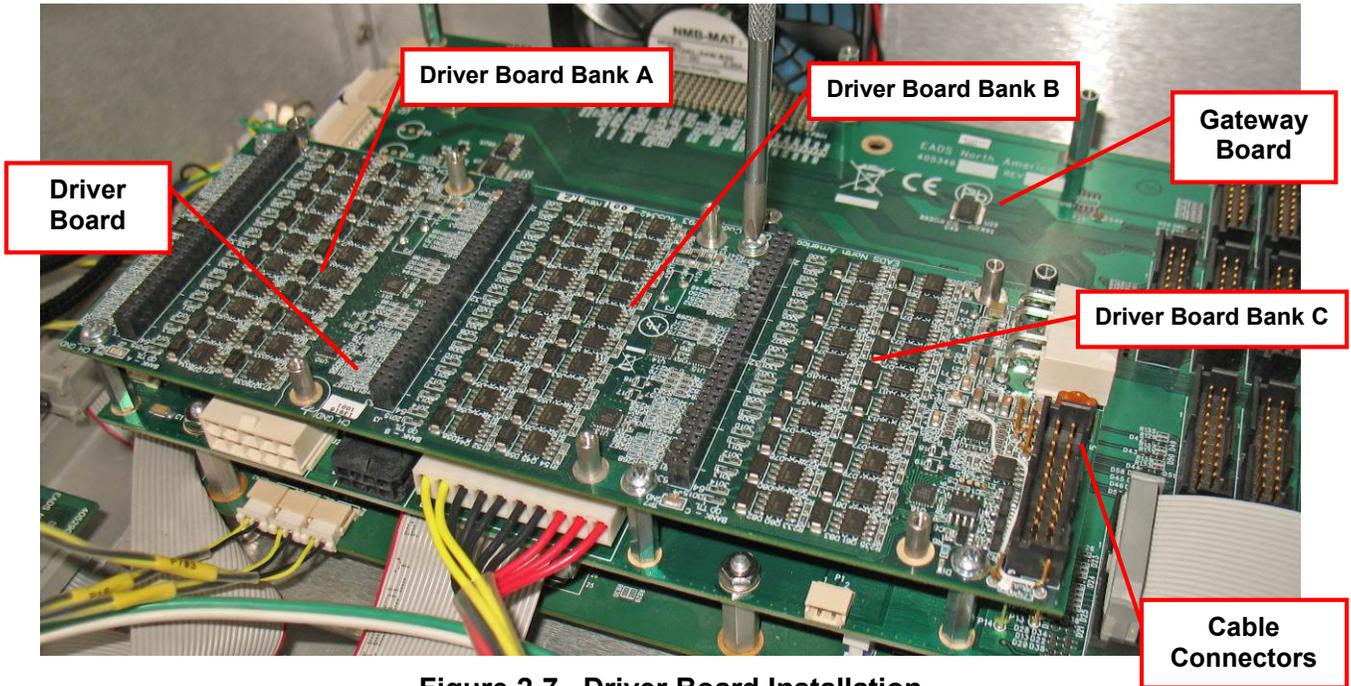
Figure 2-7, Driver Board Installation
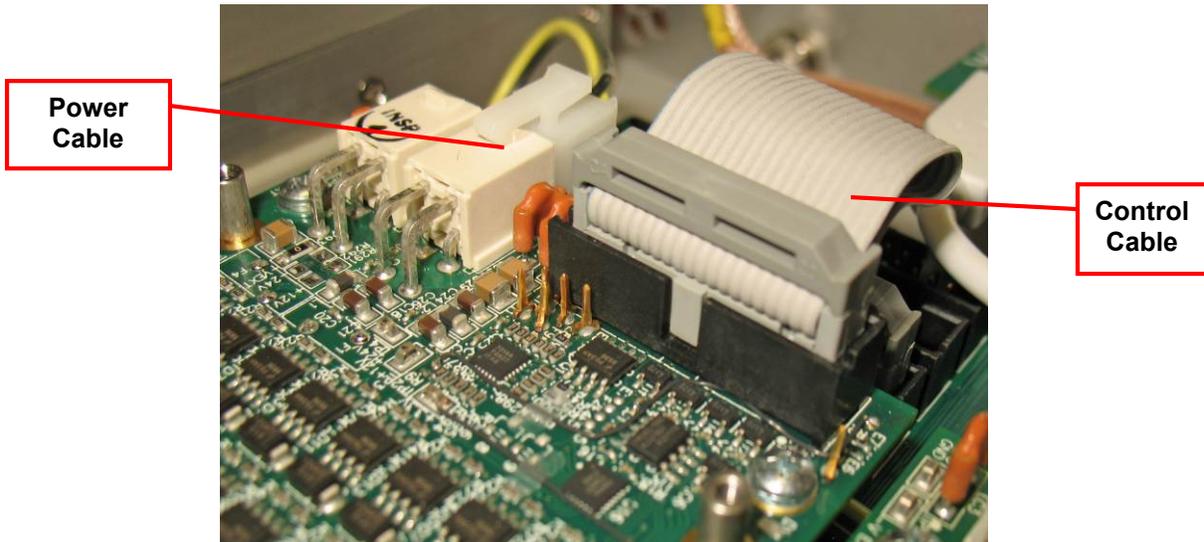
Figure 2-8, Power and Control Cables

Driver Board Stacking

Depending on the space available in your chassis, you may be able to stack another driver board on top of the first one.

See **Figure 2-9** as an example showing a driver board stacked on top of another driver board. Both boards have adapter boards as well. (The cables are not shown in this figure but need to be taken into account for space considerations.)

Use the hardware and standoffs included in your driver board kit to attach the second driver board. Remove the original attaching screws and replace them with standoffs. Prior to adding the board, ensure that the lower adapter boards and cables are attached properly and secure.
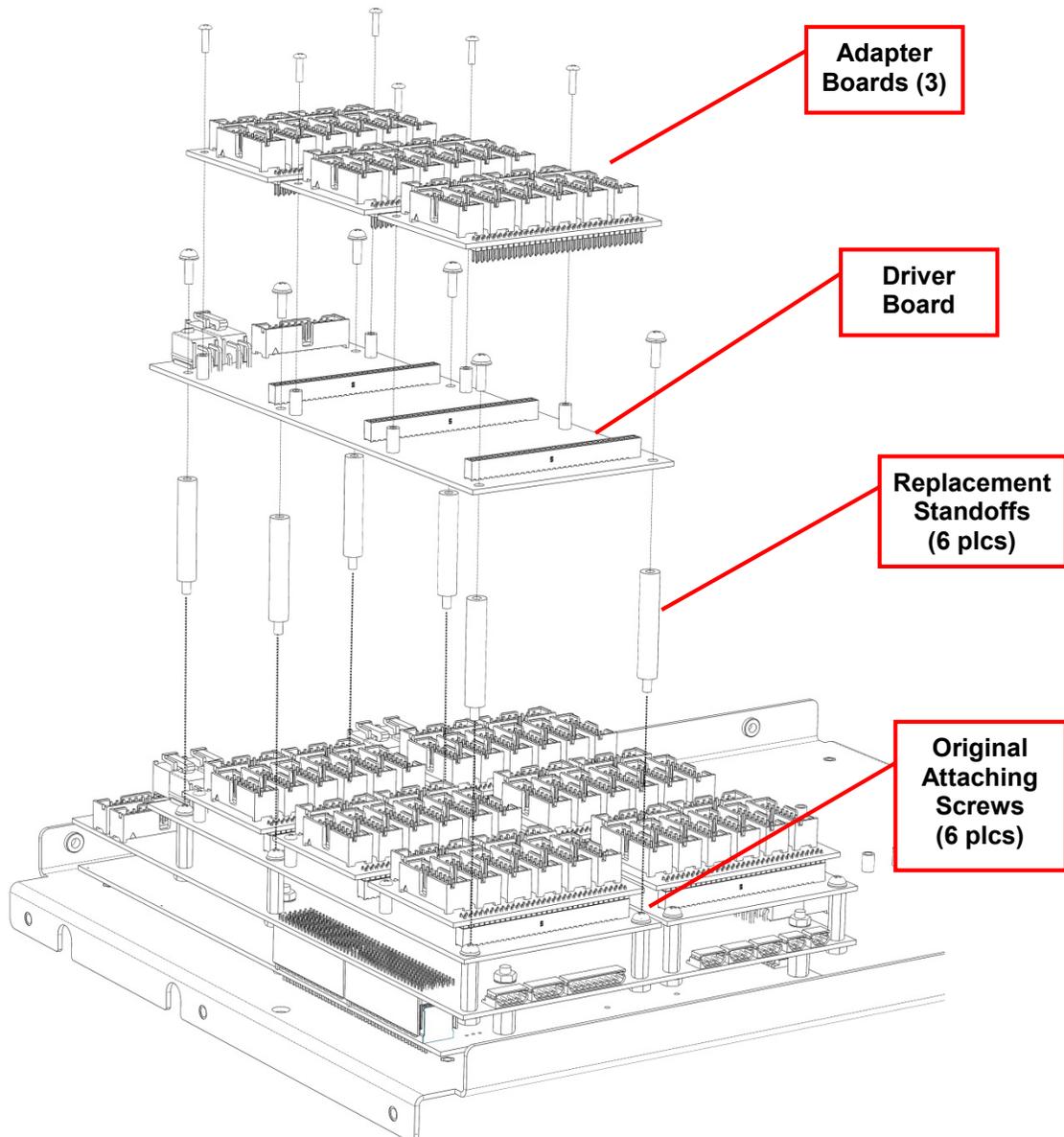
Figure 2-9, Driver Board Stacking

Installing Adapter Boards

Before installing the adapter boards, ensure that you have the correct boards for the components (switches, relays, and attenuators) that you are using. Refer to **Appendix B, Mounting Hardware and Cables** for detailed information.

Installing Adapter Board(s)

Refer to **Figure 2-10** for adapter board installation.

1. Determine location for adapter board on the driver board.

    Important Note: Refer to **Figure 2-7** *to determine (and record) the driver board position (Bank) and the specific adapter board you are using. You will need this information when you configure your system in* **Chapter 3***.*
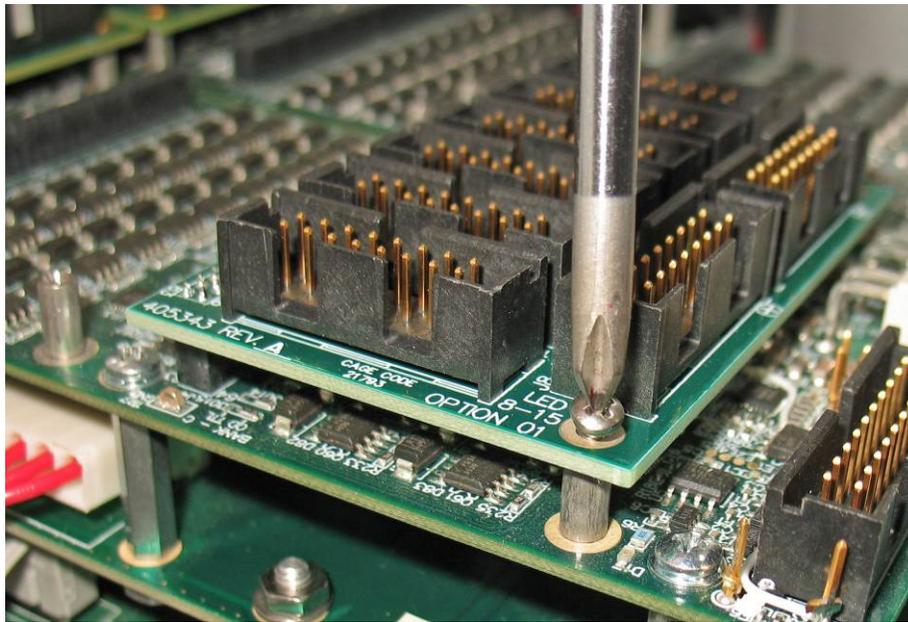
Figure 2-10, Adapter Board Location on Driver Board

2. Mount the adapter board to the driver board using the long pin connector (J11).

3. Secure the board by fastening two screws to the standoffs on the driver board.

4. Repeat steps 1 through 3 as needed for other adapter boards.

Attaching Components to Mounting Brackets/Plates

The following procedure is illustrated with numerous components that can be used. (Refer to **Figure 2-11** through **2-13**.) Generally follow this procedure for the component to which you are adding a mounting bracket or plate.

As needed, refer to **Appendix B, Mounting Hardware and Cables** for the appropriate bracket or plate to use.

To install a mounting bracket/plate, perform the following:

1. Select the appropriate bracket/plate for the component you are using.

Figure 2-11, Matching the Switch Mounting Bracket (Appearance Typical)

2. Use a screwdriver to secure the switch to the bracket/plate using the included screws.
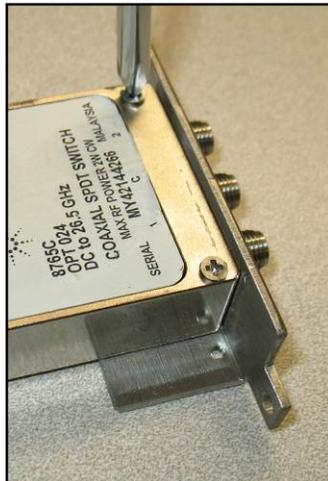
Figure 2-12, Securing the Mounting Bracket (Appearance Typical)
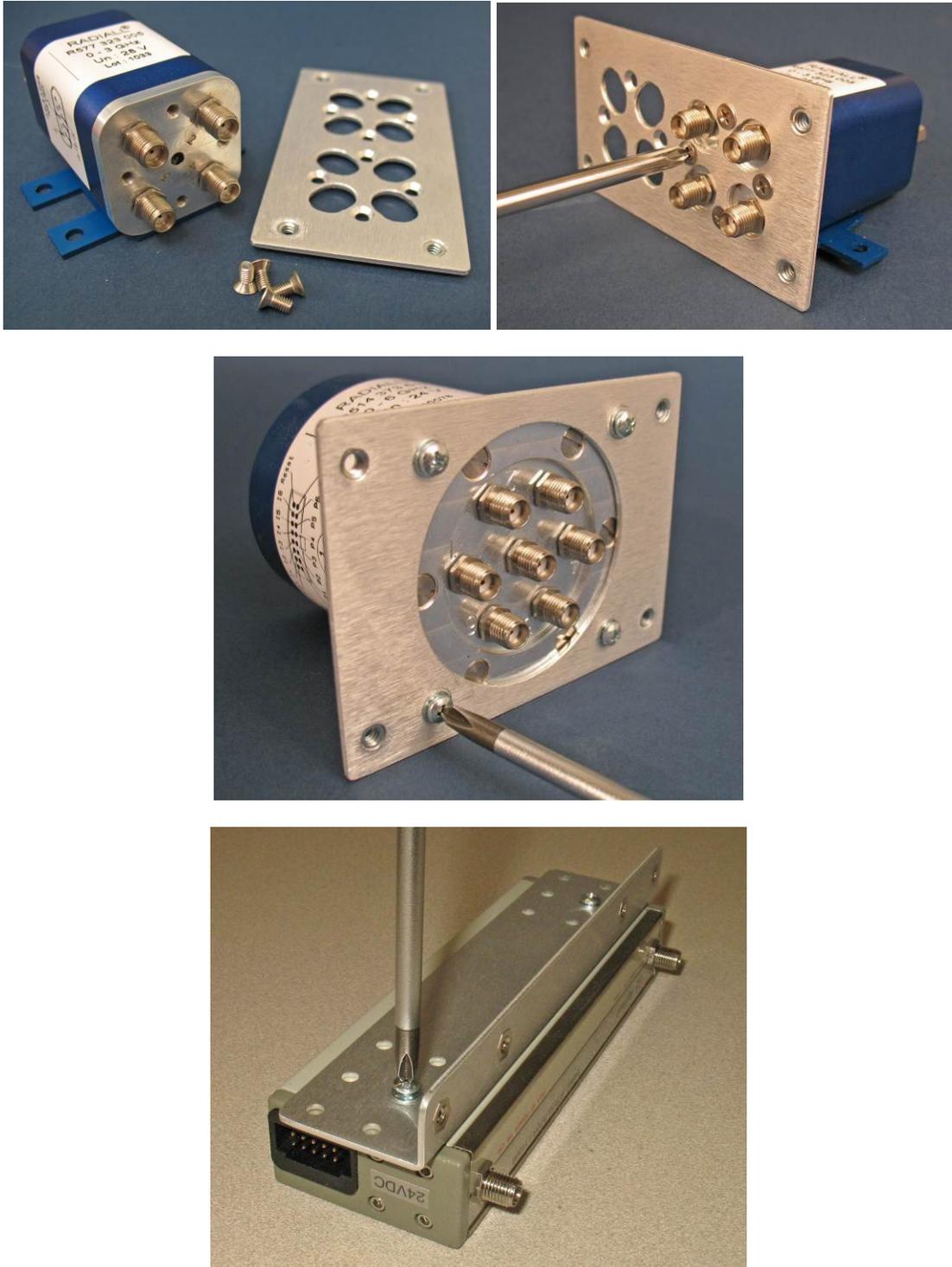
Figure 2-13, Examples of Various Components and Mounting Brackets/Plates

Installing Components

Adjusting the Chassis Rails

A number of mounting rails are already installed inside the 1257A chassis. Adjust them as necessary for the configuration of your components and mounting plates.

Attaching the Mounting Plates to the Rails

Connect the components (with their attached brackets or plates) to the rails using the included screws. See **Figure 2-14** for examples of these connections.
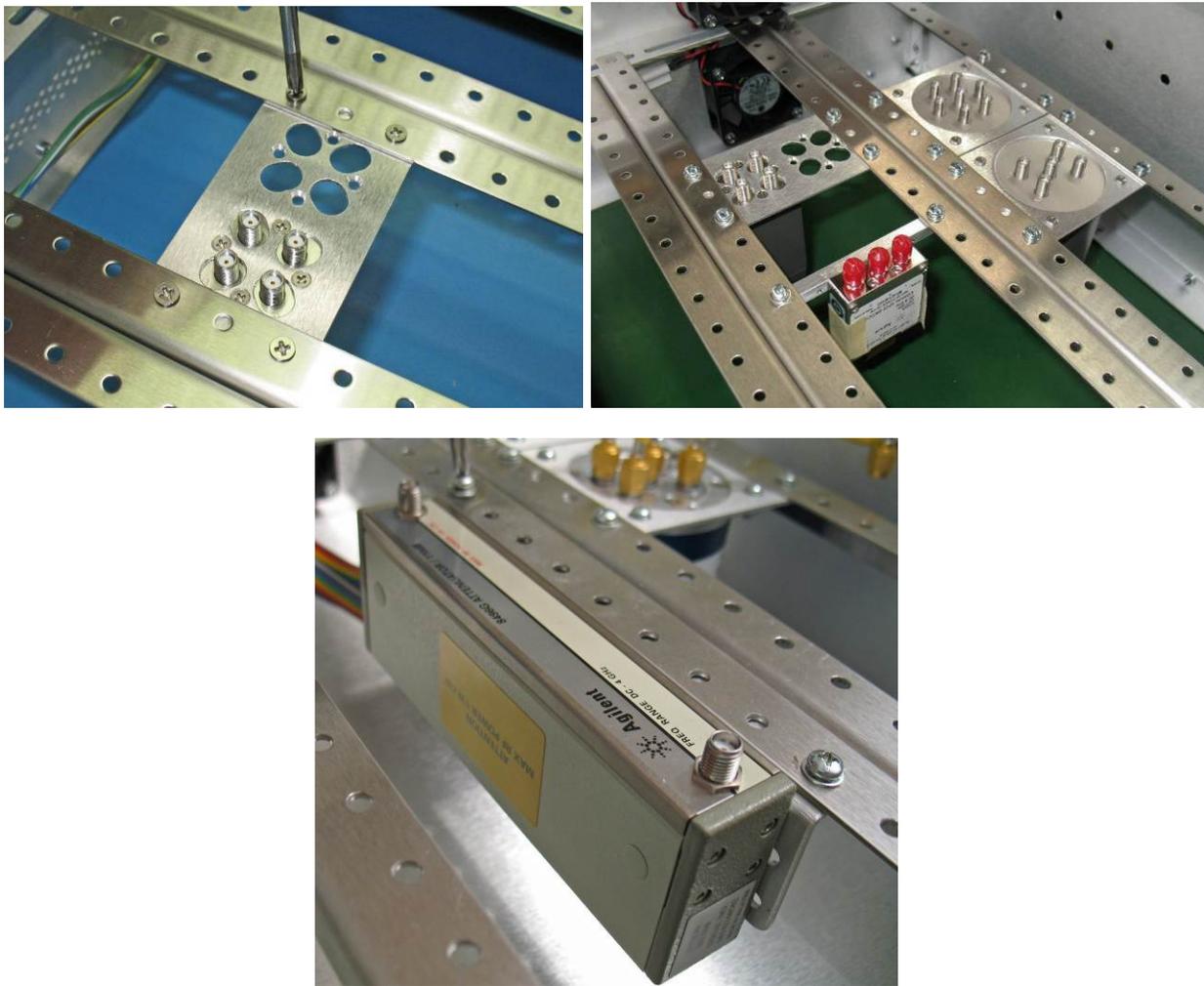
Figure 2-14, Examples of Installing Components on Rails

Adding the Accessory Mounting Plate

The optional Accessory Mounting Plate Kit (PN 408450-001) is available for additional component and driver board mounting inside the 1257A. The plate is designed for use with 4U- and 6U-sized configurations and comes with screws to mount the plate to standoffs onto the existing chassis table.

Optional Vertical Mounting Rails (PN 408449-001) are used to mount the components to the Accessory Mounting Plate. Four rails (two pair) come in each kit along with mounting screws.

Figure 2-15 and **Figure 2-16** shows the installation of the Accessory Mounting Plate, with three sets of Vertical Mounting Rails. Two sets of sample switches are shown as installation examples. (Switches are optional accessories and can be ordered as needed.)
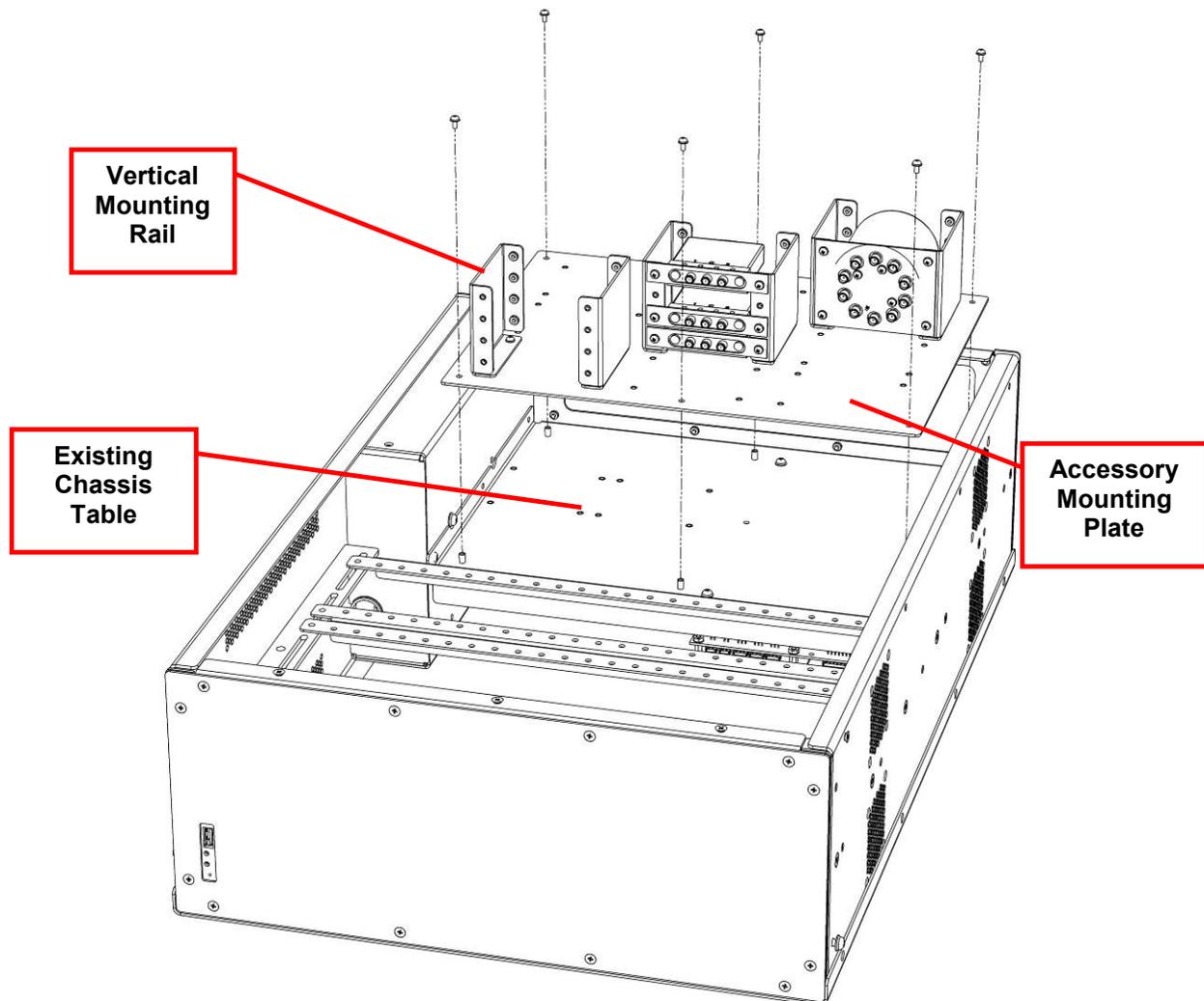
Figure 2-15, Inserting Accessory Plate and Mounting Rails
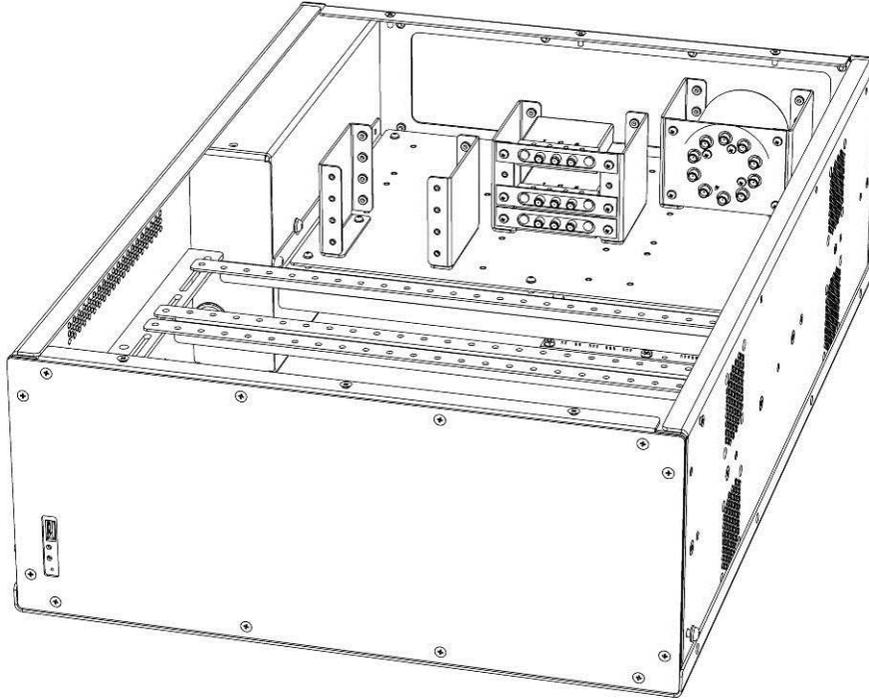
Figure 2-16, Installed Accessory Plate and Components (Example)

Cabling Components to the Adapter Boards

Connect cables from the various components to the correct adapter boards. **Figure 2-17** shows the use of cables with standard connector ends. If you have to solder pigtails or customize your ribbon cable connector, refer to **Figures 2-18** and **2-19** and **Table 2-3**. **Figure 2-20** shows cables connected to the adapter board.

Refer to the tables in **Appendix B, Mounting Hardware and Cables** to determine which adapter board is correct for the component installed.

Important Note: Record the adapter board position (Bank) on the driver board and the specific adapter board connector you are using for that component. You will need this information when you configure your system in Chapter 3.
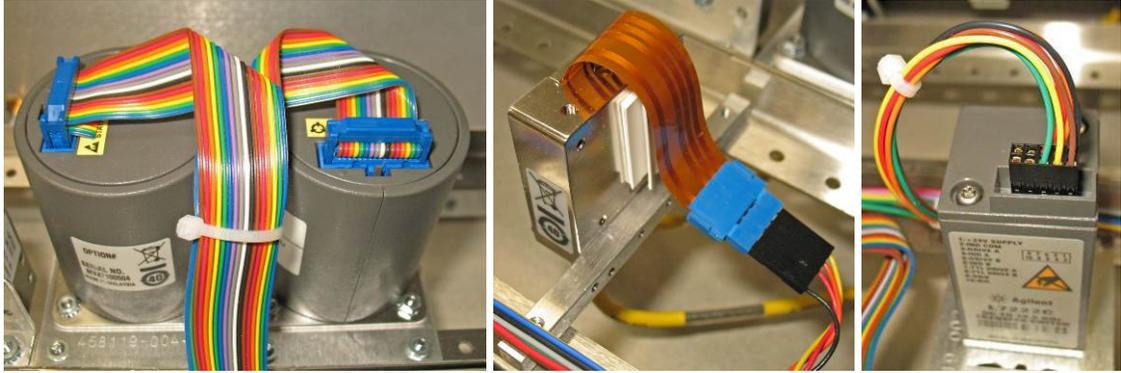
Figure 2-17, Connecting Cables to Components (Examples)
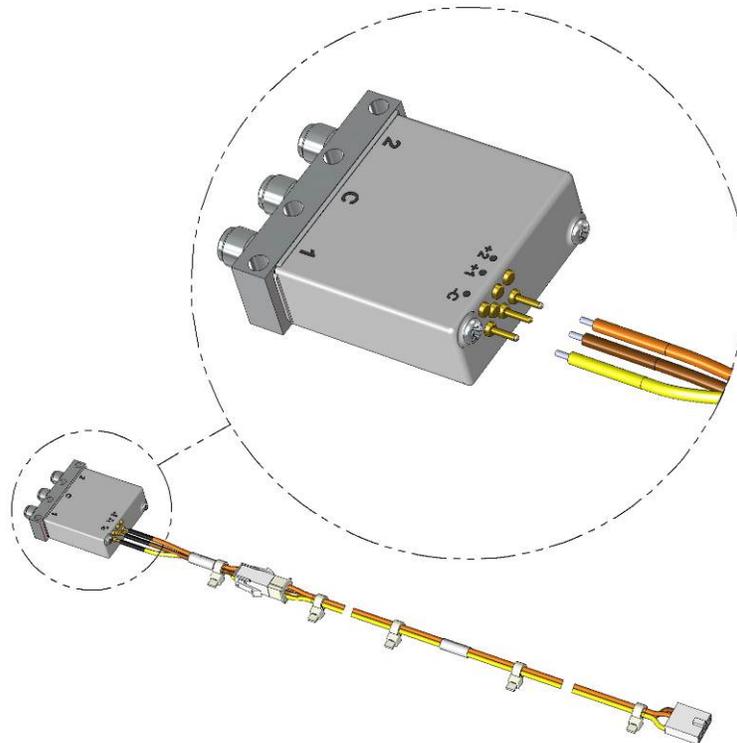
Figure 2-18, Attaching Pigtail Connectors
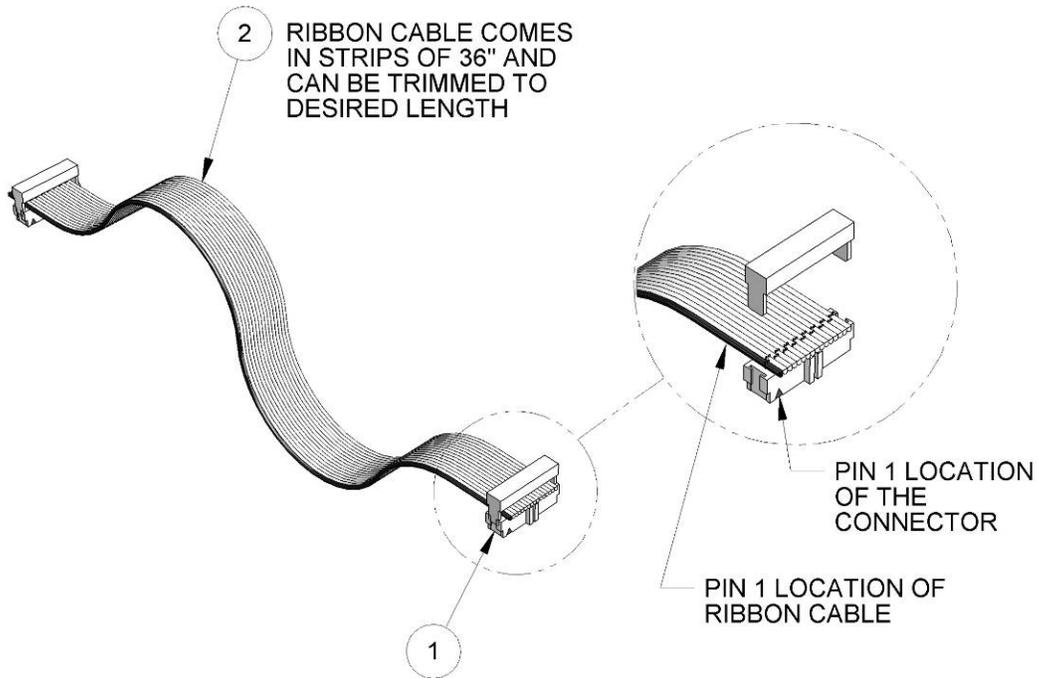
Figure 2-19, Attaching the Ribbon Cable Connector

Table 2-3, Sample Pigtail Color Code

Connector (Pin #)	Signal	Wire Color	Agilent 8762B	Radiall R570433010
1	DR0	Brown	1	-1
2	VCOM	Yellow	C	+C
3	DR1	Orange	2	-2
4	N/C	N/A	N/A	N/A
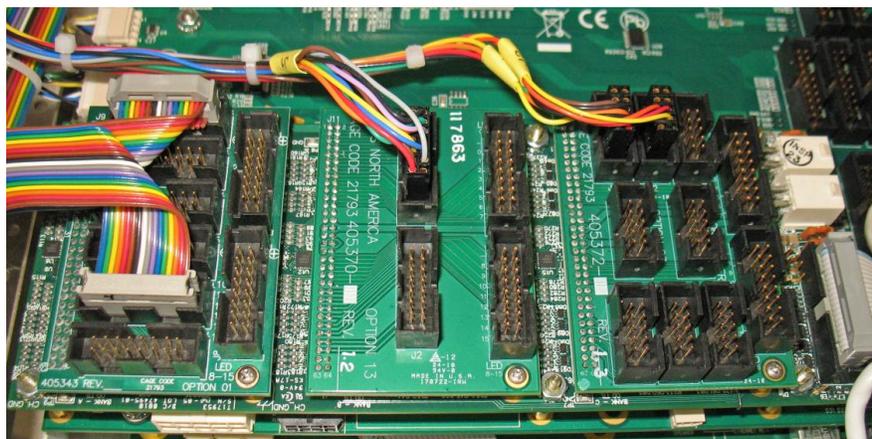
Figure 2-20, Cabling Components to Adapter Boards (Examples)

Cabling Components to Front and Rear Panels

Caution

To avoid possible metal chips or loose pieces remaining in the chassis, remove the front and rear panels from the chassis prior to punching or drilling any holes in the panels.

See **Figure 2-21** and **2-22** for an example of a chassis with a user-customized front panel with installed components cabled to the panel connectors.

Appendix B, Mounting Hardware and Cables, lists the corresponding cables for the components you will be installing in the 1257A-D.

Ensure that all connections are tightened securely before attaching the top and bottom chassis covers.

Figure 2-21,   User-Customized Front Panel (Example)

Figure 2-22, 1257A with Components Cabled to a Customized Front Panel (Example)

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Chapter 3
Getting Started

This chapter includes information on getting started with your 1257A as well as basic configuration of the unit and your computer.

The sections in this chapter include:

- Mounting options

- Main AC power

- Using IVI drivers

- Viewing the VISA resource strings

- Setting the GPIB address

- Communicating using GPIB and USB connections

- Connecting over a LAN connection

- Discovering and configuring on a LAN

- Verifying and modifying the system configuration

- Modifying the component library

Mounting Options

You may use the 1257A RF Interface Unit on a table or mounted in a standard 19-inch equipment rack. See **Appendix F, Rack-Mounting Installation**, for instructions on installing the 1257A into a rack.

Main AC Power

Caution

Use only AC power outlets that have a protective ground. DO NOT USE a two-conductor extension cord or 3-prong to 2-prong adapter that does not provide a protective ground connection.

Caution

Before connecting the unit to a power source, make sure the installed fuse has the capacity marked on the rear panel.

The 1257A is grounded in accordance with EN 61010, protecting the user from possible injury due to shorted circuits.

The 1257A requires a power input of 100-120 VAC or 200-240 VAC, 50 Hz to 60 Hz. The 1257A requires no adjustments, settings, or changes in fuse capacity to accommodate variations of voltage and frequency within these ranges.

The power outlet is wired to the standard color code as shown in **Table 3-1**.

Table 3-1, Power Outlet Wire Code

Function	American	European
Line (Live)	Black	Brown
Neutral	White	Blue
Ground (Earth)	Green	Green/Yellow

After connecting the AC power cable to the 1257A, connect the other end of the cable to your power source.

Changing a Fuse for Main AC Power

Caution

Before replacing fuse, turn power switch off and remove the AC power cord.

To replace a fuse, remove the fuse cover on the rear of the 1257A to access the two fuses. Be sure to replace the fuse with one having the same capacity as that marked on the rear panel. See the Power Supply table in the Specifications section of **Chapter 1, Overview and Features** for the type of fuse required.
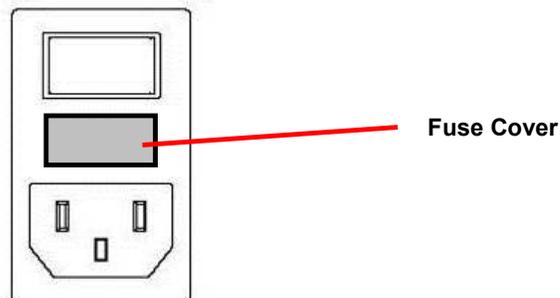
Fuse Cover

Figure 3-1, Fuse Cover (Appearance Typical)

Using IVI Drivers

The IVI Shared Components Version 2.0.0.0 (or later) must be installed before installing the appropriate drivers. You can find the IVI Shared Components on the IVI website at http://www.ivifoundation.org.

If you have questions on working with IVI drivers, you can download the IVI Getting Started Guide, also available on the IVI website.

Note: In order to use the included drivers to control the 1257A system

you need to have a current version of VISA runtime loaded on your computer system. This could include the VISA product from National Instruments, among others.

If you don't have the shared components installed prior to downloading the drivers, you might receive the following warning.
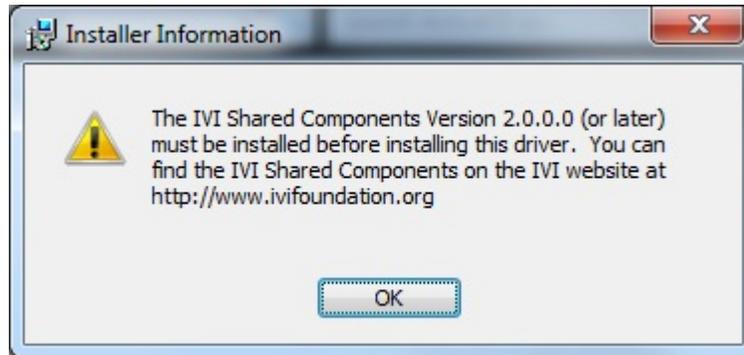
Minimum Requirements for IVI Shared Components

- Windows® 2000: Service Pack 3 (MSI installer)
- Windows 2000: no service packs (EXE installer)

    Note: The essential requirement for Windows 2000 is MSI 2.0, which is available in Windows 2000 SP3 and pre-installed by the EXE version of the IVI Shared Component Installer.

- Windows XP: no service packs
- Windows Vista: no service packs
- Windows 7: no service packs
- Microsoft® .NET Framework: 1.1

Once the IVI shared components are installed, you may install the IVI driver for the 1257A. The product CD contains an installer name "RI1257A.msi". To install the driver, run this installer on your system.

The product CD also contains a folder named "RI1257A LabVIEW driver for IVI Driver". This folder contains a library name "RI1257A.llb" that may be used to provide a programmatic interface to the IVI driver for National Instruments LabVIEW™ version 9 and later. To use this wrapper, copy the files from the folder to the LabVIEW instrument library folder on your system.

When you use the initialization function of the IVI driver, ri1257a_init(), the driver will communicate with the 1257A and determine which components have been installed in your system. The driver automatically creates channel names for each installed switch components based on the installed position (driver board) and the number of inputs and outputs for each component.

Viewing the VISA Resource Strings

To view the VISA resource strings, go to the 1257A home page (**Figure 3-2**). See **Chapter 4, LXI Web-Page Controls**, for more information on accessing the web pages. Note that the screens shown in this chapter include typical information and may differ from your actual view.
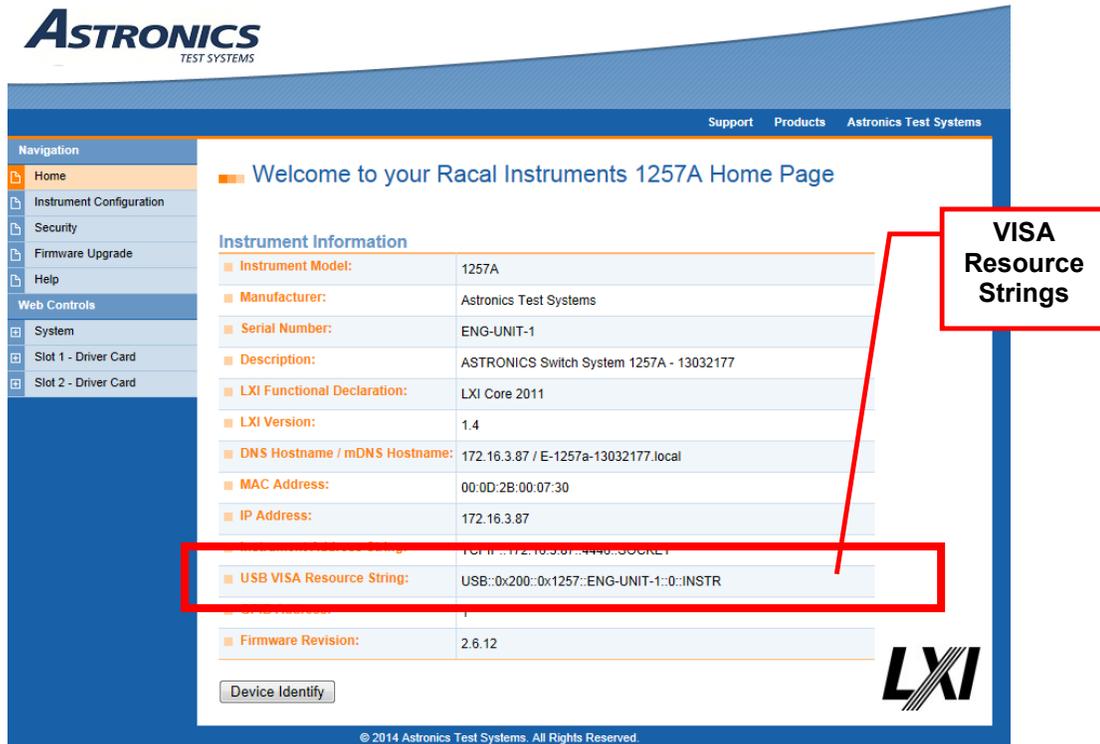
Figure 3-2, VISA Resource Strings (Screen Appearance Typical)

Setting the GPIB Address

The 1257A system does not have a physical hardware switch on the chassis to change the GPIB address. You can change the address by either using the web-page interface (using the LAN) or by sending a SCPI command (using the LAN, GPIB, or USB connection).

The factory default GPIB address setting is 1.

Using the Web-Page Interface to Set the Address

To change the GPIB address using the web-page interface, perform the following.

1. From the Home Page, select **Instrument Configuration**.
2. Click the **Modify** button.

   If asked for a username and password, complete the information. Remember the default username is **admin** and the password box is left blank. If you have changed this, enter the current username/password.

3. Change the address in the box next to GPIB Address.

4. Click **Save** to save the changes.

Using SCPI to Set the Address

To change the GPIB address of the chassis using SCPI commands (with either a LAN, GPIB, or USB connection), perform the following.

1. Send a "`SYSTem:COMMunicate:GPIB:ADDRess<n>`" command where <n> is the GPIB address and a number in the range of 0 to 30.

2. Confirm the change in GPIB address by sending the query: "`SYSTem:COMMunicate:GPIB:ADDRess?`"

Communicating with the 1257A Using GPIB and USB

While the LAN connection is considered the most convenient method to communicate with your 1257A (and we feature the next several sections showing how), you can communicated with the 1257A using the GPIB or the USB connections, as well as the VISA resources.

You first need to identify the appropriate VISA resource string. You can obtain this with the 1257A web-page interface (requiring a LAN connection) or through commonly used test programs, for instance, the National Instruments™ Measurement and Automation Explorer (NI MAX).

Communicating Using GPIB

To use VISA to communicate using GPIB, you need a GPIB VISA resource string. A GPIB VISA resource string has the format of "`GPIB<i>::<n>::INSTR`", where `<i>` is the GPIB interface number on the host computer (typically this is 0 if there's only 1 GPIB controller) and `<n>` is the GPIB address of the device.

Acquiring the GPIB VISA Resource String with the Web Page

This assumes your 1257A system is attached through a LAN connection.

1. Open the 1257A Home page using a web browser.

2. Under the **Instrument Information** section of the page, view and record the GPIB Address.

DNS Server(s):	172.16.0.5, 172.16.0.1, 172.16.0.3
Ping:	Enabled
Ethernet Connection Retention:	Enabled
LAN Keepalive Timeout:	3600
GPIB Address:	1
mDNS Service Name:	1257A Microwave Switch Platform
mDNS Service:	Enabled

3. The VISA resource string can be formed with the discovered GPIB address. Using the example above, the resource string is "`GPIB0::1::INSTR`" (assuming there is only 1 GPIB controller).

Acquiring the GPIB VISA Resource String with NI MAX

1. Connect your system to the 1257A using a GPIB cable.

2. Open up NI MAX.

3. Under **Devices and Interfaces**, click the GPIB interface item.
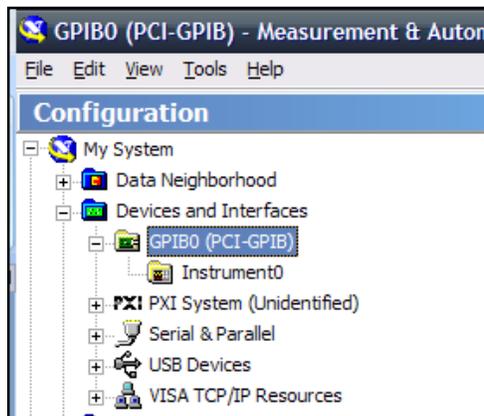
4. Click **Scan For Instruments** on NI MAX.
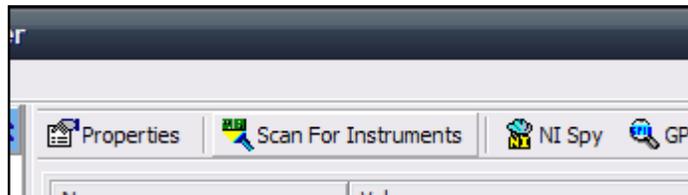
5. After the scan, all of the devices connected to the GPIB controller are displayed with the address of the device shown.
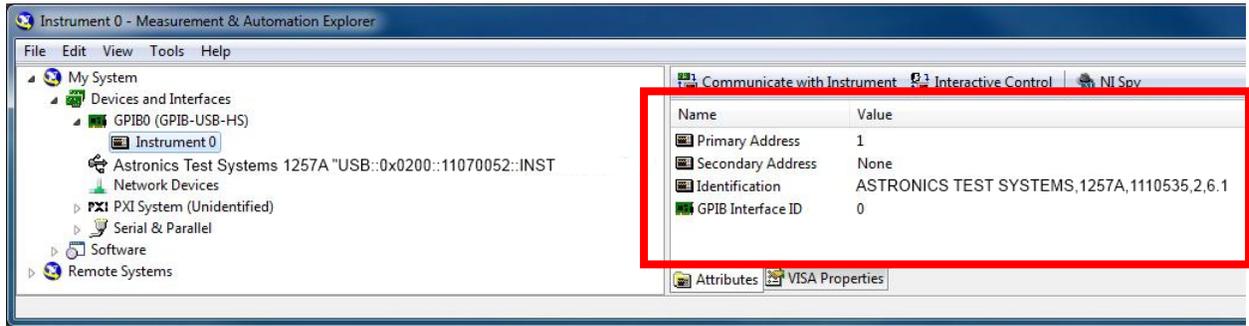
The GPIB VISA resource string for the above device is "`GPIB0::1::INSTR`". Note that if there are multiple GPIB interfaces, then the VISA resource string is "`GPIB<n>::1::INSTR`", where "n" is the number of the GPIB interface.

Communicating Using USB

To use VISA to communicate with the 1257A through USB, you need a USB VISA resource string. A USB VISA resource string has the format of "`USB0::<vendor id>::<model id>::<serial number>::0:INSTR`".

For all 1257A instruments, the vendor id is "`512`" or "`0x200`" and the model id is "`4695`" or "`0x1257`". The remaining information you need to discover is the serial number of the instrument.

Acquiring the USB VISA Resource String with the Web Page

This assumes your 1257A is attached through a LAN connection.

1. Open the 1257A Home page using a web browser.

2. Under the **Instrument Information** section, view and record the USB VISA Resource String.

Acquiring the USB VISA Resource String with NI MAX

1. Connect your system to the 1257A using a USB cable.

2. When the device is plugged in the first time, Windows asks if you want to install the appropriate drivers. Given that VISA is installed on the computer, allow Windows to automatically install the driver.

3. NI MAX recognizes Test and Measurement USB devices and they are listed under **USB Devices** along with the VISA resource string of the instrument.
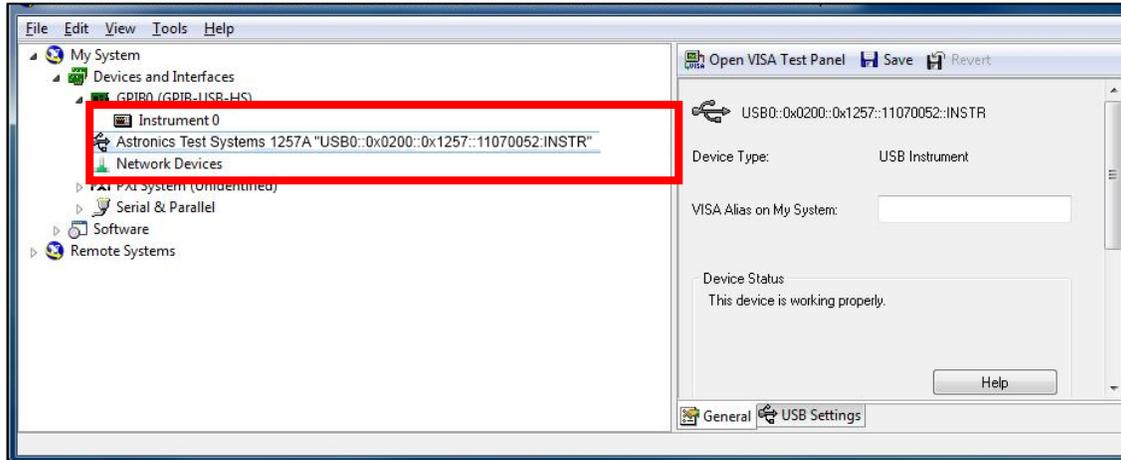
Using NI MAX to Deliver SCPI Commands Over GPIB and USB

1. In NI MAX, open the VISA Test Panel and select **Configuration**>**I/O Setting.**

2. Set the **Timeout** to 2000 to 5000 ms, **I/O Protocol** to Normal, **Termination Character** to Line Feed -\n, and enable the check boxes of **Send End On Writes** and **Enable Termination Character**. Click **Apply Changes** to see the following screen.
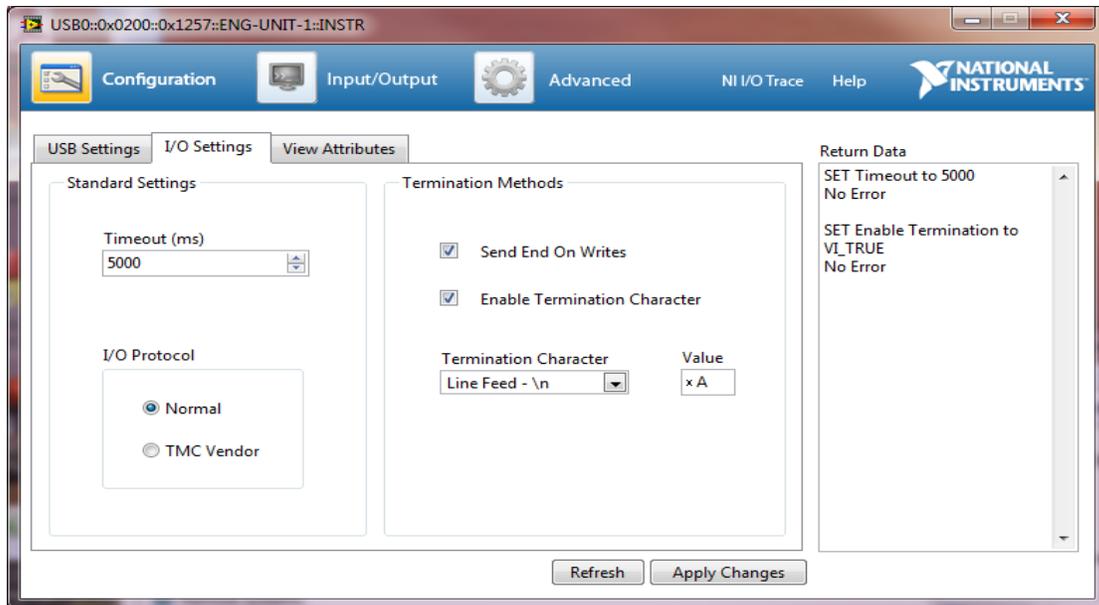
3. Switch to the **Input/Output>Basic I/O** tab and execute the following queries in the **Select or Enter Command** box. (Click **Query** after entering text).

    a. `SYSTem:COMMunicate:LAN:IPADdress?\n`

    This will return the IP address of the instrument.

b. `SYSTem:COMMunicate:GPIB:IPADdress?\n`

This will return the GPIB address of the instrument.
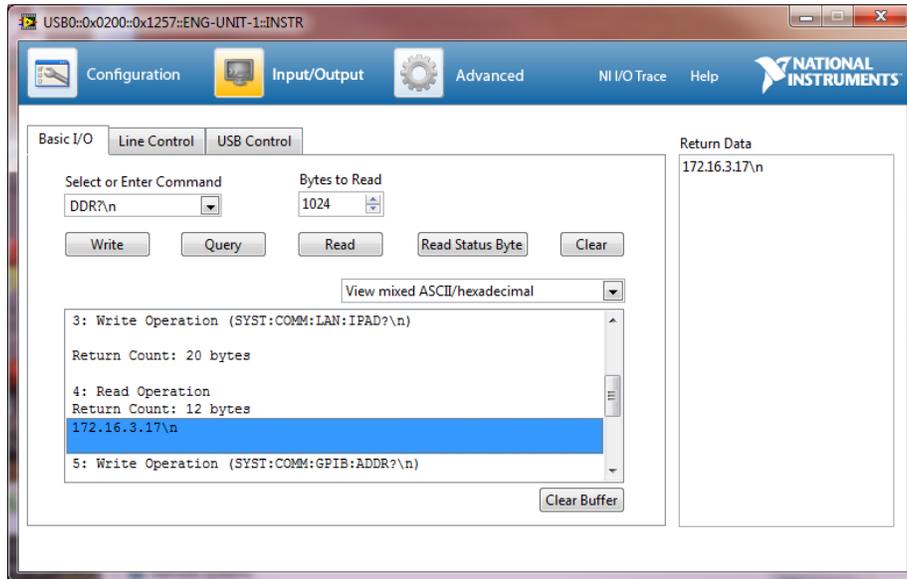
4. You can continue to use this tab to enter SCPI commands for the operation of the 1257A.

Connecting Over a Local Area Network (LAN)

Selecting the LAN Network Type

You can configure your 1257A to connect to a Site or Isolated (non-site) LAN.

- A **Site LAN network** (**Figure 3-3**) is a local area network (LAN) in which computers and LAN-enabled instruments are connected to a site LAN (workgroup LAN, Intranet, or enterprise LAN) via optional routers, hubs, and/or switches.

  Typical Site LAN network view:
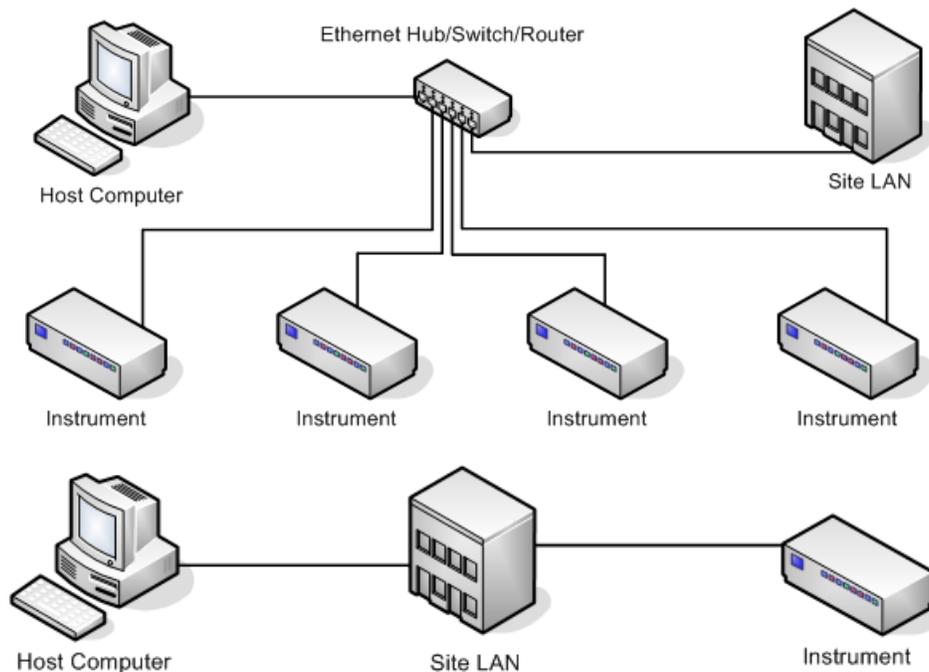
Figure 3-3, Site LAN Network

- An **Isolated LAN network** (**Figure 3-4**) is defined as a local area network (LAN) in which computers and LAN-enabled instruments are not connected to a site LAN.

Typical Isolated LAN network view:
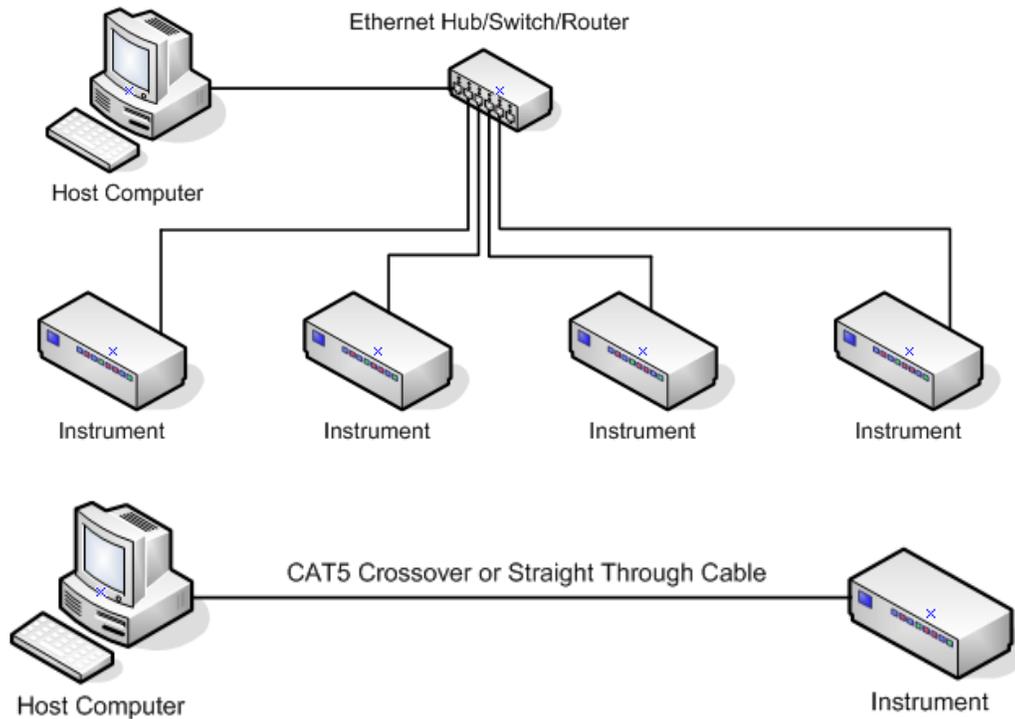
Figure 3-4, Isolated LAN Network

Select the LAN network type the 1257A will be connected to and follow the steps in the appropriate sections: **Configuring the 1257A to Work in a Site LAN** and **Configuring the 1257A to Work in an Isolated LAN**.

Static and Automatic IP Addresses

1. The 1257A can be configured to use either a *static* or an *automatic* IP address. By default, the *automatic* IP address scheme is used.

2. The automatic IP address scheme requires that a host computer on the network provide a Dynamic Host Configuration Protocol (DHCP) service to allocate a unique IP address to the 1257A when it is powered on.

3. If the 1257A is configured for automatic IP addressing and no DHCP server is located on the network, the 1257A will default to use the IP address 169.254.54.201.

4. The static IP address scheme assigns a fixed IP address to the 1257A. This IP address is used whenever the 1257A is powered on regardless of what other computers and devices may reside on the network. With the static IP address scheme, the same IP address could be assigned to more than one device on the network, and the 1257A would not be able to communicate over the network.

5. If the 1257A is configured for a static IP address, you can reset this to use the automatic IP address scheme by pressing the LAN RESET button and holding

it for 5 seconds. After you release the LAN RESET button, the 1257A will be configured for automatic addressing and will attempt to retrieve an IP address from the DHCP server on the network.

Configuring the 1257A to Work in a Site LAN

1. Using either a crossover or straight through CAT5 cable, connect the 1257A to the Site LAN or a switch/hub/router that is connected to the Site LAN. Or directly connect the 1257A to your computer.

2. Make sure power is applied to your computer and the 1257A.

3. The 1257A by default is configured to use "automatic" mode to obtain an IP address to use. In this mode the 1257A will get its IP address from the DHCP server that is on the Site LAN, provided the DHCP server is configured properly.

4. Use a LXI discovery browser to scan the network for LXI instruments. If the 1257A was not discovered during LXI discovery, activate the LAN Configuration Initialization (LCI) by pressing the LAN reset button on the back panel of the 1257A for at least **10 seconds**. Use the LXI discovery browser to rescan the network.

5. Once the 1257A has been discovered, enter the IP address of the 1257A in the address bar of your web browser to bring up the 1257A's welcome page.

Configuring the 1257A to Work in an Isolated LAN

1. Using either a crossover or straight-through CAT5 cable, connect the 1257A to the switch/hub/router that is connected to your computer. Or directly connect the 1257A to your computer.

2. Make sure power is applied to your computer and the 1257A.

3. If a router is used to connect your computer and the 1257A and DHCP is enabled on the router, go to **Configuring the 1257A to Work in a Site LAN** to complete the rest of the procedure.

   The 1257A by default is configured to use "automatic" mode to obtain an IP address to use. Since no DHCP server is connected to your Isolated LAN network, the 1257A uses link-local addressing to obtain its IP address. If GPIB or USB communication to the 1257A is available, you may assign a static IP address to the 1257A manually, or query the 1257A for its LAN settings directly (go to **Using GPIB or USB to Set 1257A LAN Settings** or **Using GPIB or USB to Query 1257A LAN Settings**). If GPIB and USB communication are not available, proceed with the following steps.

4. Use a LXI discovery tool to scan the network for all LXI instruments. If the 1257A was discovered, enter the IP address of the 1257A in the address bar of your web browser to bring up the 1257A's welcome page. If 1257A was not discovered, proceed with the following steps.

5. Open **Control Panel** and double click on **Network Connections**.

6. In the **Network Connections** window, right click on **Local Area Network Connection** and then click **Properties**.
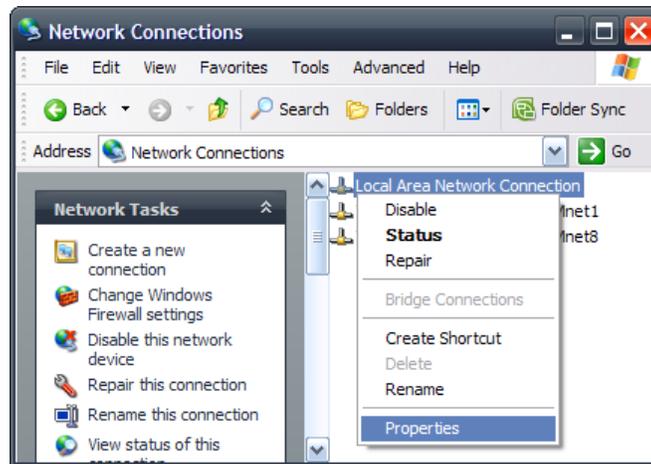
7. In the **Properties** window, click **Internet Protocol (TCP/IP)** and then click **Properties**.
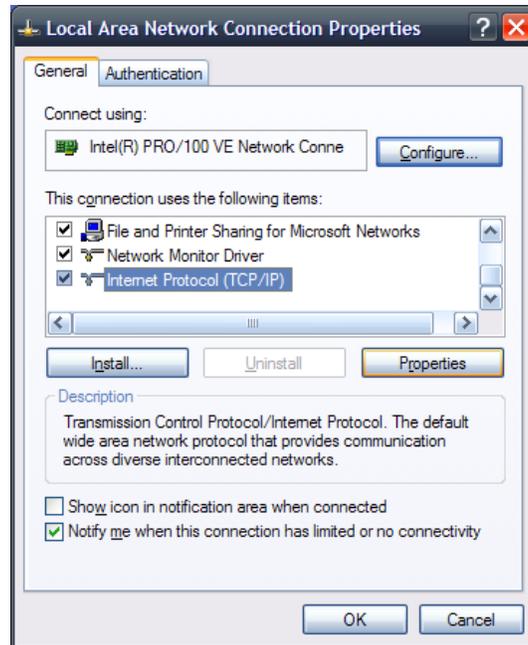
8. In the Internet Protocol (TCP/IP) Properties window, select **Use the following IP address** and input "169.254.1.0" for the IP address and "255.255.0.0" for the Subnet mask. Leave all other fields blank. Then click **OK**.
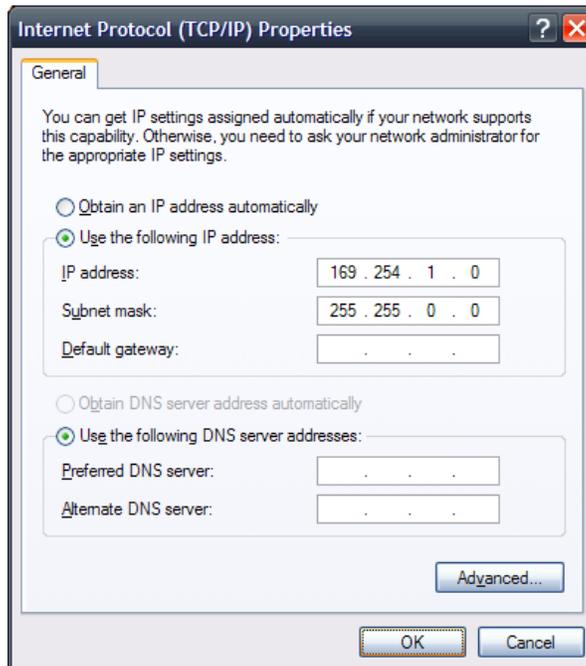
9. Click **OK** to close the **Local Area Network Connection Properties** window.

10. Use a LXI discovery tool to scan the network for LXI instruments, the 1257A should be listed as a discovered instrument. Enter the IP address of the 1257A in the address bar of your web browser to bring up the 1257A's welcome page.

Using GPIB or USB to Set 1257A LAN Settings

To assign an IP address to the 1257A manually, send the following SCPI commands via USB or GPIB:

1. `SYSTem:COMMunicate:LAN:MODe MANUAL`
2. `SYSTem:COMMunicate:LAN:MAN:IPADdress <Instrument's IP address>`
3. `SYSTem:COMMunicate:LAN:MAN:SMASk <Subnet mask>`
4. `SYSTem:COMMunicate:LAN:MAN:DNS NONE`
5. `SYSTem:COMMunicate:LAN:RESET`

For example, if your computer is using IP address "169.254.0.1" and the subnet mask is "255.255.0.0", then the Instrument's IP address will be in the range of 169.254.<0-255>.<0,2-255> and the subnet mask would be "255.255.0.0" as well.

Using GPIB or USB to Query 1257A LAN Settings

If you choose not to assign an IP address to the 1257A manually, you may query the 1257A for its LAN settings. Since the 1257A's factory default setting for LAN mode is "automatic", the 1257A will use link-local auto configuration if no DHCP server is connected to the LAN (which is the case in an isolated LAN). To query the 1257A for its' LAN settings, issue the following SCPI queries:

1. `SYSTem:COMMunicate:LAN:IPAD?`
2. `SYSTem:COMMunicate:LAN:SMASk?`

The 1257A's IP address is going to be in the range of 169.254.<0-255>.<0-255> with the subnet mask being "255.255.0.0.". Once you've obtained the instrument's IP address and subnet mask, may try to load the 1257A's welcome page by entering its IP address in your web browser's address bar. If you're unable to load the welcome page, perform Steps 5 to 10 of the previous section **Configuring the 1257A to Work in an Isolated LAN**.

Discovering and Configuring Your 1257A on a LAN

The LXI Discovery Browser software (P/N 922670) is located on the included documentation disk or you can download it from our website. You may also locate a 1257A on your LAN with discovery software tools from other vendors. The following information shows how to install and use the LXI Discovery Browser to locate your 1257A on a LAN.

If you are trying to connect to a network without a DHCP server, refer to **Discovering and Configuring without a DHCP Server**, later in this chapter.

Installing the LXI Discovery Browser

1. Locate and double-click *setup.exe* included for the LXI Discovery Browser software on the included documentation CD to begin the installation of the browser.

2. When you see the following screen, select **Next** to continue.
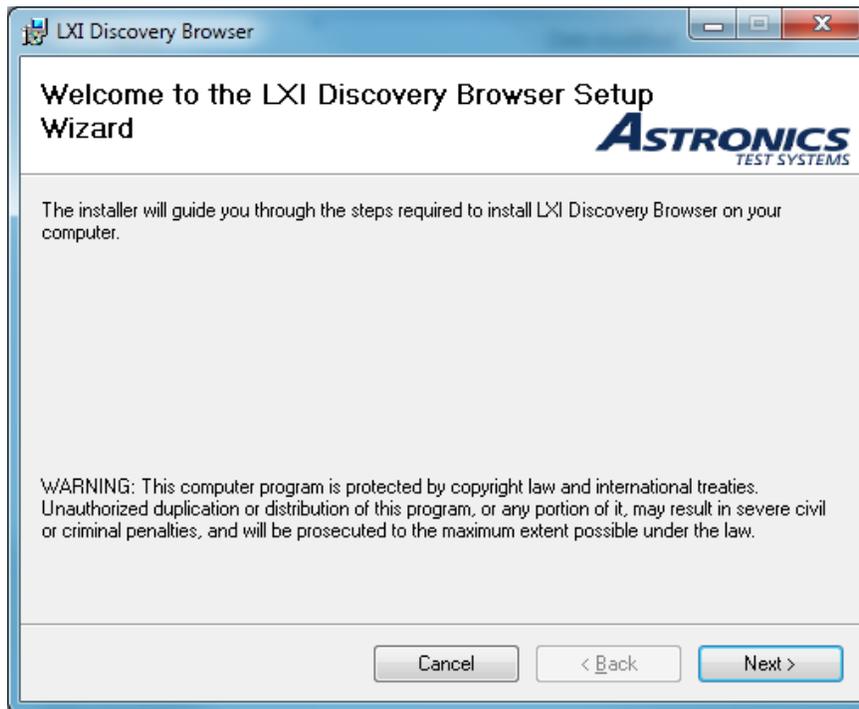
Note

The following browser setup screens are appearance typical. There may be changes to the screen and version numbers should the software be updated.

---

3. On the next screen, review the terms of the License Agreement and click the **I Agree** button. Click **Next** when done.

   Should you not accept the terms, click **Cancel** to halt the installation of the

software.
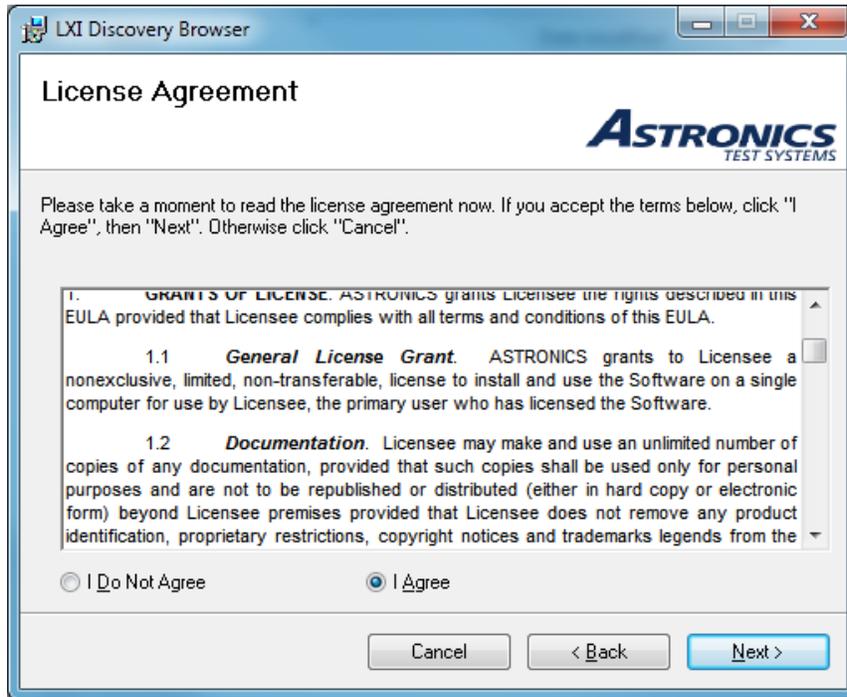
4. Confirm or change the destination folder to install the files. Click **Next**.
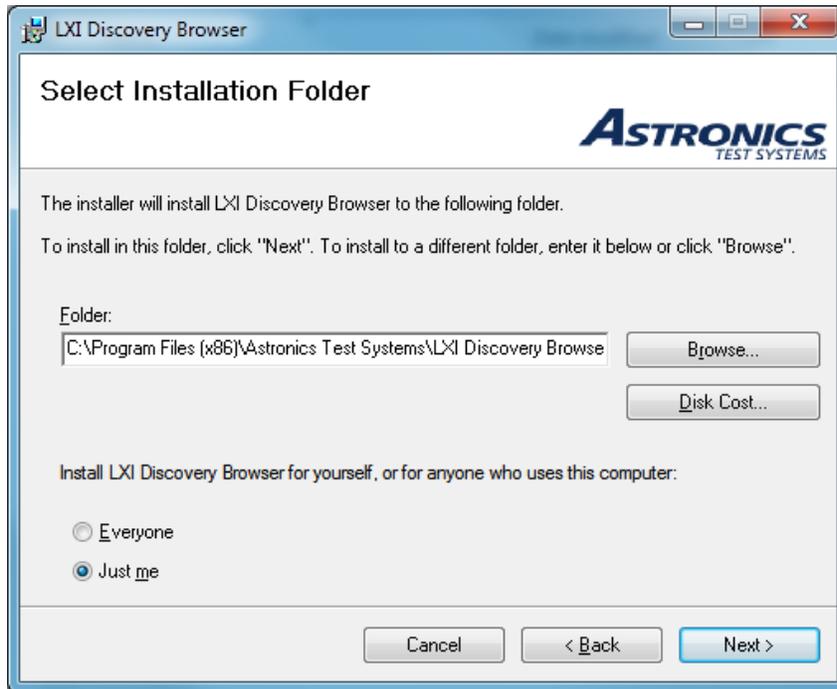
5. Click **Next** to install the browser.
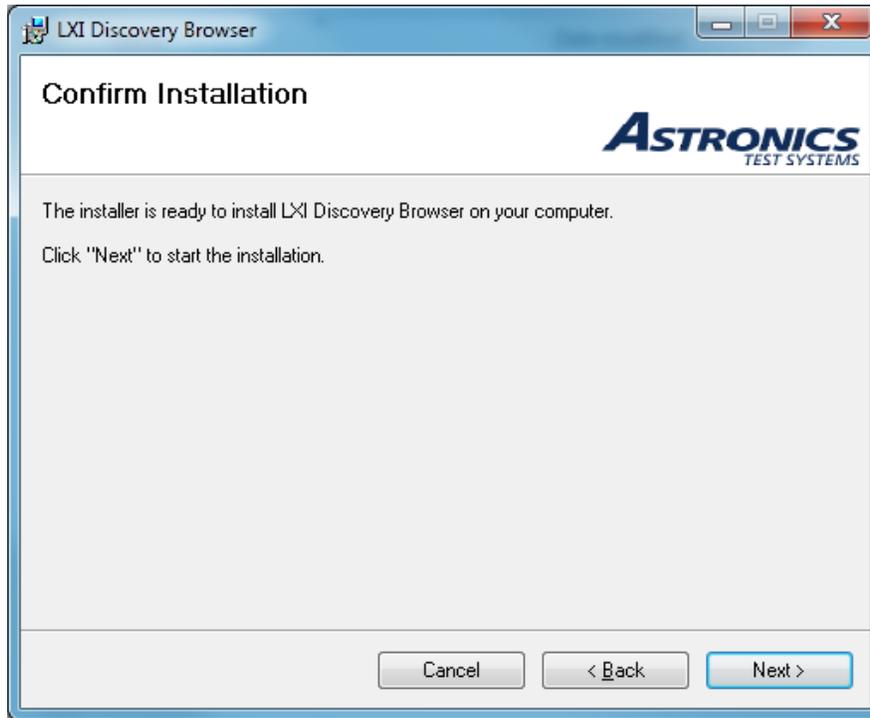
6. After a few minutes, if the setup was successful, a screen appears showing
    Installation Complete. Click **Close.** A LXI Discovery Browser icon will
    appear on your desktop screen.
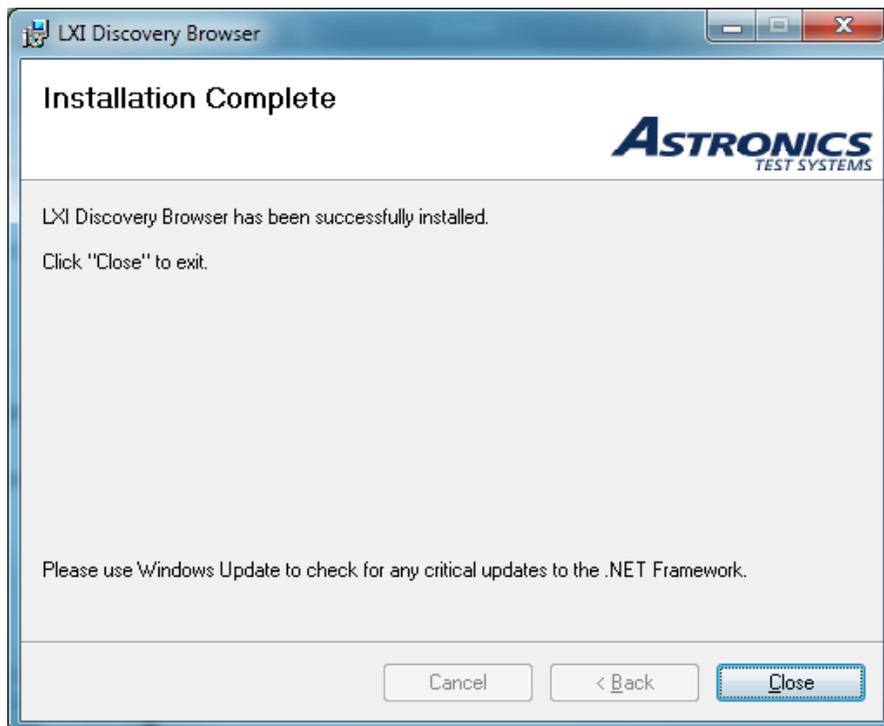
Running the LXI Discovery Browser

1. Double-click the LXI Discovery Browser icon on your desktop.

2. Click the **Find LXI Instruments** bar.
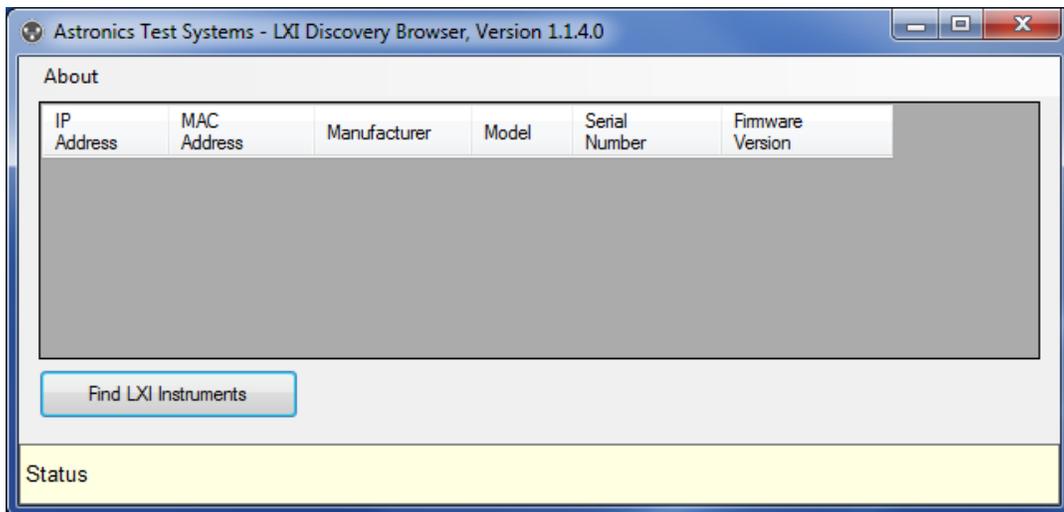
3. The browser will display all of the LXI devices it finds on your network. Select and double-click the desired instrument to open up its web page.

IP Address	MAC Address	Manufacturer	Model	Serial Number	Firmware Version
172.16.3.1	00:0D:2B:00:07:7C	Astronics Test Systems	1257A	ENG-UNIT-2	2.6.14.3
172.16.2.6	00:0D:2B:00:08:38	Astronics Test Systems	1256L	14022054	1.6.7
172.16.3.38	00:0D:2B:00:07:57	Astronics Test Systems	1257A	12100809	2.6.14.4
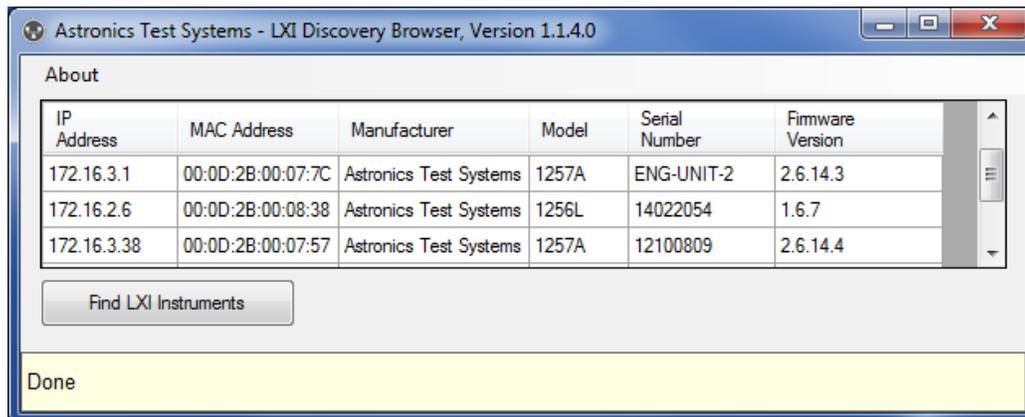
Java Security Warning

Full-feature functionality of the web page interface relies on the Java platform. This interface may cause Java security messages (similar to what is shown in **Figure 3-5**), to appear.
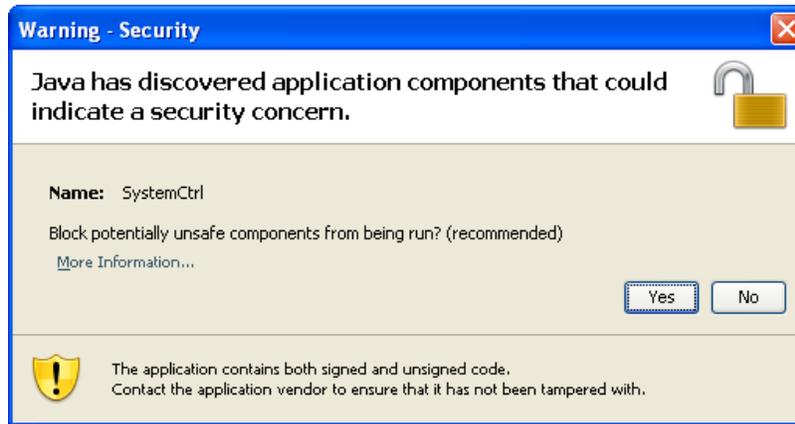
Figure 3-5, Java Security Warning Message (Appearance Typical)

You must click **No** to allow the **SystemCtrl** component to run or you can use the following procedure to disable future Java warning messages.

1. From your Windows **Start** Menu, select **Control Panel**.

2. In Microsoft Windows 7, select **Programs**. (If you have another version of Windows, the path to the Java Control Panel may be slightly different.)

3. Double-click the **Java** icon.
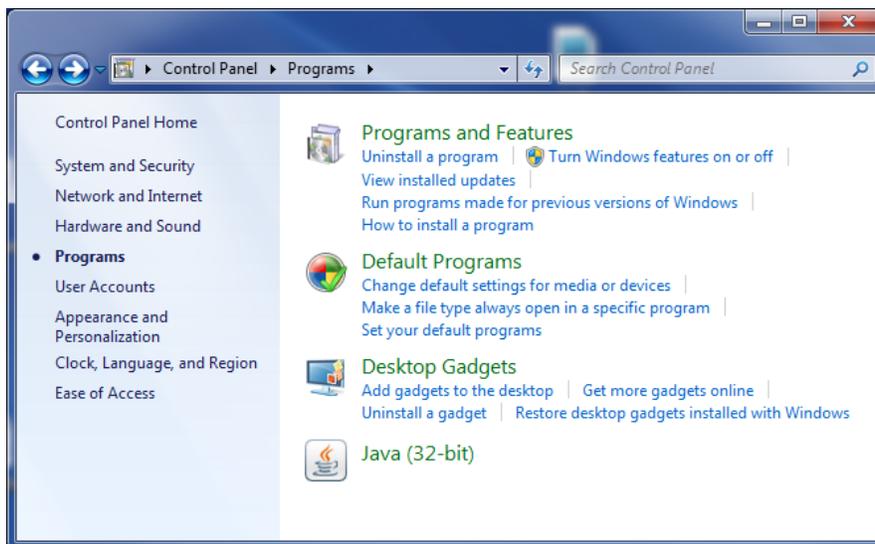
4. Select the **Security** tab.
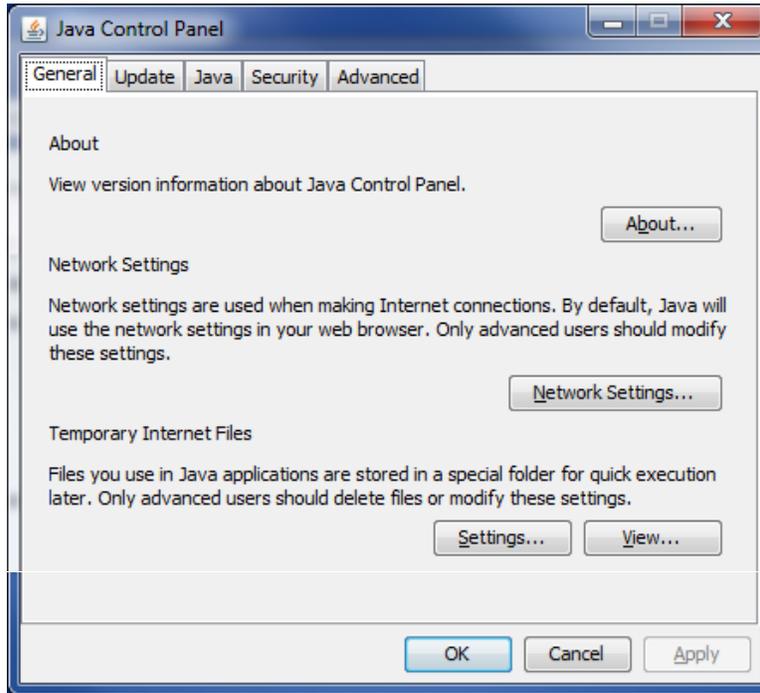
5. Set the **Security Level** to **Medium** and then select the **Advanced** tab.
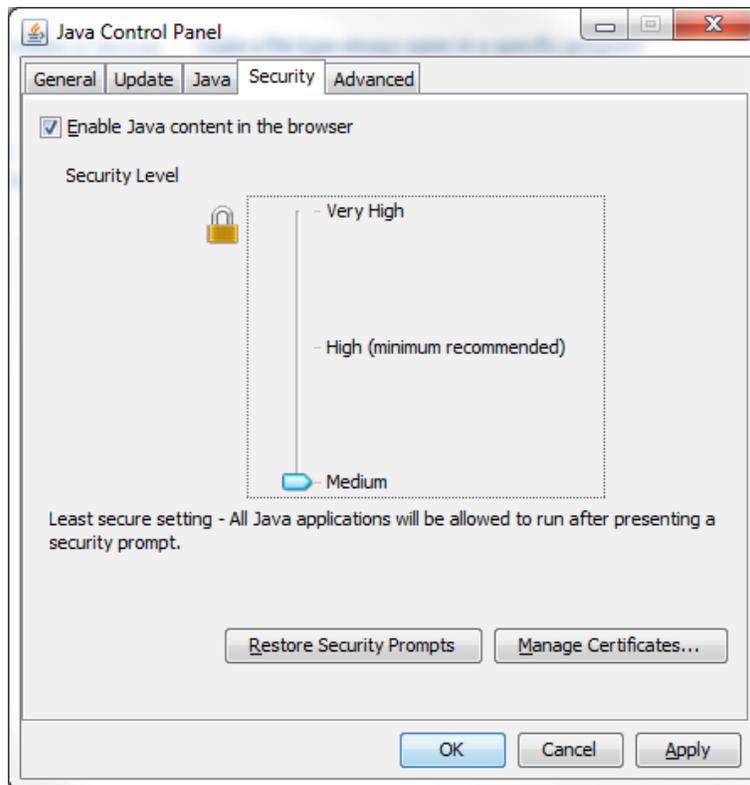
6. Scroll down to **Security** and expand **Mixed code**.
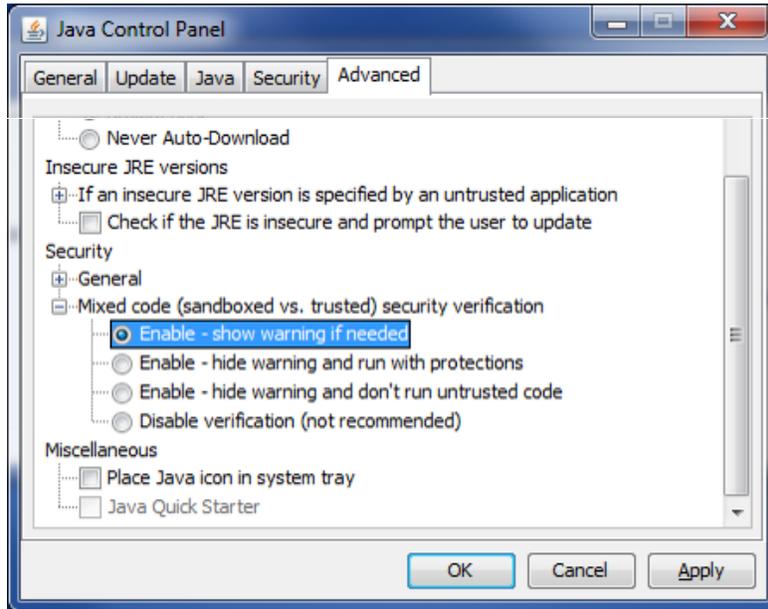
7. Click **Enable – hide warning and run with protections**.

8. Click **Apply** and then **OK** to implement the changes.

9. Close out of **Control Panel** and return to your browser.

Configuring the System to Recognize the Components

After discovering your 1257A-D in the previous section, you can now configure the system with your installed components. (Note that if the component is not already included in the system's "library" (shown in Step 7), refer to "Adding New Components to the Library" to add them.)

In **Appendix D, Component Configuration Example,** we've provided an example with step-by-step instructions to configure several specific components.

1. After you have selected your 1257A instrument, the Home Page (shown below) appears. (Note that the screens shown in this chapter include typical information and may differ from your actual view.)

2. Click **Slot 1 – Driver Card** under Web Controls to verify the installed driver boards.

    a. If a Windows Security and/or Java sign-on screen appears, use the factory default of "**admin**" as user name and no password (unless you have changed the user name and password) and click **OK**.

    b. The following screens may appear. Choose the action as noted:
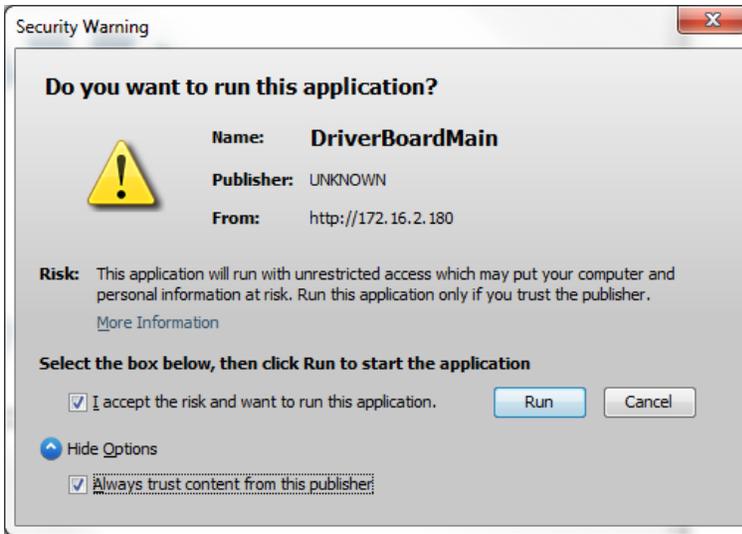
- Click **Show Options** on the lower part of the screen, click the checkboxes of "**I Accept**…" and "**Always trust**…", and then **Run**.

- Click **Run with the latest version**.
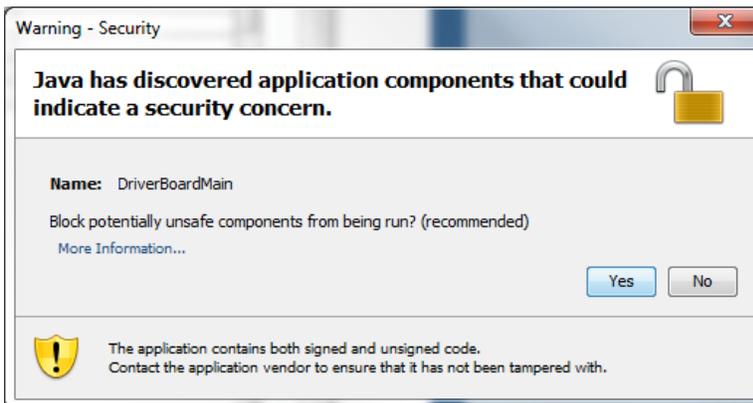
- Click **No**.

  *Important Note: If you click **Yes** in error, you will **not** be able to configure your system without going into your browser and deleting/reseting your saved security and browser settings.*

3. The **Main** tab appears. If the message displays similar to the illustration below, the driver board is not yet configured. (Alternately, you may see some components that are already configured.)
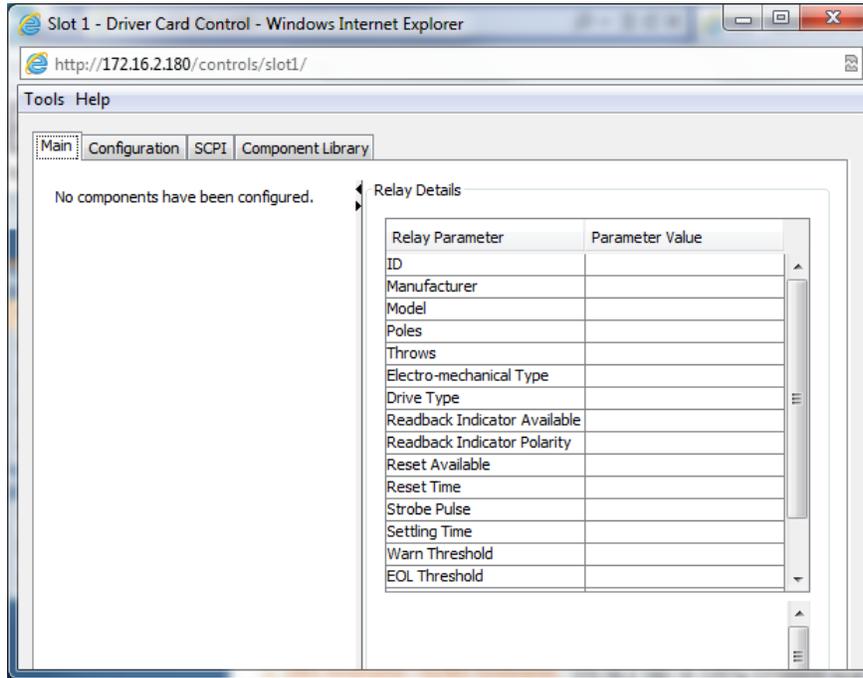
4. Select the **Configuration** tab. This will show you the **Bank Selection** menu and the available Adapter Boards.
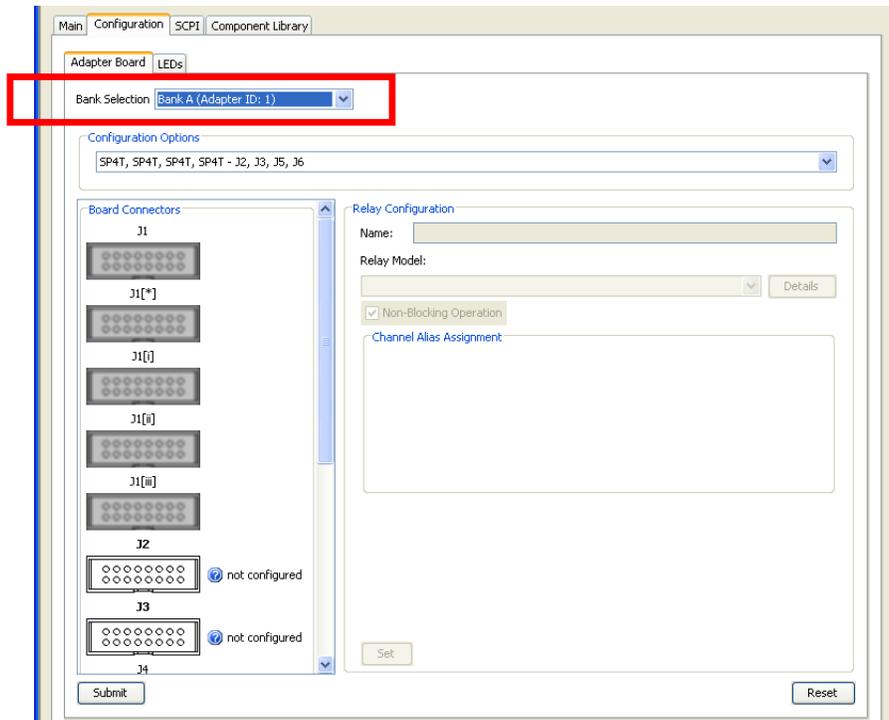
5. From the **Bank Selection** drop-down menu, select the Bank (or driver board position) for the adapter board that you wish to configure.
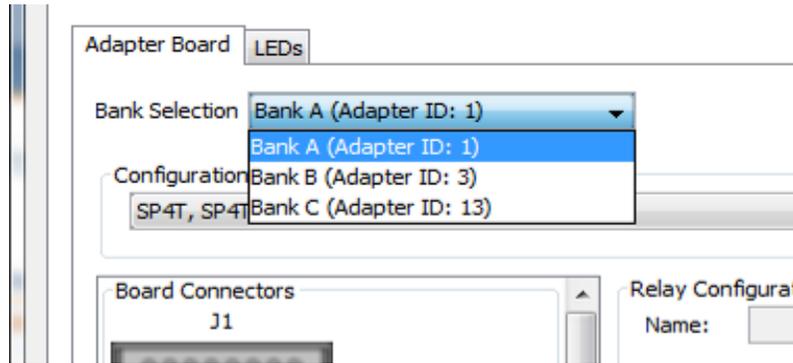
6. From the **Configuration Options** drop-down menu, choose the option that matches the type of component you wish to add.
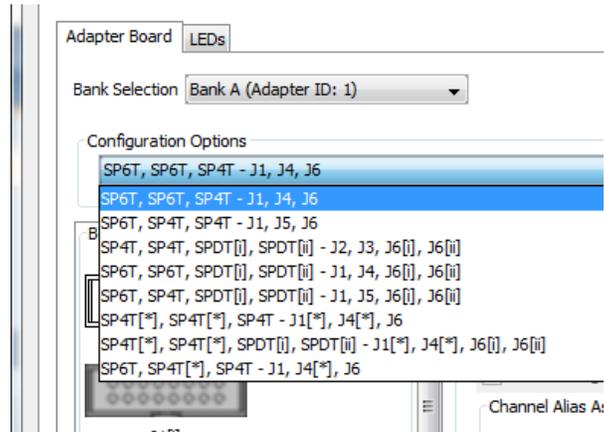
7. Click the **Board Connector** you wish to configure. The connector will turn black when selected.

   If the connector is already grayed out, it is not available for your use.
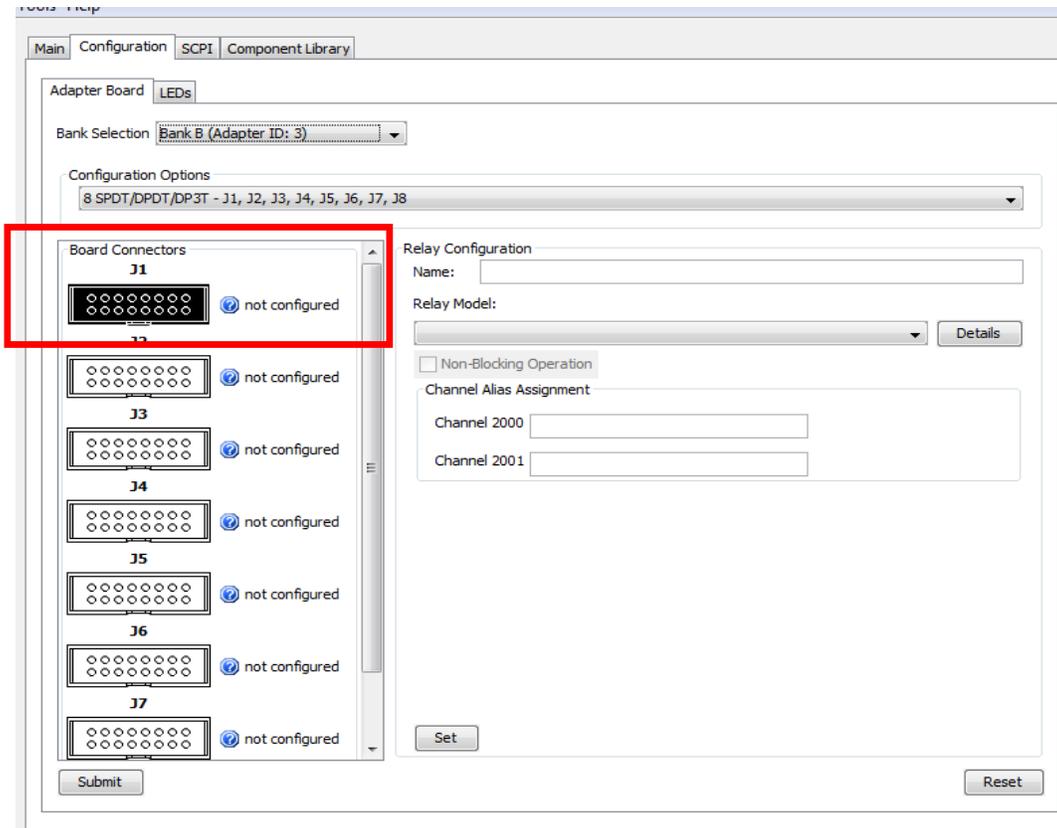
8. Provide an identifying **Name** for the component (i.e. SW1) and then choose the component to be configured by using the **Relay Model** drop-down menu for available components in the library.

   Note: If your component is not in the list, you may need to add it to the Component Library. See "Adding New Components to the Library" for details.
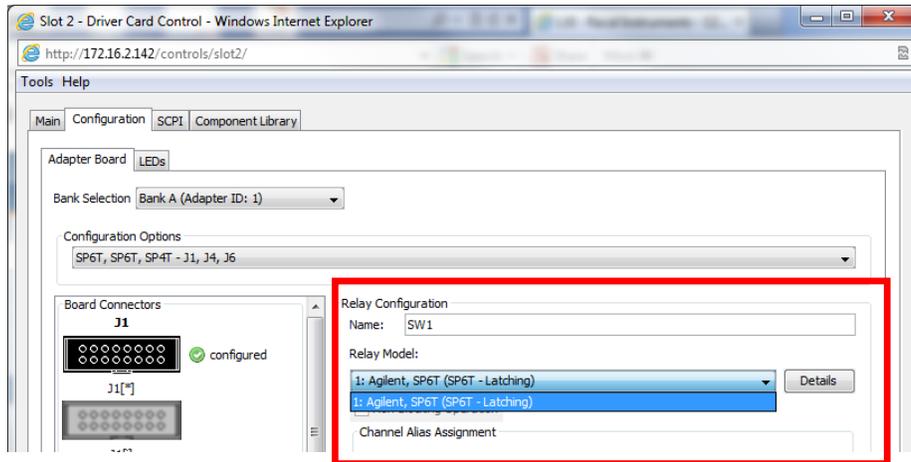
9. If desired, add a Channel Alias Assignment name/number.
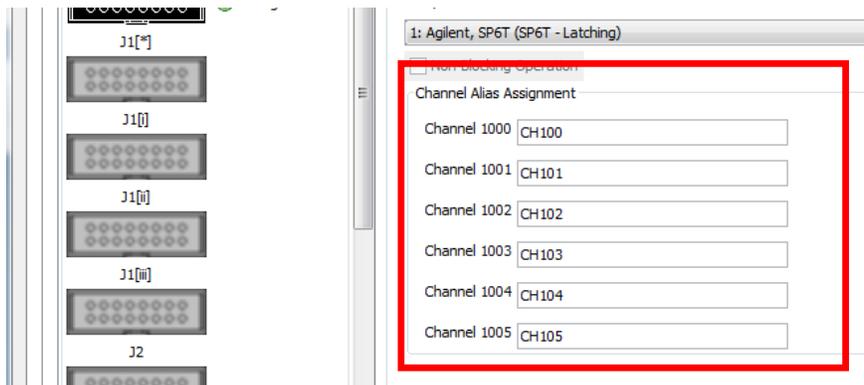
10. Click **Set** to save these details.

11. Repeat Steps **5** through **10** above to match any additional connectors with components.

12. When all configured, click **Submit** to send the configuration to the 1257A unit.

13. If a Confirmation screen appears, click **Yes** to confirm and continue.

14. When you switch back to the **Main** tab, your updated components and configuration changes will be visible.
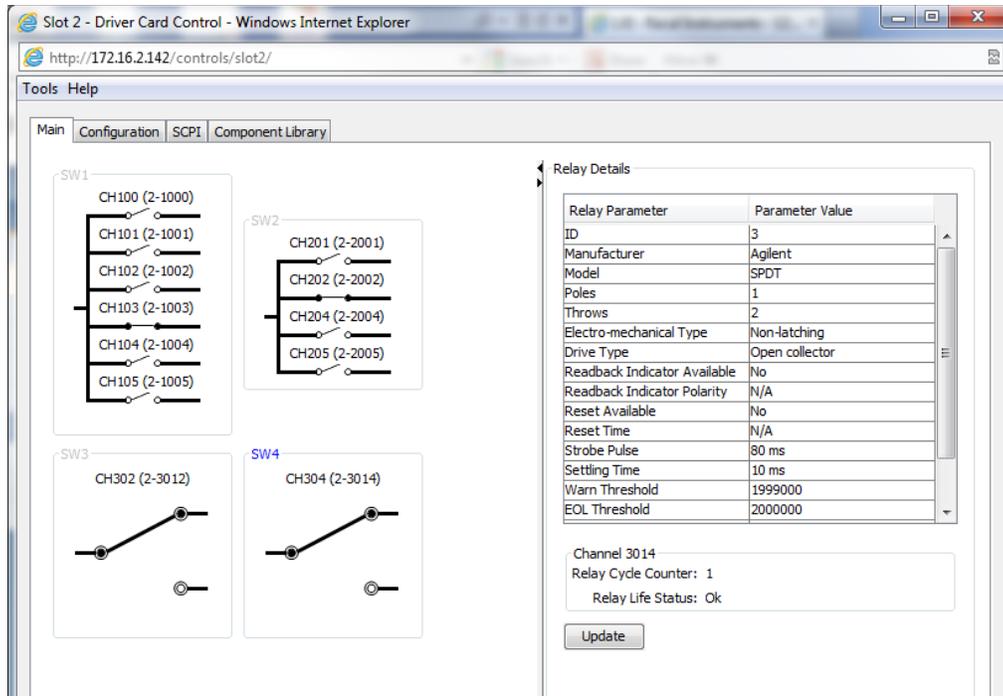
Adding New Components to the Library

To ensure that an appropriate component is populated in the **Relay Model** menu based on a selected **Configuration Options**, during configuration, it must first be added to the **Component Library.**

The current supported component types are Relays, Attenuators, or Digital Input/Outputs (DIO). Parameter information is available from the manufacturer of the component.

Important Note: Entering incorrect parameters may prevent the component from operating correctly.

The following are the Relay parameters

- ID
- Manufacturer
- Model
- Poles – Number of Poles in the Switch
- Throws – Number of Throws in the Switch
- Electro-mechanical Type
- Drive Type – [TTL or Open Collector]
- Readback Indicator Available
- Readback Indicator Polarity
- Reset Available

- Reset Time
- Strobe Pulse – Pulse width for latching relays
- Settling Time – System delay after switch actuation
- Warn Threshold – Relay Counter threshold where warning occurs
- EOL Threshold – Relay Counter threshold where EOL warning occurs
- Transfer Switch – Whether switch is a standard switch or DPDT Transfer Switch
- Miniature – Whether switch is a standard switch or miniature multiconfigured switch (2 x SPST, DPDT, 4PST, or 6PST)

The following are the Attenuator parameters

- ID
- Manufacturer
- Model
- Attenuator Type
- Drive Type
- Switching Time
- Global Reset
- Readback Ability
- Readback Available
- Readback Polarity
- Steps – Select the number of Cells
- Cell # Attenuator – Set the attenuation for the Cell

The following are the DIO parameters

- 2-8 Bit Out, 2-8 Bit In, 4-Bit Reset

Use the **Component Library** tab to Add/Modify/Delete components from the library. Click the **Add** button (**Figure 3-6**) to add a new component. **Figure 3-7** shows the displayed **Edit Relay Parameter** screen (**Figure 3-7**) to enter the data.

Click **Submit** to enter the data into the Library. Once data is submitted, the **Edit Relay Parameter** screen still displays. Click **X** or **Cancel** to close.
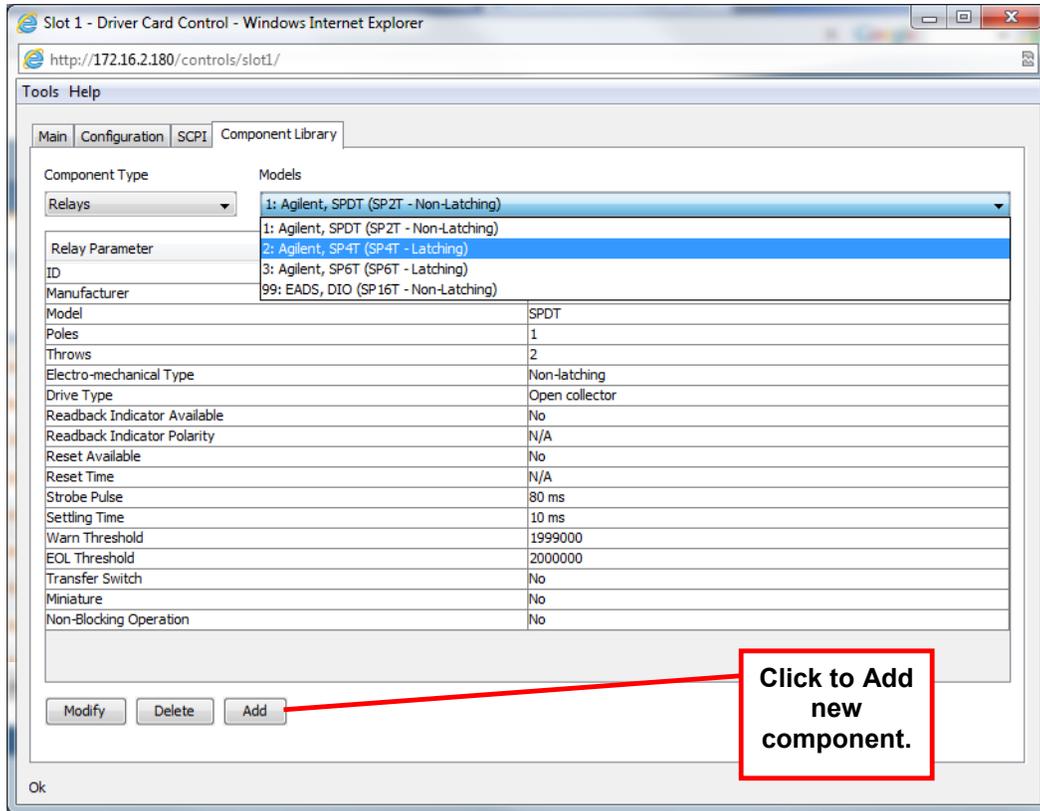
Figure 3-6, Component Library Tab
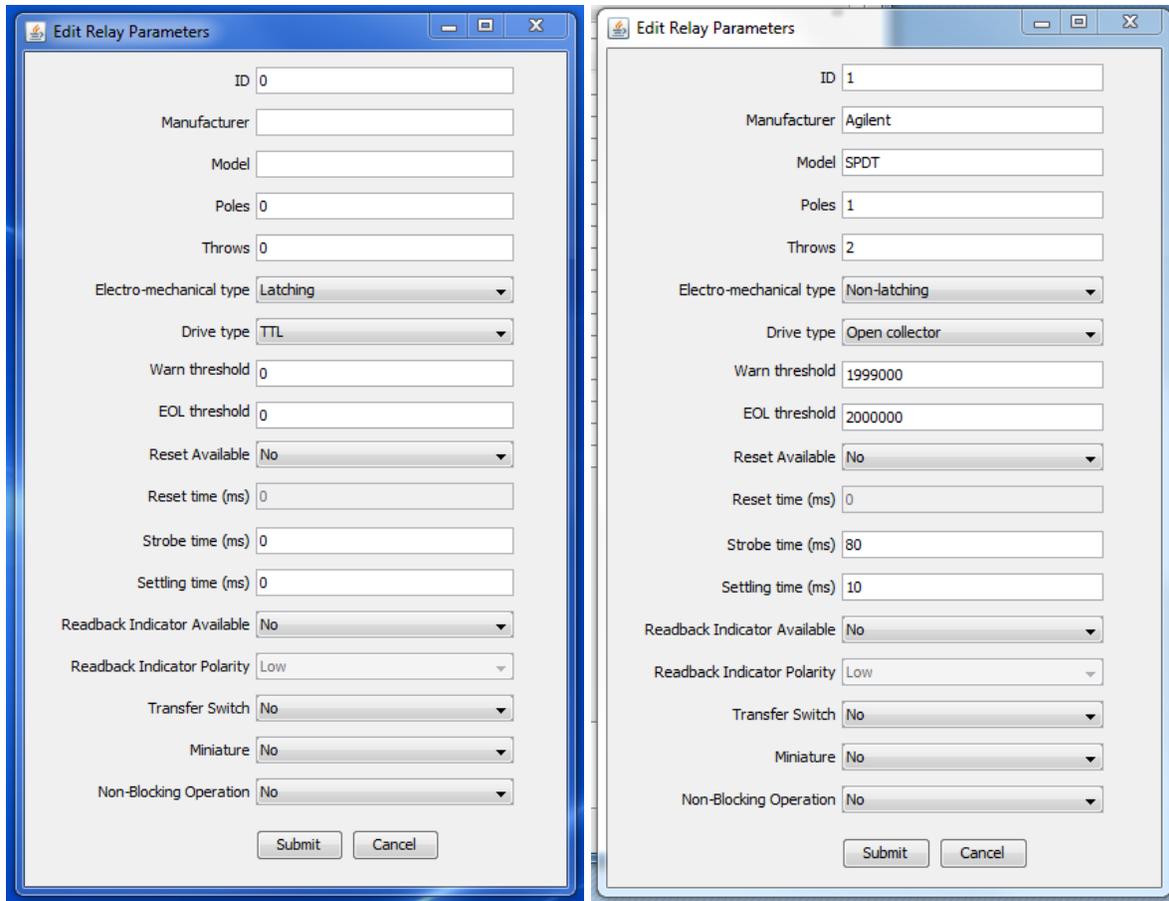
Figure 3-7, Edit Parameter Window (Before and After Data Entry)

Verifying/Modifying System Configuration

The 1257A can have up to 10 slots configured, each which is associated with a Driver Board. Each Driver Board can have up to three Banks, which are labeled A, B, and C. (See **Figure 2-7** for Bank locations.)

Selecting the **Configuration**>**Adapter Board** window, and selecting the Bank Selection drop-down shows which Adapter Boards are already configured for the driver board. **Table 3-2** summarizes the different Adapter board types available.

Table 3-2, Available Adapter Board Types

ID	Type	Connector
0	16 Channel Configurable I/O	Terminal Blocks
1	SP4T/SP6T	16-Pin Header
2	SPST/SPDT/DPDT/DP3T	10-Pin Header
3	SPST/SPDT/DPDT/DP3T Coil read-back	10-Pin Header

ID	Type	Connector
4	SP8T	26-Pin Header
5	SP10T	26-Pin Header
6	Dowkey Miniature 6L3 Dual SPDT 613 DPDT 634 SP4T 636 SP6T Open-Drain Indicator tied to Driver	10-Pin Header
13	Agilent 8490x Attenuators	16-Pin Header
15	None Installed	N/A

Selecting a bank on the **Configuration** tab enables you to see and change the bank configuration and board connector options.

Downloading or Uploading Configuration Information

The 1257A allows you to download (or copy) your current switch configuration and then upload the information to the same or another 1257A. This can be helpful to store a copy of the configuration, upload the configuration to another system, or to modify the configuration and then upload it to a 1257A. Use the Configurator Software Tool (PN 922725) which is included on the documentation CD delivered with your system.

For additional information on installing and using the Configurator Tool, refer to **Appendix E, Using the Configurator Software Tool**.

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Chapter 4
LXI Web-Page Controls

The LXI Web-Page Controls allow you to access and operate the main features of the 1257A through your LAN without installing additional drivers or software on a control computer. (Note that the screens shown in this chapter include typical information and may differ from your actual view.)

Initial Home Page

To access the web-page interface on your 1257A:

1. Ensure that an active LAN cable is attached to the 1257A

2. Ensure that the 1257A power switch is turned on.

3. If using the LXI Discovery Browser (see **Chapter 3**, **Getting Started**), double-click the instruments listing to launch the 1257A home page

    If using another method, type in the IP address into your computer's internet browser.

The 1257A instrument's built-in Home page appears as shown in **Figure 4-1**.
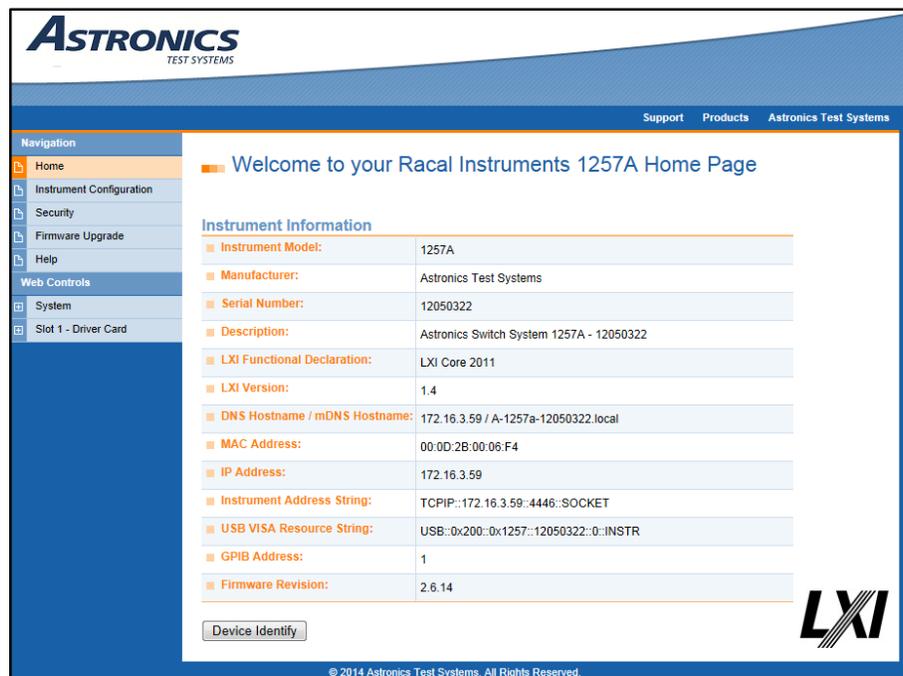
Figure 4-1, Web-Page Interface Home Page

Setting the Password

When you first access any of the web pages that allow you to modify settings, you are asked for a username and password. The default username is **admin**. Initially, there is <u>no</u> password and you can access the system by leaving password line blank and click Enter.

To change the password at any time, click Security on the navigation bar on the left side of the web page. The following screen appears.

Figure 4-2, Security, Change Password

You can type in any password. The password can be virtually any length with any combination of letters and/or numbers. The password is case sensitive.

You can make the new password effective immediately or after a restart by clicking the appropriate option button. Click Change Password when done.

If you press the LAN Reset button on the rear panel of the 1257A for over 10 seconds, resetting your LAN setup, your password is reset to the factory default setting of no password. (See **LAN Reset Button** in **Chapter 1, Overview and Features**, for additional information on the operation of the button.)

System Web Page Descriptions

Home Page

The Home page (**Figure 3-1**) provides a navigation bar on the left side of the screen which allows you to select specific pages for

- Instrument Configuration
- Security (password configuration)
- Firmware upgrade
- Help
- Web controls for the system and the installed cards
- USB Flash drive access (if attached)

The main panel on the web page provides information on

- Instrument model

- Manufacturer

- Serial number

- Instrument description (the instrument's description – which may be modified on the **Instrument Configuration** web page)

- LXI functional declaration

- LXI version (the version for which the instrument has been certified)

- DNS Hostname (the Internet domain name for the instrument or its IP address)

- MAC address

- IP address

- Instrument address string (TCP/IP VISA resource string – the connection string that may be used when making a VISA connection to the instrument over TCP/IP)

- USB VISA resource string (the connection string that may be used when making a VISA connection to the instrument over USB)

- GPIB address

- Firmware revision

Device Identify Button

Figure 4-3, Device Identify Button

When you click the Device Identify button on the Home page (**Figure 4-3**), the LAN status light on the 1257A front panel begins to blink. This feature allows you to identify which 1257A you are communicating with should you have a bank of them.

When you click the button a second time, the status light stops blinking and returns to normal mode.

External Storage Devices
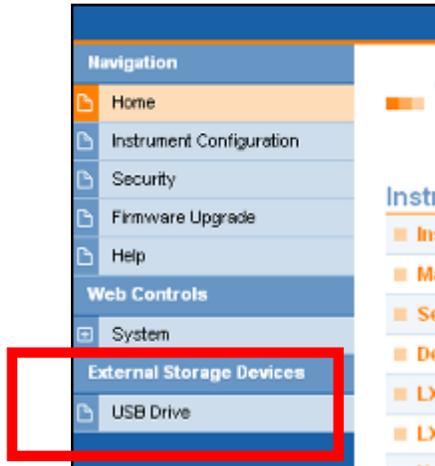
Figure 4-4, USB Drive Indicator

When a USB flash drive is installed to the front USB connector, an **External Storage Devices** list appears on the Home page (**Figure 4-4**).

Click **USB Drive** to see a directory of what is on the drive.

The USB connector on the rear of the chassis is only a communication interface. For memory purposes, use the USB connector on the front panel.

Instrument Configuration Page
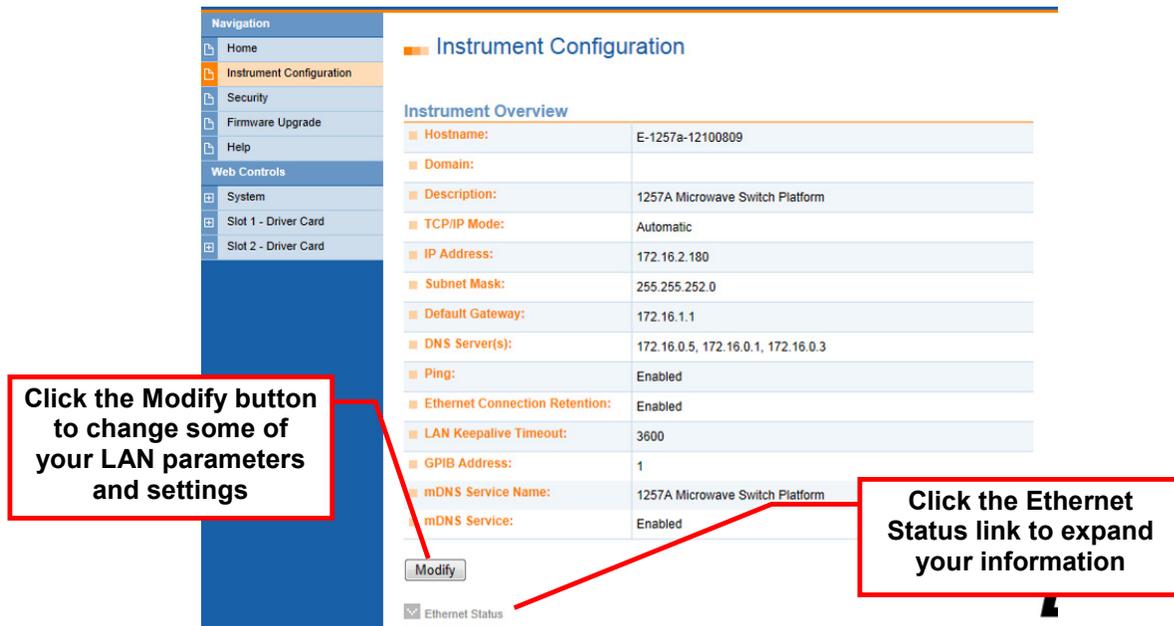
Figure 4-5, Instrument Configuration (Appearance Typical)

The Instrument Configuration page (**Figure 4-5**) provides information on

- Hostname
- Domain (if any)
- Description
- TCP/IP mode
- IP address
- Subnet mask
- Default gateway (if any)
- DNS servers(s) (if any)
- Ping
- Ethernet connection retention
- LAN Keep Alive timeout
- GPIB address
- mDNS service name
- mDNS service enabled/not

Click the link to access additional Ethernet Status information (**Figure 4-6**). This portion of the screen provides additional packet information. You can click the **Refresh** button to get updated information.

You can change some of these configurations by clicking the **Modify** button. See the next section, **Modifying the Instrument Configuration,** for more information.

Figure 4-6, Ethernet Status

Modifying the Instrument Configuration
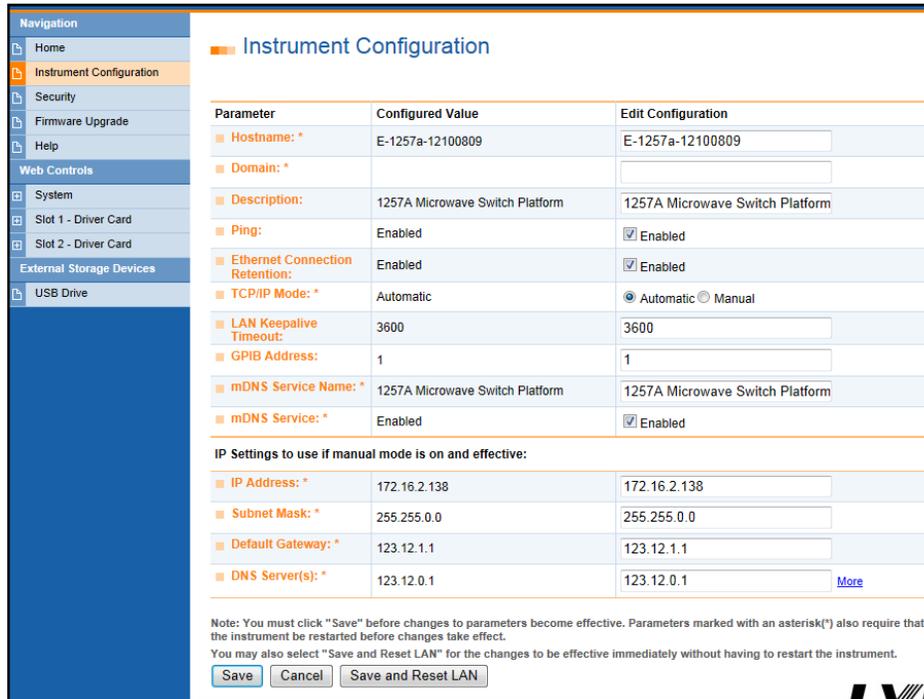
Figure 4-7, Instrument Configuration, Modify Button

After clicking the **Modify** button, you can modify the following parameters:

- Hostname *
- Domain *
- Description
- Ping (enabled or not)
- Ethernet connection retention (enabled or not)
- TCP/IP mode (automatic or manual) *
- LAN Keepalive timeout
- GPIB address
- mDNS Service Name
- mDNS Service (enabled or not)
- IP address *
- Subnet mask *
- Default gateway *
- DNS server(s) * (Click **More** to add another)

You must click the **Save** button before changes to the parameters become effective. Parameters marked with an asterisk (*) also require that the instrument be restarted before changes take effect.

You may also select the **Save and Reset LAN** button for the changes to be effective immediately without having to restart the instrument.

Security Page

Refer to **Setting the Password** earlier in this chapter.

Firmware Upgrade Page

Refer to **Upgrading the Firmware** later in this chapter.

Help Page

The Help page offers a variety of information on the main navigation features and options of the 1257A web pages.

Web Controls

The Web Control section allows you to view and modify the status of your 1257A System as well as to view and modify the various installed cards on your system.

In order to use the various web controls, you need to have a recent version of the Java™ Runtime Engine (JRE) installed on your computer. JRE can be downloaded from

<div align="center">

http://www.java.com/en/download/index.jsp

</div>

Some versions of JRE cause a pop up Warning Security window to appear while trying to open a 1257A web controls page (**Figure 4-8**). In order for the 1257A web controls page to display correctly, user should click **No** to allow the web controls to run. Refer to the **Java Security Warning** in **Chapter 3, Getting Started**, for information on disabling the warning notices from regularly appearing.

<div align="center">

Figure 4-8, JavaScript Warning

</div>

System Controls

Status/Configuration Tab

On this tab (**Figure 4-9**), you can view the current switch configuration as well as reorder the location of the components. (For instance, you might want to this screen to resemble your connectors on the outside panels of your 1257A.) Click **Reorder Components** to move the component locations.

From this tab, components can also be controlled. Left-clicking on the switch will cause the switch to open or close. Clicking on the programmable attenuator drop-down allows for selecting the available attenuator valves.

To change the drive/output signals of the DIO component, select the bits to toggle their value. They will then be highlighted. Selecting **Write** will write the values and update their status. Selecting **Read** will perform a Read update and update the Read values.
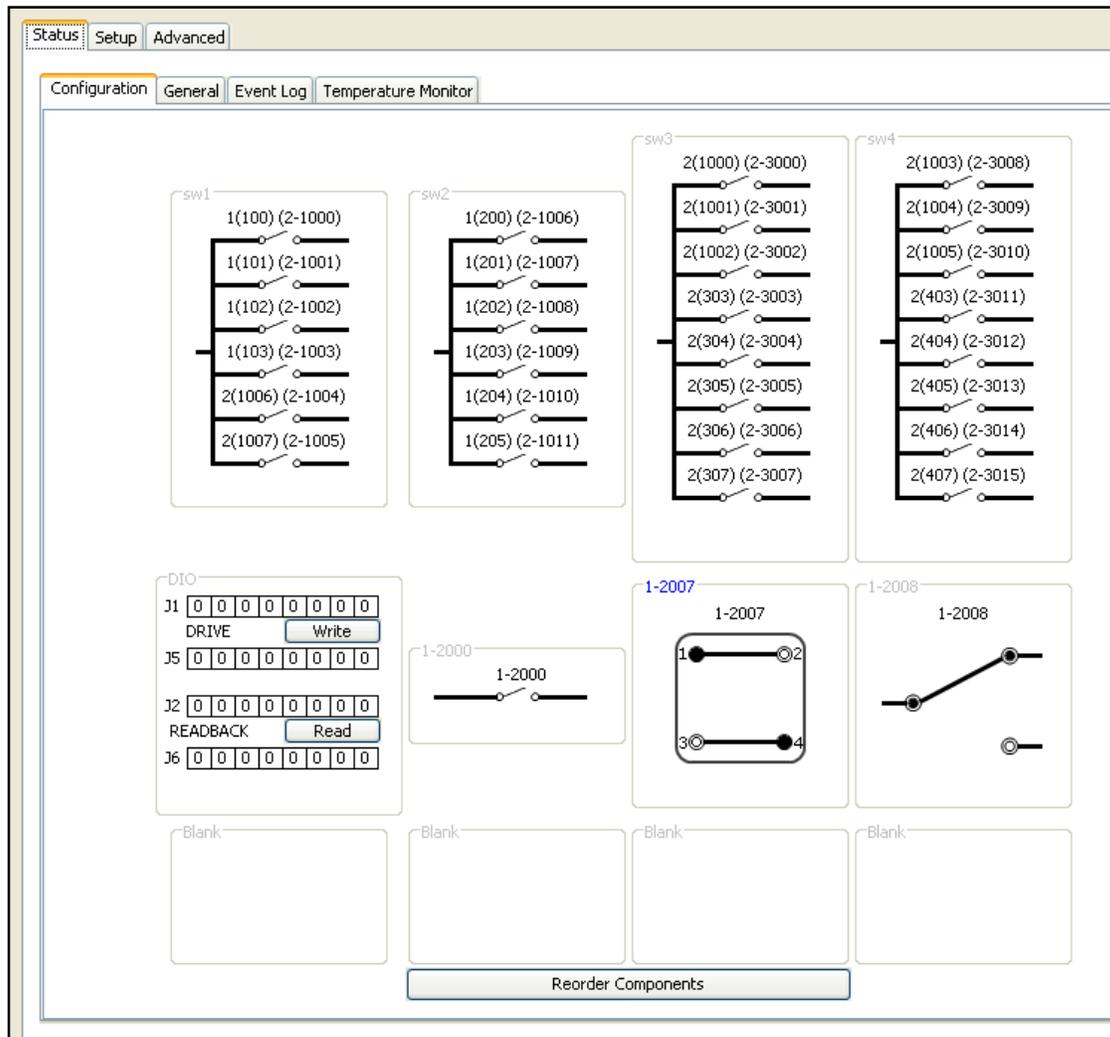
Figure 4-9, System Controls, Configuration Tab

Reorder Components Display

In order to reposition the components on the **Configuration** tab screen, click the Reorder Component button and use the next screen to adjust their positions (**Figure 4-10**). You can update this screen to change the number of rows and columns shown on the **Configuration** tab. You can change the position of the components by moving them up or down on the list. Click **Submit** for the changes to take effect.
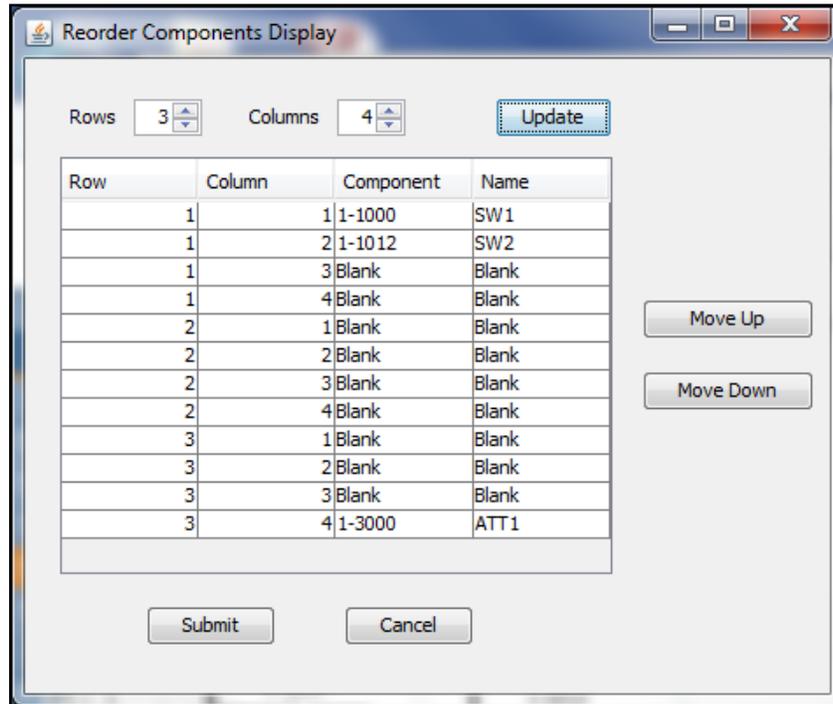
Figure 4-10, Reorder Components Display

Status/General Tab

On this tab (**Figure 4-11**), you can view mainframe and module (card) serial number and revision information, as well as driver board slot configuration.
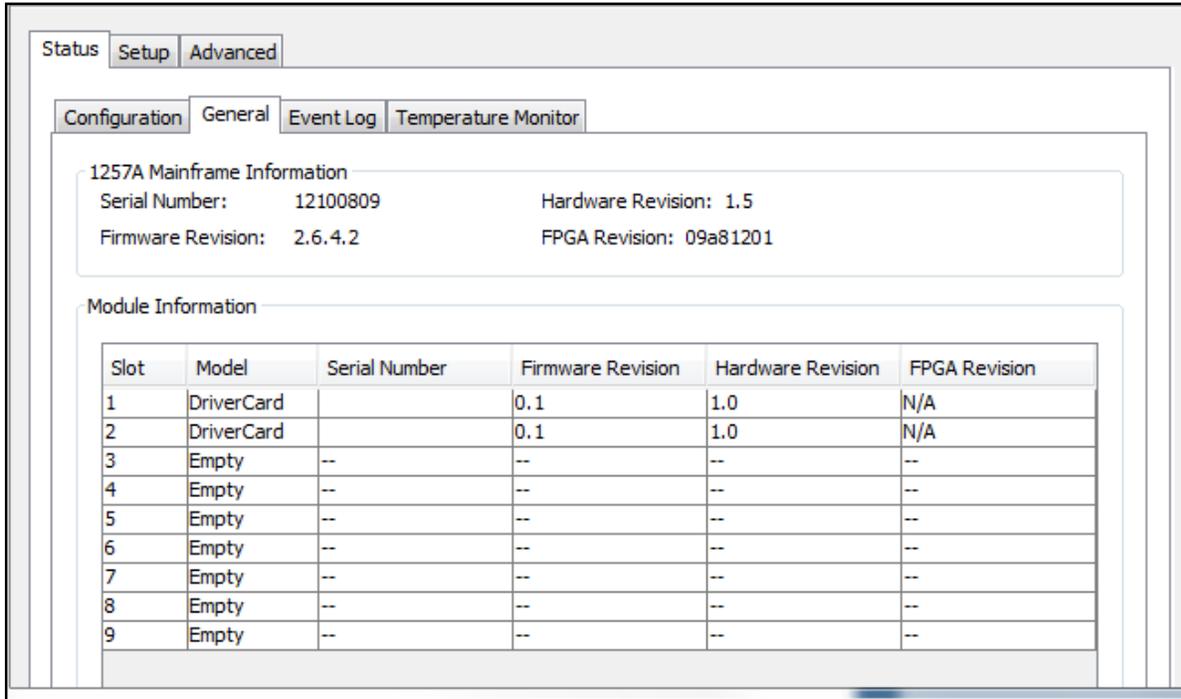
Figure 4-11, System Controls, Status/General Tab

Status/Event Log Tab

On this tab (**Figure 4-12**), you can view the recent events including power-on, system faults, and errors. You can select and clear specific event numbers.

At the bottom of the screen, the **Read All** button shows you all the events in the log. The **Clear All** button clears all of the events from the log. The **Clear** button clears the events from this page.
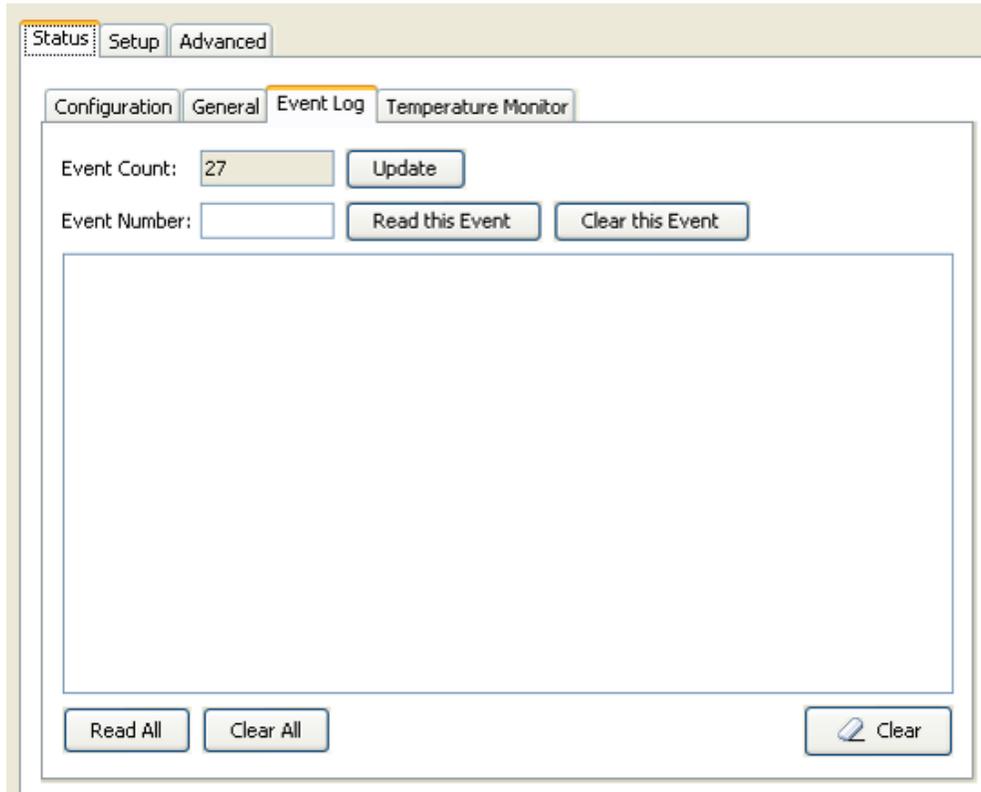
Figure 4-12, System Controls, Status/Event Log Tab

Status/Temperature Monitor

On this tab (**Figure 4-13**), you can view the current temperature condition of various thermistors mounted at different points in the chassis. You can select the reading to report in Celcius or Fahrenheit. Click the **Refresh** button for manual refresh or check the **Auto Refresh** box for continuous updates.
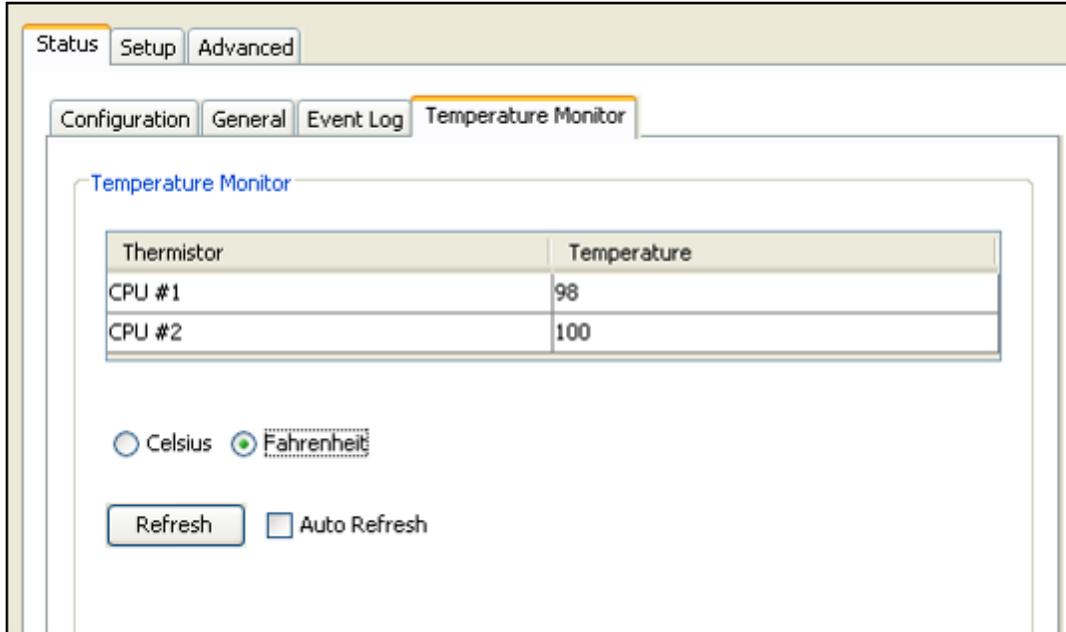
Figure 4-13, System Controls, Status/Temperature Monitor Tab

Setup Tab

The **Setup** tab (**Recall State (0) after a '*RST'** command can be selected to recall State (0) anytime a '*RST' (or SYST:PRESet) command is executed.

Refer to **Power On Recall** in **Chapter 1, Overview and Features**, for additional information about using this feature.

Figure 4-14) allows you to save and recall 1257A system relay states as well as change or update the real time clock.

Save a relay state by setting a location using the drop-down box and then clicking **Save**.

Recall a saved relay state by selecting the location the state is in and clicking **Recall**.

Recall State (0) after a '*RST' command can be selected to recall State (0) anytime a '*RST' (or SYST:PRESet) command is executed.

Refer to **Power On Recall** in **Chapter 1, Overview and Features**, for additional information about using this feature.

Figure 4-14, System Controls, Setup Tab

Advanced Tab

The **Advanced** tab allows you to set or modify the following 1257A system conditions.

- Include/Exclude Lists
- Path Names
- SCPI Command Read and Write

Advanced/Include/Exclude Lists

On Include **Lists/Exclude Lists** tab (**Figure 4-15**), you can:

- Define new include lists and exclude lists
- View the set of include lists and exclude lists you have defined
- Store the include lists and exclude lists to non-volatile memory
- Recall previously stored include lists and exclude lists from non-volatile memory
- Configure the system to automatically recall include lists and excludes lists from non-volatile memory at power-up
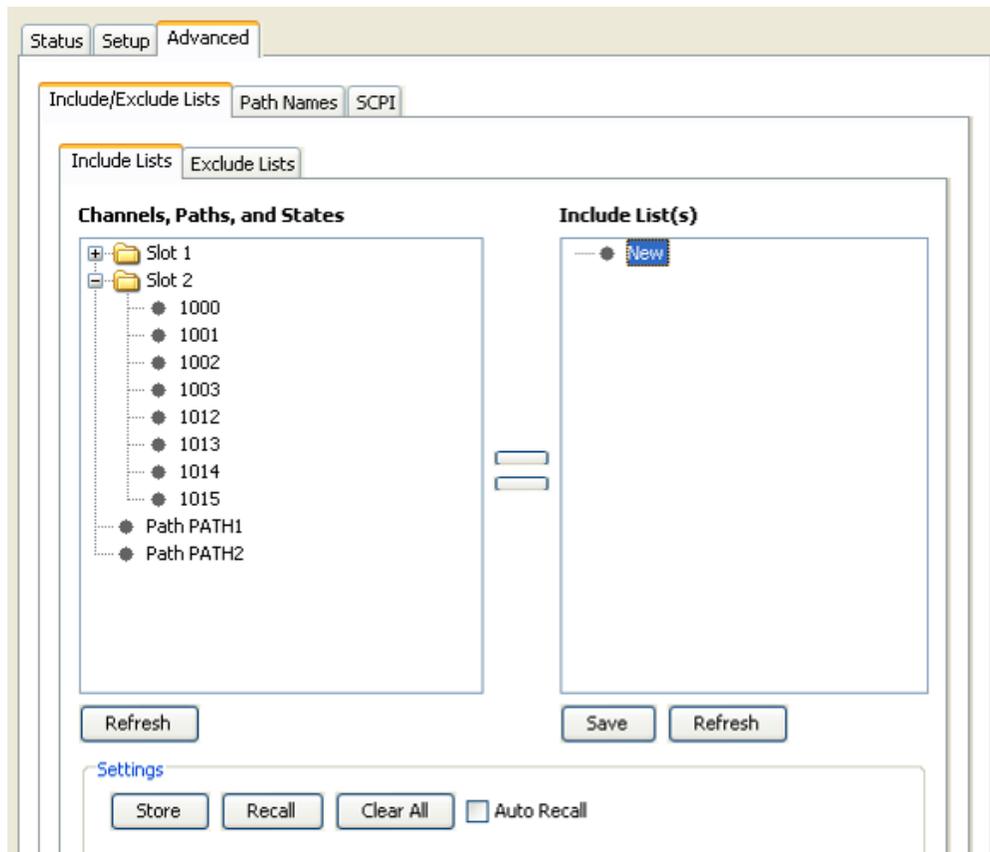
Figure 4-15, System Controls, Advanced/Include/Exclude Lists Tab

Advanced/Path Names Tab

On the **General** tab (**Figure 4-16**), you can:

- View the paths that have been previously defined

- Delete individual path names from volatile memory

- Delete all path names from volatile memory

- Store all presently defined path names from volatile memory to non-volatile memory

- Recall previously stored path names from non-volatile memory

- Configure the system to automatically recall path names from non-volatile memory at power-up
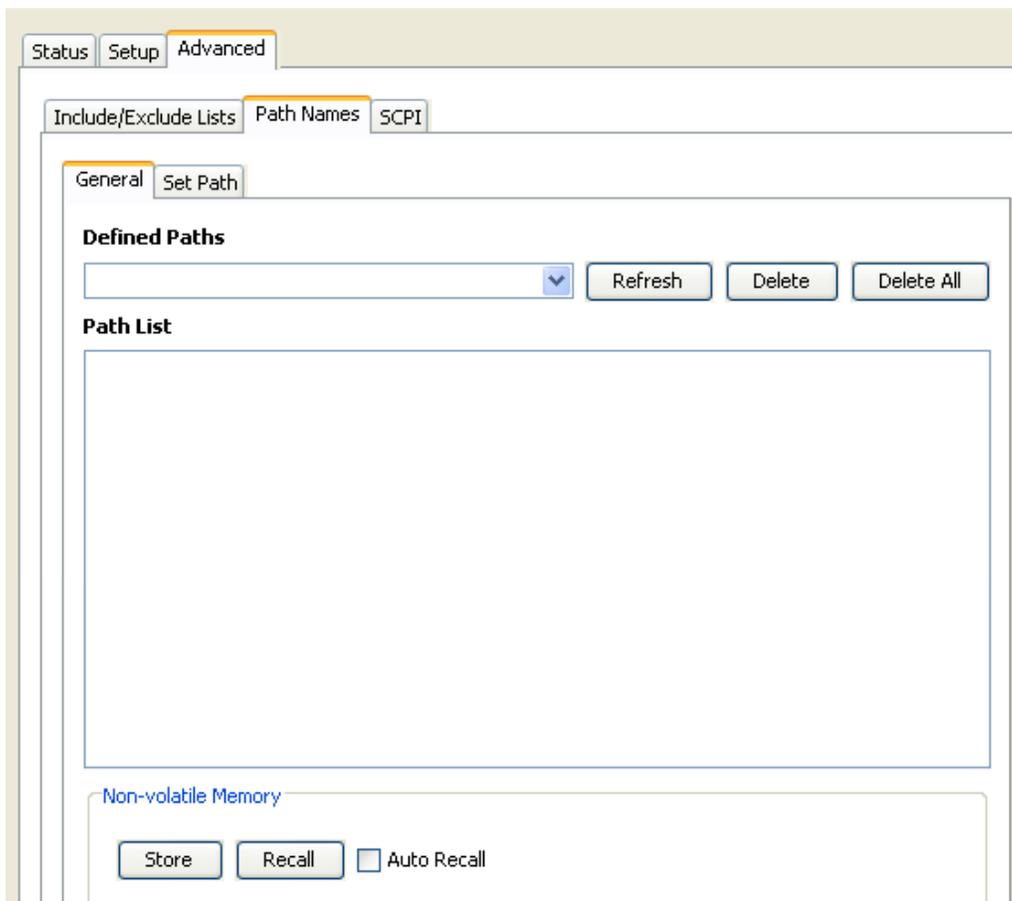
Figure 4-16, System Controls, Advanced/Path Names/General Tab

On the **Set Path** tab (**Figure 4-17**), you can define new paths consisting of one or more relay channels from the 1257A.
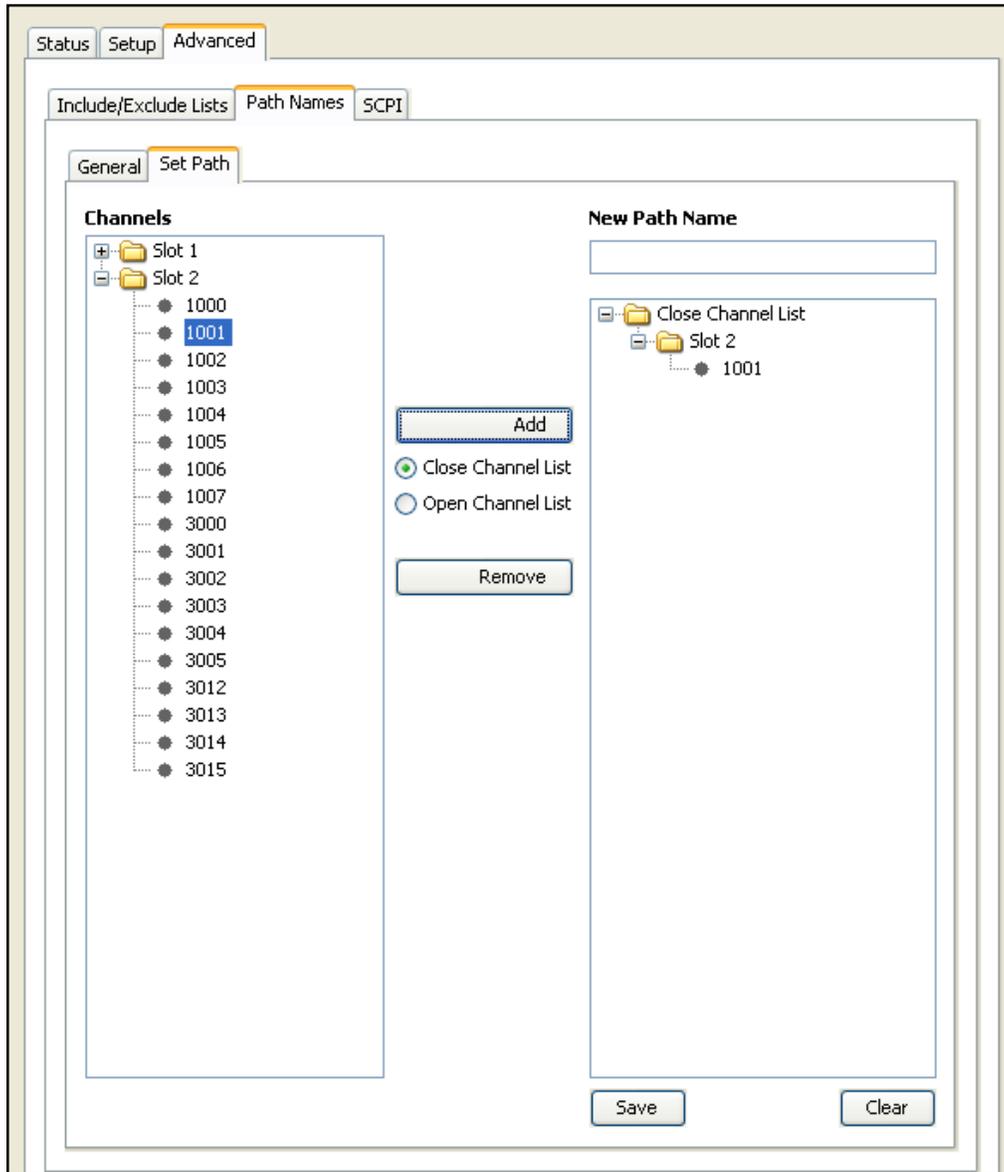
Figure 4-17, System Controls, Advanced/Path Names/Set Path Tab

Advanced/SCPI Tab

The **SCPI** tab (**Figure 4-18**) allows you to send SCPI commands to the 1257A. A few common SCPI commands have clickable icons along the top of the tab including:

- **SYST:ERR?:** Checks on and reports if there are any errors
- **Read STB:** Reads the value of the Status Byte Register
- **Device Clear:** Clears error queue and flushes all input and output buffers
- **Clear:** Clears any text in the text message box below the icons
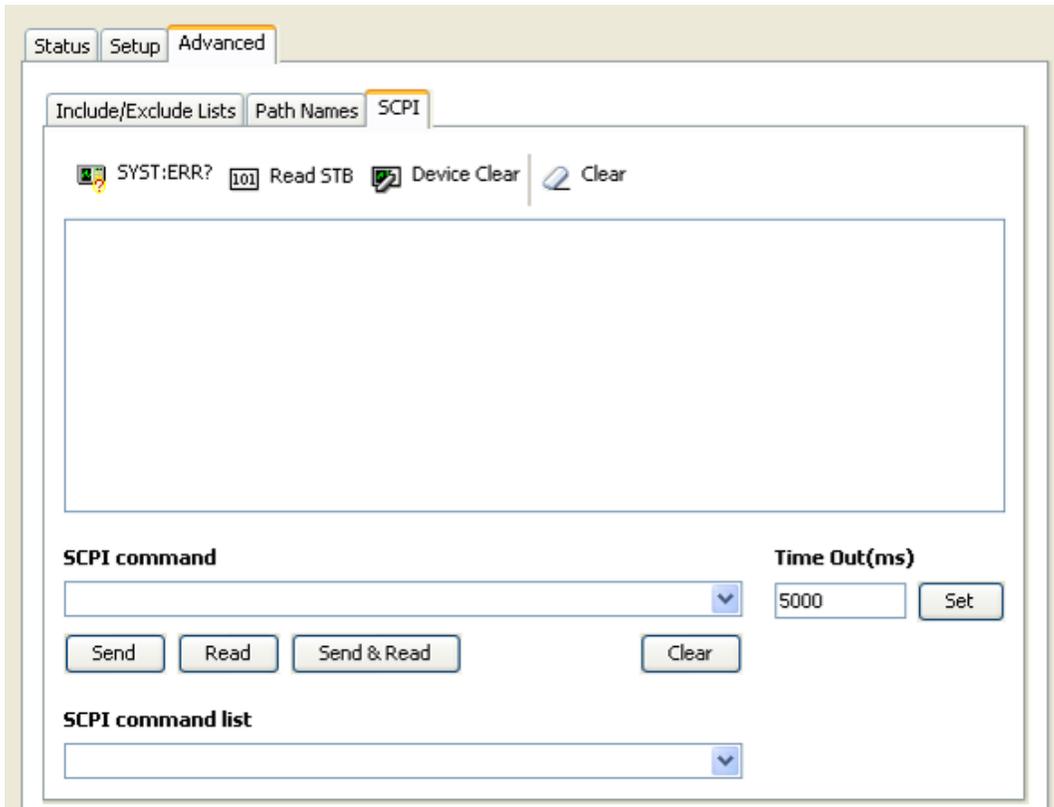
Figure 4-18, System Controls, Advanced/SCPI Tab

Use the **SCPI Command** input box to enter SCPI commands. The command and any replies will be shown in the text message box.

- Clicking **Send** sends the command.
- Clicking **Read** receives the reply.
- Clicking **Send/Read** both sends and then receives the reply.
- Clicking **Clear** clears the commands history in the **SCPI command** input box

Additional information and a listing of SCPI commands can be found in **Chapter 5, SCPI Command Basics**.

Slot Controls Descriptions

Selecting a slot (driver board) from the main Web Page provides access for driver/component configuration and control.

Main Tab

The **Main** tab (**Figure 4-19**) allows you to control the components configured for this driver board. Selecting the component will show the component details and relay cycle counter (where applicable).

From this tab, components can also be controlled. Left-clicking on the switch will cause the switch to open or close. Clicking on the programmable attenuator drop-down allows for selecting the available attenuator valves.

To change the drive/output signals of the DIO component, select the bits to toggle their value. They will then be highlighted. Selecting **Write** will write the values and update their status. Selecting **Read** will perform a Read update and update the Read values.

Figure 4-19, Slot Controls/Main Tab

Configuration Tab

The **Configuration** Tab is used to configure the appropriate components compatible with each driver board. The tabs will differ depending on the Driver Board installed. **Figure 4-20** shows the Attenuator Board view. **Figure 4-21** shows the Relay Board view. **Figure 4-22** shows the DIO Board view.

Each Driver Board has three configurable banks: A, B, and C. The Bank Selection shows the installed adapters for the drive card. These are labeled A, B, and C. An adapter type ID:15 indicates NO adapter is installed into that position.

After a Bank/Adapter Card is selected, the next step is to select or review the configuration options. The configuration options present which types of components and what configuration is supported by the selected adapter board. Based upon the configuration options, the Board Connectors Section will show the available connectors and their configuration status.

To configure a connector, select the connector. For the relay configuration, enter the relay name (for example, SW2). The relay model is selected from the drop-down menu.

The Channel Alias Assignment section can be utilized to enter channel aliases.

Figure 4-20, Slot Controls/Configuration/Adapter Board Tab (Attenuator Configuration)
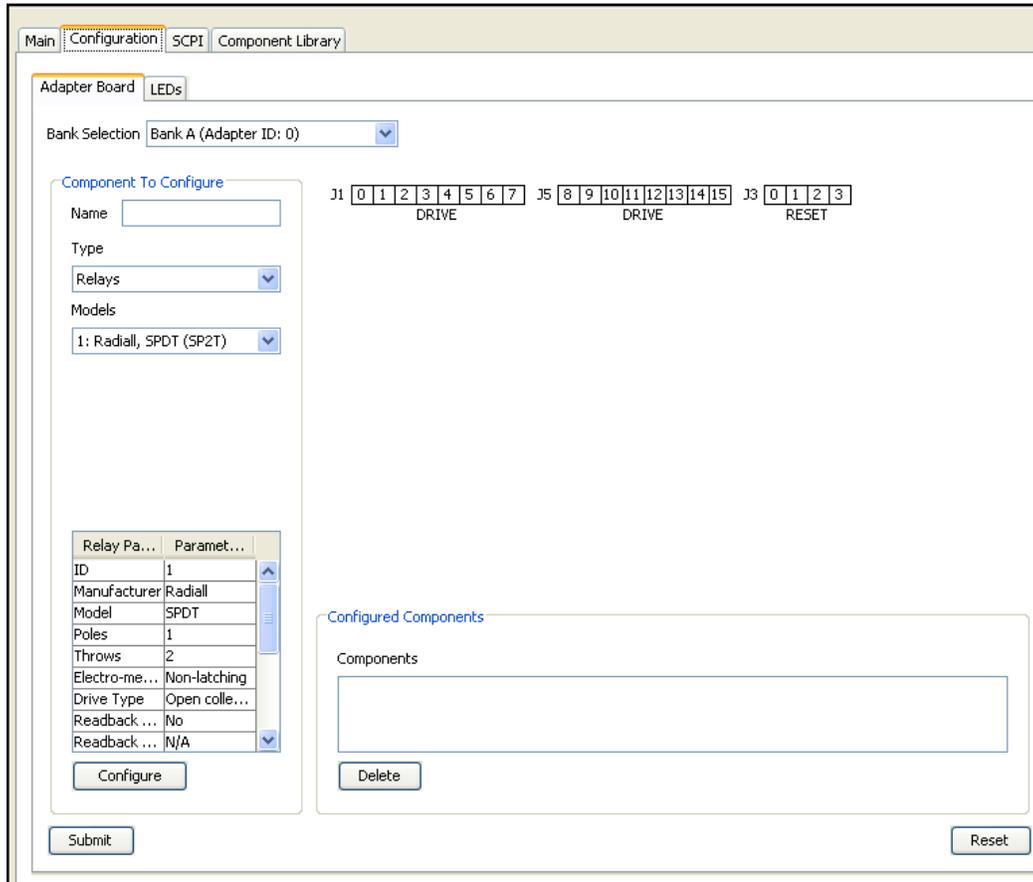
Figure 4-21, Slot Controls/Configuration/Adapter Board Tab (Relay Configuration)
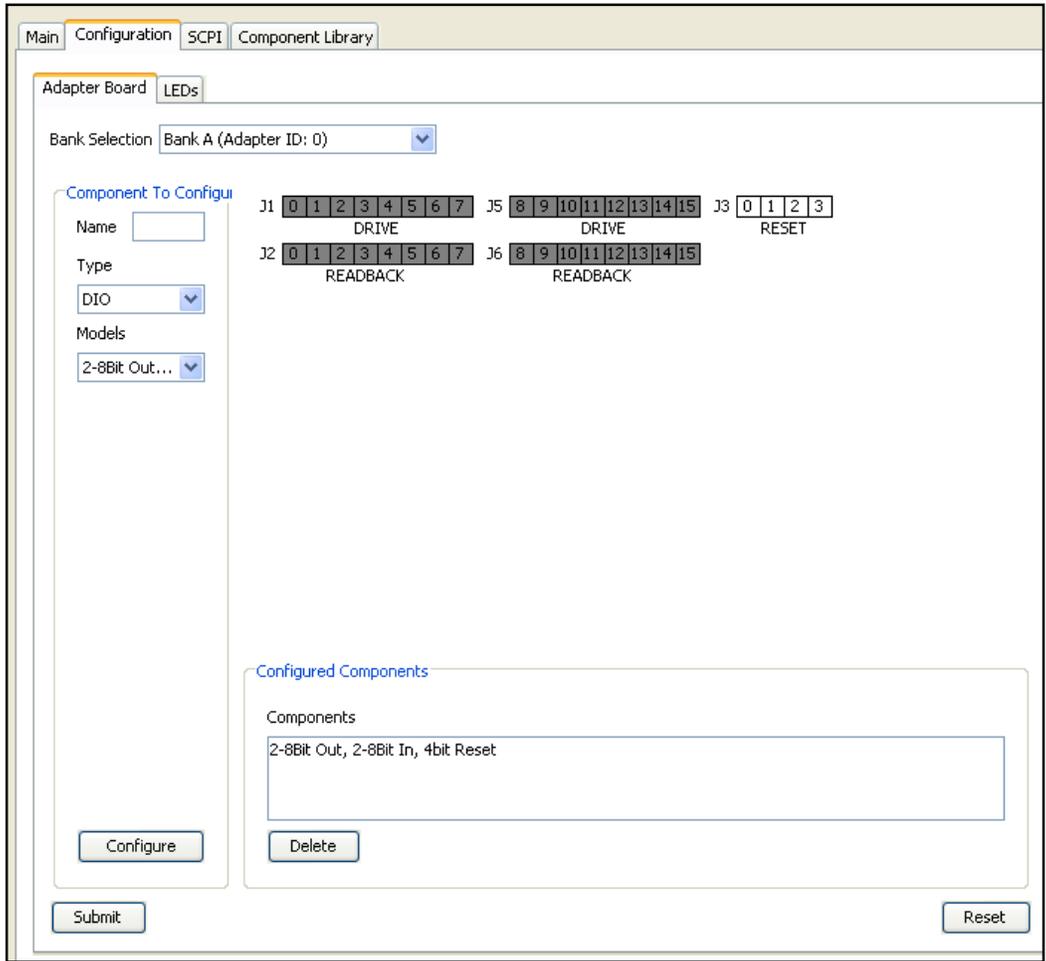
Figure 4-22, Slot Controls/Configuration/Adapter Board Tab (DIO Configuration)

The **LEDs** tab (**Figure 4-23**) allows for configuring 16 LEDs per bank. The polarity and drive current for the LED can be selected along with its mode.

The mode choices are: Control, Indicator, and Manual.

- Control ties the LED state to the relay commanded stated.

- Indicator ties the LED state to the Indicator state.

- Manual control allows for independent LED control.
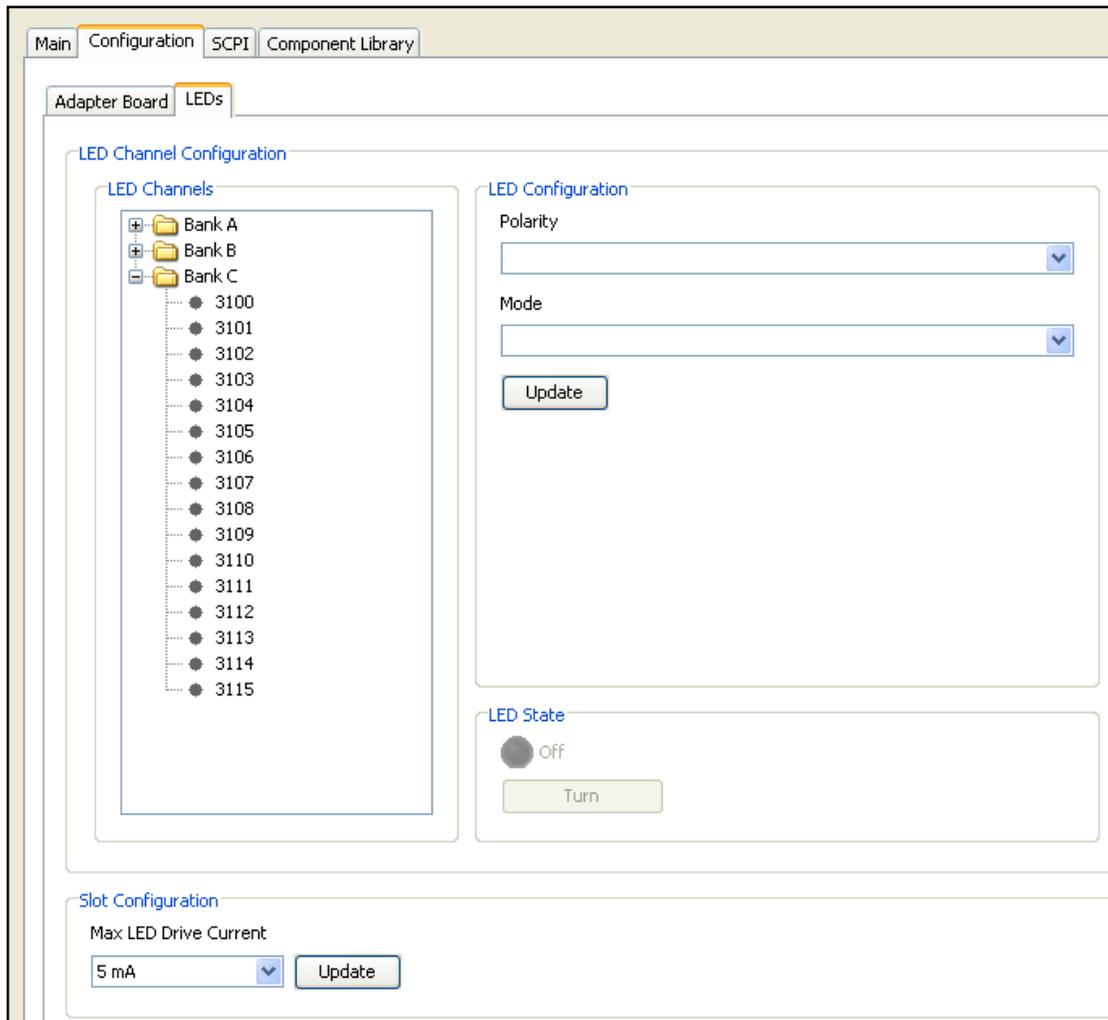
Figure 4-23, Slot Controls/Configuration/LEDs Tab

SCPI Tab

The **SCPI** tab (**Figure 4-24**) allows you to send SCPI commands to the card. A few common SCPI commands have clickable icons along the top of the tab including:

- **SYST:ERR?:** Checks on and reports if there are any errors
- **Read STB:** Reads the value of the Status Byte Register
- **Device Clear:** Clears error queue and flushes all input and output buffers
- **Clear:** Clears any text in the text message box below the icons
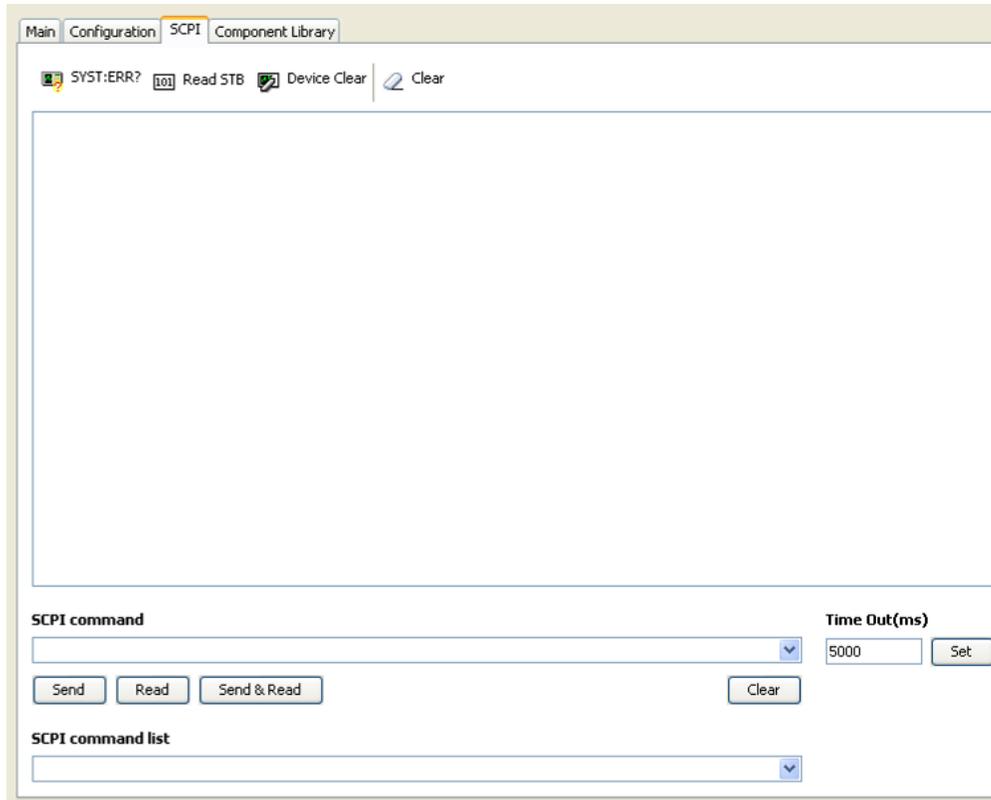
Figure 4-24, Slot Controls/SCPI Tab

Use the **SCPI Command** input box to enter SCPI commands. The command and any replies will be shown in the text message box.

- Clicking **Send** sends the command.
- Clicking **Read** receives the reply.
- Clicking **Send/Read** both sends and then receives the reply.
- Clicking **Clear** clears the commands history in the **SCPI command** input box

Additional information and a listing of SCPI commands can be found in **Chapter 5, SCPI Command Basics**.

Component Library Tab

The **Component Library** tab (**Figure 4-25**) allows you to:

- Add new relay types to the library of relays

- Add new attenuator types to the library of attenuators

- View the attributes of the existing relay and attenuator types

- Modify existing relay and attenuator types (not recommended as this may affect the operation of your system)
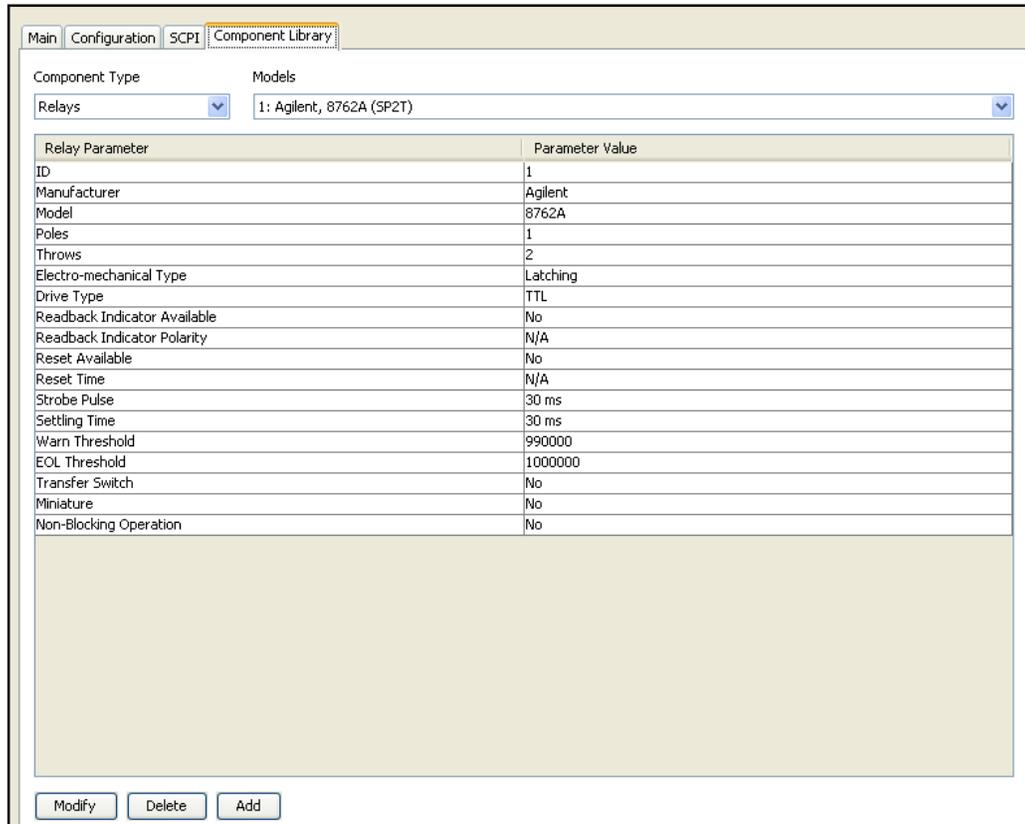
Figure 4-25, Slot Controls/Component Library Tab

Upgrading the Firmware

The embedded software (firmware) supports downloading newer versions from our support website so that the 1257A system does not have to be returned to the factory for a software update. The firmware uses the built-in web-page interface to upload the software through the 1257A Ethernet port.

Note

Before upgrading your firmware, make a copy of your existing 1257A switch configuration information by following the procedure in *Appendix E, Using the Software Configurator Tool*. **Having a copy enables you to easily restore the system configuration if needed.**

When there's a new 1257A firmware upgrade available, it'll be downloadable from our website. Once you've downloaded the new firmware upgrade file to your computer, you're ready to perform the upgrade (if a zip file was downloaded, unzip it).

1. Open the Home page of the 1257A system to upgrade.
2. Select and open the **Firmware Upgrade** link on the navigation bar (refer to Setting the Password section of this book for information regarding the user name and password).
3. Use the **Browse** button to select the file downloaded (with the ".bin" extension). It is important that you do **not** change the extension (.bin) of the upgrade firmware file (**Figure 4-26**).
4. Click the **Upgrade** button. The software begins to upgrade your current firmware. (**Figure 4-27**).

Caution

DO NOT power off the 1257A system while the upgrade is taking place. If the upgrade is interrupted and the firmware is corrupted, the 1257A systemautomatically loads the backup firmware. This version includes only a minimal feature set and you must upgrade the firmware again.

Figure 4-26, Firmware Upgrade File

Figure 4-27, Firmware Upgrade Status

5. When the upgrade is complete (**Figure 4-28**), exit out of the message box.

Figure 4-28, Firmware Upgrade Complete

6. Turn the system power off and then back on, in order to restart the system and initialize the firmware upgrade.

7. Review the Instrument Information section of the Home page and confirm that the Firmware level is at the correct revision (**Figure 4-29**).
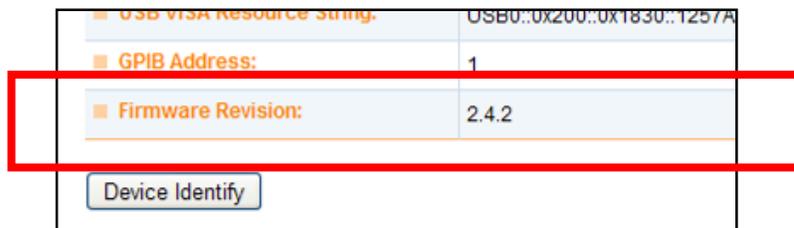
Figure 4-29, Checking Firmware Revision Level

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Chapter 5
SCPI Command Basics

SCPI Command Overview

The 1257A accepts commands via either the GPIB or serial interface. 1257A commands follow the rules defined by the SCPI standard. SCPI is an acronym for "Standard Commands for Programmable Instruments", and defines standard command names and syntax rules for commands to the instrument and replies from the instrument.

Input Format

SCPI is an ASCII-based instrument command language designed for test and measurement instruments. SCPI commands are based on a hierarchical structure known as a tree system. In this system, associated commands are grouped together under a common root, thus forming command subsystems. Throughout this chapter, the following conventions are used for SCPI command syntax.

Square Brackets ([ ])	Enclose optional keywords or parameters
Braces ({ })	Enclose possible parameters within a command
Triangle Brackets (< >)	Substitute a value for the enclosed parameter
Vertical Bar (|)	Separate multiple parameter choices
Bold Typeface Letters	Designate factory default values
(Command Only)	Indicates the command cannot be used in query form
(Query Only)	Indicates the command can only be used as a query. The command form, without the question mark is not allowed.

To illustrate the SCPI notation, a part of the ROUTE command subsystem is shown below:

```
[:ROUTe]
    :CLOSe <channel list>
    :OPEN <channel list>
        :ALL
```

ROUTE is the root keyword of the command. This keyword is optional, since it is shown enclosed in square brackets. CLOSE and OPEN are the next level keywords. The "ALL" keyword is below the OPEN keyword in this command tree.

The colon (:) is used to separate keywords from different levels on the command

tree. Each keyword is separated from the next by a single colon.

The SCPI commands which may be formed by this tree are shown below (a <channel list> of (@1(1000)) is used in these examples):

```
ROUTE:CLOSE (@1(1000))
CLOSE (@1(1000))
ROUTE:CLOSE? (@1(1000))
CLOSE? (@1(1000))
ROUTE:OPEN (@1(1000))
OPEN (@1(1000))
ROUTE:OPEN? (@1(1000))
OPEN? (@1(1000))
ROUTE:OPEN:ALL
OPEN:ALL
```

Note that the optional ROUTE keyword is omitted in many of the examples. This is allowed because the ROUTE keyword is optional, as indicated by the square brackets in the command tree.

Command Keyword Long Form and Short Form

Each keyword defined by SCPI has both a **long form** and a **short form**. The long form is formed by using all letters shown in the keyword. The short form is formed by using only those letters shown in upper-case in the command tree.

The short form is normally three or four letters in length ending with a consonant where possible.

For example, the ROUTE keyword may be specified by either of the following:

```
ROUT
ROUTE
```

since the final "e" is shown in lower-case in the command tree.

Only the long form or short form may be used. For example, the keyword "DEFINE" is shown as:

```
DEFine
```

Therefore, the following two command keywords are valid:

```
DEFINE
DEF
```

But the keywords

```
DE
DEFI
DEFIN
```

are NOT valid.

Case Sensitivity

Command keywords are **NOT** case sensitive. Command parameters are not case sensitive, unless the parameter is string data enclosed in quotes.

For example, the following commands are equivalent:

```
CLOSE
Close
```

Optional Keywords

Command keywords enclosed in square brackets are optional. For example, the following commands are valid and equivalent:

```
ROUTE:CLOSE (@1(1000))
CLOSE (@1(1000))
```

Querying Parameter Setting

Most SCPI commands have corresponding query forms. The query is used to read the present state of the item that is set with the command. The query is formed by appending a question mark (?) to the end of the command keyword.

For example, the command:

```
ROUTE:CLOSE (@1(1000))
```

Has an equivalent query:

```
ROUTE:CLOSE? (@1(1000))
```

The command instructs the 1257A to close channel 0 in the drawer. The query inquires about the present open or close state of channel 0 in the drawer.

SCPI Command Terminator

For commands sent through the GPIB interface, a command string sent to the 1257A must be terminated with one of the following:

1. An ASCII linefeed character (decimal 10, hex 0A, or a character '\n').

2. The last character with the EOI signal asserted.

3. An ASCII linefeed character with the EOI signal asserted.

For commands sent through either the TCP/IP (Ethernet) or USB interfaces, the commands must be terminated with an ASCII linefeed character.

IEEE-STD-488.2 Common Commands

The IEEE-STD-488.2 standard defines a set of common commands that perform functions like reset, trigger and status operations. Common commands begin with an asterisk (*), are four to five characters in length, and may include parameters. The command keyword is separated from the first parameter by a blank space. A semicolon (;) may be used to separate multiple commands as shown below:

```
*RST; *STB?; *IDN?
```

The IEEE-488. 2 common commands implemented by the 1257A are described later in this chapter.

IEEE 488.2 Common Commands

The 1257A supports all required IEEE-488.2 commands. In addition, the optional *OPT?, *SAV, and *RCL commands are supported. The following paragraphs describe the IEEE-488.2 commands supported by the 1257. In addition, the status reporting model implemented by the 1257A is described. The 1257A complies with this standard for both GPIB and RS-232 operational modes.

The following commands are implemented:

    *IDN?   Identification query

    *RST   Instrument reset

    *TST?   Commanded self-test

    *CLS   Clear status

    *ESE   Set the Standard Event Status Enable register

    *ESE?   Read the Standard Event Status Enable register

    *ESR?   Read the Standard Event Status register

    *SRE   Set the Service Request Enable register

    *SRE?   Read the Service Request Enable register

    *STB?   Read the status byte

    *OPC   Set the OPC bit of the Standard Event Status register

    *OPC? Reply with "1" when executed (used for synchronizing)

    *SAV   Store relay states in non-volatile memory

    *RCL   Recall relay states from non-volatile memory

    *OPT? Read if any options are installed

These commands are described in greater detail in this section of the manual.

IEEE-488. 2 Status Description

The IEEE-488.2 Status Reporting Model is shown in **Figure 5-1**. This figure shows how the status reporting data structures are implemented and the commands used to set and read each of the registers

The 1257A maintains a set of these status registers for each individual interface it supports. That is, there is one set of these registers for the GPIB interface, one set of these registers for the USB interface, and one set of these registers for EACH connection over the TCP/IP (Ethernet) interface.

This means that an error in the command received over the GPIB interface will not be reflected in the Standard Event Status Register value read over the USB interface or any Ethernet connection.

Standard EVENT STATUS Register

Figure 5-1 shows four related registers. The *Standard Event Status Register*, the *Standard Event Status Enable Register*, the *Status Byte Register*, and the *Service Request Enable Register*.

The *Standard Event Status Register* reflects the present status of the instrument. This register consists of eight 1-bit flags. Each flag represents a true or false indication of the corresponding condition. The bits of this register are:

PON	Power-On	

PON   Power-On
Bit 7 (MSB), Bit weight = 128 decimal = 80 hexadecimal
This flag is set when the instrument is powered on.

URQ   User Request
Bit 6, Bit weight = 64 decimal = 40 hexadecimal
This bit is never set by the 1257A and will always read 0.

CME   Command Error
Bit 5, Bit weight = 32 decimal = 20 hexadecimal
This bit is set when a command error is detected by the 1257.

Errors of this type will also result with an error added to the error queue. The error may be read using the SYST:ERR? query to determine the cause of the error

EXE   Execution Error
Bit 4, Bit weight = 16 decimal = 10 hexadecimal
This bit is set when a valid command is received but cannot be executed for some reason. For example, an error occurs while executing the *SAV command. When this error occurs, an error message is added to the message queue and may be read using the SYST:ERR? query.
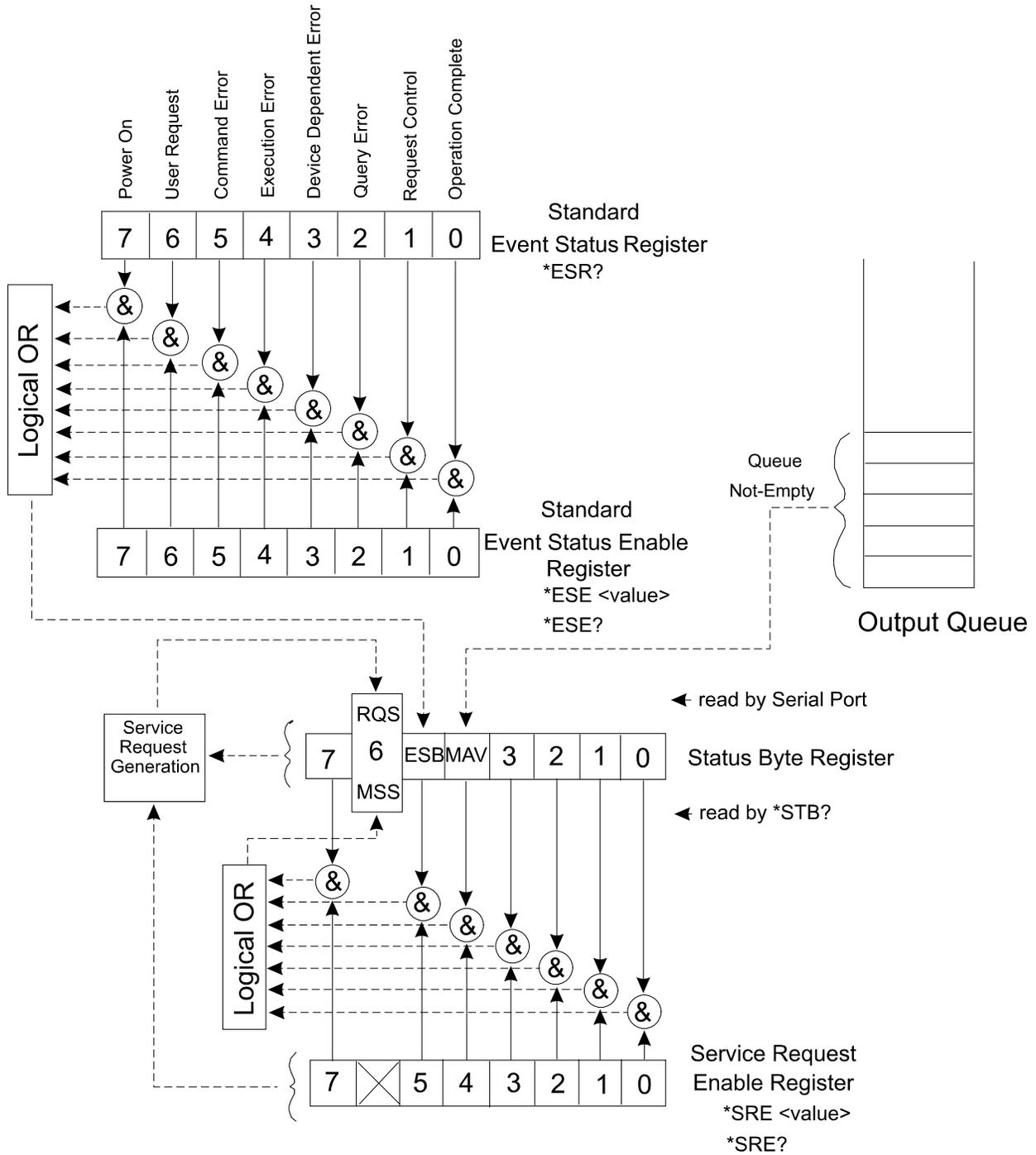
Figure 5-1, IEEE-488.2 Status Reporting Model

DDE	Device Dependent Error	

DDE   Device Dependent Error

Bit 3, Bit weight = 8 decimal = 8 hexadecimal

This bit is set when a device-dependent error is detected. For example, a channel in the 1257A drawer is read during power-up but contains an unknown identification byte value. When this error occurs, an error is added to the error message queue.

QYE   Query Error

Bit 2, Bit weight = 4 decimal = 4 hexadecimal

This bit is set when a query error is detected. For example, a query is sent, but the reply is not read, and a second query or command is sent. When this error occurs, an error is added to the error message queue.

RQC   Request Control

Bit 1, Bit weight = 2 decimal = 2 hexadecimal

This bit is never set by the 1257A and will always read 0.

OPC   Operation Complete

Bit 0, Bit weight = 1 decimal = 1 hexadecimal

This bit is set when the *OPC command is executed. This may be used to synchronize the 1257A with the commands (to ensure that the 1257A command buffer is empty).

A bit is set in this register when the corresponding condition becomes true. It remains set until the *ESR? query is executed. When the query is executed, the reply contains the present value of the register, and the register is then cleared to 0.

The value returned by the *ESR? query represents a sum of the bit-weight values for all conditions that are true. For example, if the PON bit is set and the QYE bit is set, and the rest of the bits are cleared, then the value returned for the *ESR? query is:

PON + QYE = 128 + 4 = 132

The Standard Event Status Enable Register provides a mask register. The value of this register is logically ANDed with the Standard Event Status Register. If the value of this ANDing is nonzero, then bit 5 of the Status Byte Register is set. This bit is known as the "Event Summary Bit", or ESB.

For example, if the PON and QYE bits of the *Standard Event Status Register* are set, but the *Standard Event Status Enable Register* value is 0, then the ESB of the *Status Byte Register* will not be set. If either, or both, bits 7 and bit 2 of the *Standard Event Status Enable Register* are set, then the ESB bit of the *Status Byte Register* will be set.

Another way of viewing the *Standard Event Status Enable Register* is that it selects which conditions reflected in the *Standard Event Status Register* are enabled to set the ESB bit of the status byte.

STATUS BYTE Register

The Status Byte Register is similar to the Standard Event Status Register. Each bit of this register reflects the true or false condition of the corresponding bit. These bits reflect the PRESENT value of the condition, whereas the Standard Event Status Register bits are latched. That is, once a bit in the Standard Event Status Register is set, it remains set until a *CLS command is executed or an *ESR? Query is executed. However, the bits of the Status Byte Register change states as the corresponding condition becomes true or false. These bits are NOT latched.

Also, the Status Byte is not cleared by reading the register. Each bit of the Status Byte remains set until the condition indicated by the bit is no longer present.

The following bits are assigned in the Status Byte Register. All other bits are not used and will return "0" when read with the *STB? query:

OSE   Operation Status Event

Bit 7, bit weight = 128 decimal = 80 hexadecimal

This bit is set when any of the bits of the Operation Status Event Register are set. (This bit is NOT shown on the diagram. For a description of the Operation Status Event Register, consult the "SCPI Status Registers" section of this chapter.

MSS   Master Summary Status.

Bit 6, bit weight = 64 decimal = 40 hexadecimal

This bit is set when one or more of the "enabled" bits of the Status Byte are set. In other words:

(Status Byte bit 0 AND SRE bit 0)
OR
(Status Byte bit 1 AND SRE bit 1)
OR
(Status Byte bit 2 AND SRE bit 2)
OR
(Status Byte bit 3 AND SRE bit 3)
OR
(Status Byte bit 4 AND SRE bit 4)
OR
(Status Byte bit 5 AND SRE bit 5)
OR
(Status Byte bit 7 AND SRE bit 7)

where the SRE is the Service Request Enable Register.

ESB   Event Summary Bit

Bit 5, bit weight = 32 decimal = 20 hexadecimal

This bit is set when one of the enabled Standard Event Status Enable Register bits is set. The previous paragraphs describe the formation of the ESB bit.

MAV   Message Available

Bit 4, bit weight = 16 decimal = 10 hexadecimal

This bit is set when there is a message in the output buffer of the 1257.

All other bits (3, 2, 1, and 0) of the Status Byte are not assigned and will always return 0.

When the MSS transitions from a 0 to a 1, the GPIB SRQ interrupt is generated. The MSS will remain 1 until all enabled bits of the Status Byte have returned to 0.

SERVICE Register ENABLE Register

The Service Request Enable Register is used to individually enable bits in the Status Byte to set the MSS bit of the Status Byte, thereby generating an interrupt. When the bit of the Service Request Enable bit is set, and the corresponding bit of the Status Byte Register is set, then the MSS bit will be a 1.

The following IEEE-488.2 Common Commands relate to the Status Reporting Model:

*CLS   Clear status

*ESE   Set the Standard Event Status Enable register

*ESE?   Read the Standard Event Status Enable register

*ESR?   Read the Standard Event Status register

*SRE   Set the Service Request Enable register

*SRE?   Read the Service Request Enable register

*STB?   Read the status byte

These common commands are described below.

*CLS Command

The *CLS command clears the SCPI and IEEE-488.2 defined status event registers. These include:

- The Standard Event Status Register is cleared to 0. This register is read using the *ESR? query.

- The Standard Event Status Enable Register is cleared to 0. This register is set using the *ESE command, and read using the *ESE? query.

- The Service Request Enable Register is cleared to 0. This register is set using the *SRE command, and read using the *SRE? query.

- The Operation Status Enable Register is cleared to 0. This register is set using the STATUS:OPERATION:ENABLE command. This register can also be read using the STATUS:OPERATION:ENABLE? query.

- The Operation Status Event Register is cleared to 0 This register is read using the STATUS:OPERATION:EVENT? query.

- The Questionable Status Enable Register is cleared to 0. To set this register, the STATUS:QUESTIONABLE:ENABLE command is issued. To read this register, the query STATUS:QUESTIONABLE:ENABLE? indicates whether this feature is enabled.

- The Questionable Status Event Register is cleared to 0. It is read using the STATUS:QUESTIONABLE:EVENT? Query.

*ESE Command

The *ESE command sets the value of the Standard Event Status Enable Register. The value of this register is logically ANDed with the contents of the Standard Event Status Register (see the *ESR? query description). If any bits of this AND operation are set, then bit 5 of the Status Byte is set. Bit 5 is known as the Event Summary Bit, or ESB.

ESB of Status Byte =

(bit 0 of ESE AND bit 0 of ESR)

OR

(bit 1 of ESE AND bit 1 of ESR)

OR

(bit 2 of ESE AND bit 2 of ESR)

OR

(bit 3 of ESE AND bit 3 of ESR)

OR

(bit 4 of ESE AND bit 4 of ESR)

OR

(bit 5 of ESE AND bit 5 of ESR)

OR

(bit 7 of ESE AND bit 7 of ESR)

where:

ESE is the value as set by the *ESE command

ESR is the value which may be read with *ESR? query

The *ESE command has the format:

```
*ESE <ESE value>
```

where the "<ESE value>" is an integer numeric value in the range from 0 through 255.

*ESE? Query

This query reads the value presently programmed for the *Standard Event Status Enable Register*. This reads the value as programmed by the "*ESE" command.

*ESR? Query

This query reads the value of *the Standard Event Status Register*. Each bit of this register indicates a true/false status condition. When the bit is set, the condition is TRUE; when the bit is cleared, the condition is FALSE. The bit assignments are defined by the IEEE-488.2 specification. The bit assignments are described in the text following **Figure 5-1**.

The bits of the *Standard Event Status Register* are cleared at power-on, except for bit 7, which is set. As conditions become true, the corresponding bit in the register are set. These bits remain set until:

- The *ESR? query is executed

- The *CLS command is executed

The reply to the *ESR? query is a numeric integer value in the range "0" to "255".

*IDN Query

This query requests the instrument to identify itself. The 1257A responds to this query with the following reply:

```
Astronics Test Systems,1257A,<serial number>,<firmware
revision>
```

This reply indicates the instrument is from Astronics Test Systems, that it is a 1257A model, and includes the serial number and the current firmware revision. The serial number is an 8-digit numeric value. The firmware revision is a three field numeric value, where each number is separated by a period. For example, a firmware revision might be "2.5.1".:

```
Astronics Test Systems,1257A,10181382,2.5.1
```

*SRE Command

The *SRE command sets the value of Service Request Enable Register. The value of this register is logically ANDed with the contents of the Status Byte. If any bits of this AND operation are set, bit 6 of the Status Byte is set. Bit 6 is also known as the Master Status Summary bit. Consult the text immediately following **Figure 5-1** for a description of the MSS bit and the Service Request Enable register.

The command has the format:

```
*SRE <SRE value>
```

where the "<SRE value>" is an integer numeric value in the range 0 to 255. The value of bit 6 of this register is ignored, since it does not make sense to enable an interrupt when an interrupt is generated.

*SRE? Query

This query reads the value presently programmed for *the Service Request Enable Register*. This reads the value as programmed by the *SRE command. The reply to this command is a numeric value in the range 0 to 255.

*STB? Query

This query reads the value of the *Status Byte Register*. Each bit of this register indicates a true/false status condition. When the bit is set, the corresponding condition is TRUE; when the bit is cleared, the condition is FALSE. The bit assignments are defined by the IEEE-488.2 specification. The bit assignments are described in the Status Byte Register section of this chapter.

The value returned by the *STB? Query may also be read using the GPIB serial poll command.

Note that bit 6 (MSS) of the *Status Byte Register* remains set until all enabled conditions are cleared. This is in contrast to the GPIB serial poll operation, where the SRQ bit is set until it is read once, and cleared after.

*OPC Command

The *OPC command will cause the Operation Complete bit of the Standard Event Status Register to be set when the command is executed. This is bit 0 of the register.

This command could be used to cause an interrupt (if bit 0 of the *Standard Event Status Enable Register* is set, and bit 5 of the *Service Request Enable Register* is set). This provides a means of synchronizing the application program with the 1257A and ensuring that all commands have been parsed and executed before continuing execution of the application program.

The *OPC command has no parameters. The only valid syntax for this command is:

```
*OPC
```

*OPC? Query

The *OPC? query causes the 1257A to reply with the value of "1" when the query is executed. This query may be used to ensure that all previous commands have been executed so the application program may be sure that relays have been programmed to their desired states before continuing execution of the application program.

SYSTem:PRESet Command

The System Preset command is provided as an alternative to the '*RST' command. This command works just like the '*RST' command, however, it only re-initializes the driver card hardware and does not issue a reset to relays,

The command may be useful in applications where a functionality similar to "*RST" (reset instrument) needs to be performed, without modifying (opening/closing) relays that are already in a given (or "Power on Recall") state.

Note: During the execution of this command, "State-0" will be recalled if Power On Recall is enabled and "Recall State (0) after *RST" command is enabled.

*RST Command

The *RST command resets the instrument to its power-on default state. These settings are shown in **Table 5-1**.

This command does **NOT** change the value of SCPI Operation or Questionable status registers or IEEE-488. 2 status registers, condition registers, or enable registers. This command does **NOT** clear the error message queue, the input command buffer, or the output reply buffer.

Note: During the execution of this command, "State-0" will be recalled if Power On Recall is enabled and "Recall State (0) after *RST" command is enabled.

Table 5-1, Power-On and Reset State

Attribute	Related Command(s)	Reset State

Relay States	`*RCL 0`
	`OPEN`
	`CLOSE`
		The states are recalled from non-volatile memory location 0 (power-up state). As shipped from the factory, these are all in the OPEN position.

Confidence Mode	`CONFIDENCE:STATE`	Off
Path List	`ROUTE:PATH`	Path names stored in non-volatile memory may be automatically recalled at power-up. This is configurable.
Include List	`ROUTE:INCLUDE`	Include Lists may be automatically recalled at power-up. This is configurable.
Exclude List	`ROUTE:EXCLUDE`	Excludes Lists may be automatically recalled at power-up. This is configurable.

*TST? Query

The *TST? query is supported only to be compliant with the IEEE-488.2 standard. The 1257A performs an extensive self-test at every power-up. If the system detects any failures, the condition is indicated directly on the front-panel display. Normally self-test errors are considered to be fatal conditions and result in a system halt. In this condition, communication activity ceases. For this reason, communications only function if self-test passes and therefore a *TST? will always report a value of "0" indicating a successful self-test.

*RCL Command

The *RCL command will recall the relay states from non-volatile memory. The *RCL command may specify a non-volatile memory location from which to recall the instrument state. That is, both of the following formats are accepted:

```
*RCL

*RCL <location>
```

If <location> is specified, it must be in the range 0 to 100. If <location> is not specified, it will default to 100.

Location "0" has a special purpose. This location is used by the 1257A at power-up to set the initial relay states.

The *RCL command recalls the states of all relays in the system. The relay states are stored using the *SAV command.

Note that path names, module names, status registers, include lists, and so on are NOT affected by the *RCL command.

*SAV Command

The *SAV command will prepare to store a given state into non-volatile memory.

The *SAV command accepts one of two formats:

```
*SAV
*SAV <location>
```

If <location> is specified, it must be in the range from 0 through 100. If <location> is not specified, the instrument state will be saved into non-volatile memory location 100.

State 0 is recalled at power-up, only if the "Power On Recall" flag is set.. The 1257A is shipped without any data in state 0. This effectively tells the 1257A to open all relays at power-up. This default may be overwritten by placing all relays in the desired power-up state, and then executing the command:

```
*SAV 0
```

If new relays are configuredin the system after the *SAV 0 command has been executed, the new relays will not be programmed at power-up. In general, whenever relays are added, removed, or reconfigured in the system, the *SAV 0 command should be used to place the relays of the new modules into the desired power-up state.

*WAI Command

The *WAI command is required by the IEEE-488. 2 specification. This command is accepted but has no effect on the 1257A.

SCPI Status Registers

SCPI defines two additional registers beyond those shown in **Figure 5-1**. These are the *Operation Status Register* and the *Questionable Status Register*.

The *Operation Status Register* consists of three logical registers: a condition register, an enable register, and an event register.

Presently, there are no semantic meaning associated with any of the bits in these registers. They are implemented solely for compliance with the IEEE-488.2 specification.

The *Operation Status Enable Register* enables individual bits to pass through to the Operation Status Event Register. The bits of the *Operation Status Enable Register* are ANDed with the bits of the *Operation Status Condition Register*. If both bits are set, then the corresponding bit in the *Operation Status Event Register* is set. For example, if bits 1, 5, and 6 of *the Operation Status Condition Register*

are set, and bits 5 and 6 of *the Operation Status Enable Register* are set, then bits 5 and 6 of the *Operation Status Event Register* will be set.

The *Operation Status Event Register* latches the status information. Once a bit is set in the *Operation Status Event Register*, it remains set until the bit is cleared by reading the register with the STATUS:OPERATION:EVENT? query, or by sending the *CLS command.

When any of the bits of the *Operation Status Event Register* are set, bit 7 of the *Status Byte Register* will be set.

The *Questionable Status Register* is not used by the 1257. When the *Questionable Status Condition Register* or *Questionable Status Event Register* are read, they will return a value of 0. The *Questionable Status Event Register* may be programmed and queried, but will have no effect on the operation of the 1257.

The STATUS:PRESET command is implemented for SCPI compliance only.

The following SCPI command tree shows the syntax of the SCPI STATUS commands:

```
:STATus
    :OPERation
        [:EVENt]?
        :CONDition?
        :ENABle
    :PRESet (Command Only)
    :QUEStionable
        [:EVENt]?
        :CONDition?
        :ENABle
```

Examples of the commands are shown below. To enable the "Wait for Trigger" and "Wait for Arm" bits the following command achieves this:

```
STAT:OPER:ENABLE 96
```

To read the value of the enable register:

```
STAT:OPER:ENABLE?
```

To read the value of the event register (and clear the event register):

```
STAT:OPER?
```

To read the value of the condition register:

```
STAT:OPER:COND?
```

These commands augment the IEEE-488. 2 Common Commands to provide additional status information.

SCPI Parameter Type

The SCPI language defines several different data formats to be used in program messages and response messages.

Numeric Parameters

Commands that require numeric parameters will accept all commonly used decimal representations of numbers including optional signs, decimal points, and scientific notation.

```
ATTENUATE 0.25,(@3(2000))
```

When a real, non-integer value is returned in a reply from the 1257A, exponential notation will be used. The exponential format is:

<Sign if negative> <Digit> . <8 Digits> E <sign> <2 digits>

For example:

1.0000000E+01

Is returned for a value of 10.0, and

-1.0000000E-01

Is returned for a value of -10.0.

Integer values may be sent in the command using decimal, octal, hexadecimal, or binary values. The default base for values is decimal.

To specify a hexadecimal value, use the prefix #H. To specify an octal value, use the prefix "#Q". To specify a binary value, use the prefix "#B". The following values are all equivalent.

```
123              123 decimal
#B1111011        1111011 binary = 123 decimal
#H7B             7B hex = 123 decimal
#Q173            173 octal = 123 decimal
```

When an integer value is returned in a reply from the 1257A, the value will be a decimal number.

Discrete Parameters

Discrete parameters are used to program settings that have a limited number of values. Parameters are NOT case sensitive. As an example of the discrete parameter, the [:ROUTe]:ORDer command is specified as:

```
[:ROUTe]
    :ORDer  { BBM | MBB } IMMediate}}
```

Meaning the parameter must be one of the following:

```
BBM
MBB
IMM
IMMEDIATE
```

Note that, just like command keywords, discrete parameters may be specified using either the long form or the short form.

Whenever a discrete parameter is used, the query form of the command returns the SHORT form of the parameter value, in upper-case characters. That is, the

command may be specified using either "IMMEDIATE" or "IMM", but the query:

```
ROUTE:ORDER?
```

will return the reply

```
IMM
```

NOT

```
IMMEDIATE
```

Boolean Parameters

Boolean parameters represent a single binary condition that is either true or false. The 1257A accepts "OFF" or "0" for a false condition. The 1257A accepts "ON" or "1" for a true condition. The following command uses a boolean parameter:

```
[:ROUTe]
      :CONfidence
            [:STATe]  { OFF | ON | 0 | 1 }
```

The following commands turn Confidence Mode OFF:

```
ROUTE:CONFIDENCE:STATE OFF
ROUTE:CONFIDENCE:STATE 0
```

The following commands turn the Confidence Mode ON:

```
ROUTE:CONFIDENCE:STATE ON
ROUTE:CONFIDENCE:STATE 1
```

When Boolean parameters are queried, the 1257A always replies with a "1", if the state is on, or "0", if the state is off. The keywords "ON" and "OFF" are NOT returned in the reply to ROUTE: CONFIDENCE:STATE?, or any other query.

Confidence Mode

In Confidence Mode, the 1257A monitors the open and close status for each relay that has read-back capability. Each time a relay is opened or closed, those relays which have read-back capability are compared to the programmed state. If all relay states match their expected state, no action is taken. However, if there is a mismatch between the programmed and read-back state for any relay, an event is added to the event queue.

The format of the event message in the event queue will be:

```
Confidence failure on slot X, channel Y
```

Please refer to the section describing the Event System in this chapter to find how to read and clear events in the event queue.

To enable or disable Confidence Mode, use the command:

```
ROUTE:CONFIDENCE:STATE { OFF | ON | 1 | 0 }
```

Since the "ROUTE" and "STATE" keyword are optional, all of the following examples are valid:

```
ROUTE:CONFIDENCE:STATE ON
```

```
CONFIDENCE ON

CONF OFF

ROUT:CONF 1

CONF 0

CONF:STATE 1

CONF:STAT 0
```

To query whether confidence mode is on or off, use the query:

```
ROUTE:CONFIDENCE:STATE?
```

Since the "ROUTE" and "STATE" keyword are optional, all of the following examples are valid:

```
ROUTE:CONFIDENCE:STATE?

CONFIDENCE?

CONF?

ROUT:CONF?
```

ROUTe:VERify?

The VERIFY query compares the programmed state with each relay's read-back state. The VERIFY query can be used to read a set of relays, or all relays in the system.

There are two forms of the VERIFY query.   The first includes a set of channels to compare. The second requests that all channels are compared to their programmed states.

```
ROUTE:VERIFY? <channel list>

ROUTE:VERIFY:ALL?
```

When a channel list is specified, each channel in the channel list has its programmed state compared to the read-back state. For each channel that matches, a '1' is retuned in the reply. If any channel does not match its read-back state, a '0' is returned.

For example, the query

```
VERIFY? (@1(3,4,7))
```

May return the reply

```
1 0 1
```

This reply indicates that the programmed and read-back states for channels 3 and 7 in slot 1 match. It further indicates that the programmed state does not match the read-back state for channel 4 in slot 1.

The query

```
VERIFY:ALL?
```

Will return a single reply, either a '1' or a '0'.   If the reply is '1', then every channel's programmed state matches its read-back state.   If the reply is '0', then

there is at least one channel that does not match.   Each channel that has a mismatch between the programmed and read-back state will generate an event that will be added to the event queue. The event message will be in the format:

```
Verification failed for slot X, channel Y
```

Please refer to the section describing the Event System in this chapter to find how to read and clear events in the event queue.

Event System

The 1257A implements an event queue. Events include such items as power-on and power-off events, confidence mode failures, verify state failures, and so on.

The event queue maintains a queue of up to 50 events.   The queue is stored in nonvolatile memory and recalled when the power is turned on.   Once the queue fills up, no more events will be stored until the event queue is cleared.

The number of events in the event queue can be read by using the query:

```
SYSTEM:EVENT:COUNT?
```

This query will return an integer reply, indicating the number of events presently on the event queue. This will be a number between 0 and 50.

To read a particular event from the event queue, the following query can be used:

```
SYSTEM:EVENT?  <event number>
```

For example, to read event #3, the following query would be used:

```
SYSTEM:EVENT? 3
```

The oldest event is event #1.   More recent events have a higher number.

The format for the reply to this query is:

```
YYYY/MM/DD HH:MM:SS,<event number>,<event message>
```

An example reply is shown below:

```
2014/03/14 16:05:37,3,Verification failed for slot 1,
channel 4
```

A single event may be removed from the queue by using the command:

```
SYSTEM:EVENT:CLEAR <event number>
```

For example

```
SYST:EVENT:CLEAR 7
```

Clears event number 7. After an event is cleared, all higher-numbered (more recent) events are "moved up" into the queue, so that event number 8 moves up to event number 7, event number 9 is moved up to event number 8, and so on.

To remove all events from the event queue, the following command may be used:

```
SYSTEM:EVENT:CLEAR:ALL
```

Because the event queue fills up when 50 events are stored, the following procedure is recommended when using the CONFIDENCE mode or the VERIFY query:

1. After power-up, read the number of events on the event queue with the "SYSTEM:EVENT:COUNT?" query.

2. For each event in the queue, read the event with the "SYSTEM:EVENT?" query.

3. After you have analyzed the events for any system anomalies, clear the event queue with the "SYSTEM:EVENT:CLEAR:ALL" command.

4. When opening and closing relays with the CONFIDENCE mode on, or whenever a VERIFY query indicates a failure, use the "SYSTEM:EVENT:COUNT?" query to determine if any events have been added to the queue since you cleared it.   If there are any events on the queue, read each event to determine the cause of the failed CONFIDENCE mode or VERIFY query.

Power On Recall

To enable/disable the Power On Recall State:

```
POWeron:RECall:STATE {0}
```

where {0} is **0** - false, **1** - true.

To query the Power On Recall State:

```
POWeron:RECall:STATE?
```

Returns: **0** – disabled, **1** - enabled

Command Input Buffer

The 1257A uses a command buffer to store commands sent either through the Ethernet, USB, or GPIB interfaces. The input buffer is 10240 characters in length. Each interface, and each Ethernet connection, maintains its own input queue.

Reply Output Buffer

The 1257A maintains an output buffer for sending replies to commands. This buffer is 10240 characters in length. There is a separate output queue maintained for each interface, including the GPIB, USB, and each Ethernet connection.

Although unlikely, the output buffer could become filled with replies to commands. If the output buffer is filled, the SCPI-defined "QUERY Deadlock" condition will be detected by the 1257A. In this case, the output buffer will be cleared and an error will be added to the error queue. In addition, the query error bit (QYE) of the IEEE-488.2 Standard Event Status Register will be set. The error queue may be read using the "SYST:ERR?" query, while the Standard Event Status Register may be read using the *ESR? query.

When a reply is in the output queue, the message available, or MAV, bit of the status byte is set. The status byte of the 1257A may be read using the GPIB serial poll feature.

A Query Error may also occur if the output queue for the interface contains a reply

and query is received on the same interface. If this occurs, the query error bit (QYE) will be set as above, and an error will be added to the error queue for that interface.

Specifying Channels in Commands

Many 1257A commands use a "channel list" to control the relays and attenuators in the system. A channel list consists of one of more channels. Each channel uses the form:

> (@<slot number> (<channel number>))

The <slot number> refers to the plug-in card in the 1257A. The 1257A supports up to 10 plug-in modules. The <slot number> can be any number from 1 to 10. The valid <slot number>s depend on the number and location of the plug-in modules for each uniquely configured 1257A. Typically, the first plug in module would be installed into slot 1, the second into slot 2, and so on.

Each plug-in module controls up to 3 banks of channels. The <channel number> selects the channel within the plug-in module that is controlled.

The valid <channel number>s are shown in **Table 5-2**.

Table 5-2, Valid Channel Numbers

Bank	Pin out	Channel
A	Relay Drive 0 – 15	1000 – 1015
A	LED 0 – 15	1100 – 1115
A	Read Indicator 0 – 15	1200 – 1215
A	Relay Reset 0 – 3	1300 – 1303
B	Relay Drive 0 – 15	2000 – 2015
B	LED 0 – 15	2100 – 2115
B	Read Indicator 0 – 15	2200 – 2215
B	Relay Reset 0 – 3	2300 – 2303
C	Relay Drive 0 – 15	3000 – 3015
C	LED 0 – 15	3100 – 3115
C	Read Indicator 0 – 15	3200 – 3215
C	Relay Reset 0 – 3	3300 – 3303

This means that when using commands such as OPEN, CLOSE, PATH:DEFINE, and so on, the commands will typically access the channels 1000 to 1015, 2000 to 2015, and 3000 to 3015.

A range of channels for a drawer may be specified by using the syntax:

```
(@<address>(<channel1>:<channel2>))
```

This format indicates that all channels between <channel1> and <channel2> are to be operated. The command:

```
CLOSE (@2(1000:1003))
```

closes channels 1000 through 1003, inclusive, on the adapter board in slot 2.

A range of channels and a list of single channels may be mixed in a command. For example, the command:

```
CLOSE (@2(1000:1003,2012,2015,3000:3002))
```

closes channels 1000, 1001, 1002, 1003, 2012, 2015, 3000, 3001, and 3002 on the adapter board in slot 2.

Closing Relays

The CLOSE command may be used to close relay channels. To say a channel is closed, it means either:

1. The input of the channel is connected to the output (Single-Pole Single Throw).

2. The input of the channel is disconnected from the "normally closed" output and connected to the "normally open" output (Single-Pole Double-Throw).

3. The common input of a mux is connected to the output denoted by the channel number (1PNT)

The syntax for the CLOSE command is:

```
[:ROUTe]
        :CLOSe <channel list>
```

The format for a "<channel list>" is described in the previous paragraphs of this section of the manual.

The CLOSE? command may be used to query the present state of the relays in the system. This command returns a reply of a sequence of "0" and "1", each of which are separated by a single ASCII space character. The value of the reply is "0" if the corresponding relay is opened, or "1" if the corresponding relay is closed.

The reply is one-for-one with the <channel-list>. For example, suppose the query

```
CLOSE? (@1(1000:1015))
```

is sent to the 1257A. Assuming channels 1003 and 1014 are closed and the rest are open, the reply would be:

```
0 0 0 1 0 0 0 0 0 0 0 0 0 0 0 1 0 Command:
CLOSE? (@1(1000))
```

Reply:

```
0
```

Command:

```
CLOSE? (@1(1003. 1010. 1014))
```

Reply:

```
1 0 1
```

Opening Relays

The OPEN command may be used to open channels. To say a channel is open means:

1. The input of the channel is disconnected to the output   (Single-Pole Single Throw)

2. The input of the channel is disconnected from the "normally   open" output and connected to the "normally closed" output   (Single-Pole Double-Throw).

3. The common input of the relay is disconnected from the specified output denoted by the channel (1PNT)

The syntax for the "OPEN" command is:

```
[:ROUTe]
     :OPEN <channel list>
```

The format for a "<channel list>" is described in the previous paragraphs of this section of the manual.

The OPEN? command may be used to query the present state of the channels in the system. This command returns a reply of a sequence of "0" and "1", each of which are separated by a single ASCII space character. The value of the reply is "1" if the corresponding relay is opened, or "0" if the corresponding relay is closed. **Note that this is the opposite state from the** CLOSE? **query**.

Operating Attenuators

The ATTenuate command may be used to set the attenuation value for attenuators that have been configured on the system:

The syntax for the "ATTenuate" command is:

```
     :ATTenuate <level> , <channel list>
```

The < level> value identifies the attenuation level, in dB, to set for each channel in the channel list. The attenuator is selected by the first channel in the set of channels associated with the channel when the attenuator was configured in the system. For more information, consult the "CONFigure" command summary.

The format for a "<channel list>" is described in the previous paragraphs of this section of the manual.

The "ATTenuate?" query may be used to read back the presently programmed attenuation value. The format for this query is

```
     :ATTenuate? <channel list>
```

This query will return the presently programmed attenuation level for each attenuator identified with the <channel list>.

Using Paths

A group of channels may also be assigned a name. When a group of channels is named, it is called a "path". A path may consist not only of elements that must be closed (the closed channel list) but also of elements that must be opened (the open channel list) to complete the path. This may at first seem contradictory to close a path with switching elements that must be opened, but it nevertheless occurs.

A good example which illustrates the concept of an open channel list is a typical 1P2T failsafe microwave switch. Because this type of switch is failsafe, it only requires a single control line. By default, with no power applied, the first throw is always connected to the common. Energizing the control line opens the first throw and closes the connection between the second throw and the common. Both are obviously mutually exclusive.

Defining a path through the second throw of the example switch can be done strictly with a close channel list. The problem occurs when a path must go through the failsafe throw (first throw). To make this path the switch must be in an open or off state. The open channel list readily accomplishes this task.

Defining Path Names

The PATH:DEFINE command may be used to associate a name with one or more relays. The format for this command is:

```
[:ROUTe]
   :PATH
      :DEFine <path name> ,
                <close channel list> ,
                [,<open channel list>]
```

The <path name> is an identifier from 1 to 256 characters in length. The first character of a path name must begin with a character "A" through "Z". The remainder of the path names can be comprised of the characters "A" through "Z", the numbers "0" through "9", and the underscore ("_") character.. A path name may be sent in upper, lower, or mixed case but will always be reported and used as an upper case name.

The <close channel list> and the optional <open channel list> follows the syntax rules described in the "Specifying Relays in Commands" section of this chapter.

The following examples illustrate the use of a path name.

This associates the name path1 with the close channel list encompassingchannels 1006 and 2009 on the adapter board in slot 1 and channel 2000 on the adapter board in slot 2: :

```
PATH:DEF path1,(@1(1006,2009),2(2000))
```

This associates the name signal_to_P177 with the close channel list, channels 2005 and 3005on the adapter board in slot 3:

```
PATH:DEF signal_to_P177,(@31(3005,2005))
```

The following associates the name oscope1 with a close channel list for channels 2000 and 3003 and an open channel list for channel 2015 on the adapter board in slot 1

```
PATH:DEF oscope,(@1(2000,3000)),(@1(2015))
```

Once a path name is defined, it may be used in a CLOSE or OPEN command:

```
OPEN (@path1)

CLOSE (@signal_to_P177)

CLOSE (@path1,signal_to_P177,4(1000.1005))
```

A CLOSE command, for a given path name, will effectively close all channels listed on the close channel list and open those listed on the open channel list.

An OPEN command, for a given path name, will open all channels listed on the close channel list. Channels on the open channel list are not affected when using the OPEN command.

Path names may also be used in defining an "Include List", an "Exclude List", or a "Scan List". The following sections describe each of these concepts.

When using path names with the "Include List", "Exclude List" and "Scan List", the presently defined path is used. That is, if the following sequence of commands is received:

```
PATH:DEF PATH1,(@1(1003,1007))

INCLUDE (@PATH1,1(2015))

PATH:DEF PATH1,(@1(1000,2003))

CLOSE (@1(2015))
```

then the path definition in effect when the INCLUDE command was defined is used. For the example shown then, channels 1003 and channel 1007 for the adapter board in slot 1 are affected; channel 1000 and 2003 are NOT affected.

Removing Path Names

The PATH:DELETE command may be used to delete a single path name from the list of known path names. The PATH:DELETE:ALL command may be used to remove all presently defined path names.

The syntax for these commands is:

```
[:ROUTe]
        :PATH
                :DELete <path name>
                        :ALL
```

The following examples illustrate the use of this command.

Removes the name "path1":

```
ROUTE:PATH path1
```

Removes the name "testit":

```
PATH:DEL testit
```

Removes all path names:

```
PATH:DEL:ALL
```

Reading the Presently Defined Path Names

The PATH:CATALOG? command may be used to read back all of the presently defined path names. The format for this command is:

```
[:ROUTe]
        :PATH
                :CATalog?
```

The reply to this command consists of the presently defined path names, each of which is separated by a comma. Path names are stored internally in upper-case characters by the 1257A. These upper-case names are returned in the reply.

For example, suppose the following commands have been executed:

```
PATH:DEFINE dmm_2_pin1,(@1(1000,1015))
PATH:DEFINE dmm_2_pin2,(@1(2000,3003))
PATH:DEFINE cntr_2_pin1,(@1(2015))
PATH:DEFINE cntr_2_pin2,(@1(3000),2(1003))
```

Then the query:

```
PATH:CAT?
```

returns the reply:

```
DMM_2_PIN1,DMM_2_PIN2,CNTR_2_PIN1,
CNTR_2_PIN2
```

Reading the Channel List for a Path Name

The module address associated with a module name may be read using the PATH:DEFINE? query. The syntax for this command is:

```
[:ROUTe]
        :PATH
                :DEFine? <path name>
```

Using the example in the previous section of this manual, the query:

```
PATH:DEFINE? dmm2_to_pin1
```

returns the reply:

```
(@1(1000),1(1015))
```

Storing Path Names in Non-Volatile Memory

All paths presently defined may be stored in non-volatile memory using the PATH:STORE command.

Recalling Path Names from Non-Volatile Memory

All paths stored in nonvolatile memory may be recalled by using the
PATH:RECALL command.

Enabling/Disabling Automatic Recall of Paths when the 1257A is Powered On

All paths may be recalled at power-on of the 1257A. by using the
PATH:RECALL:AUTO command. The syntax for this command is

```
[:ROUTe]
        :PATH
                :RECall
                        :AUTO { ON | OFF | 1 | 0 }
```

If the state is ON or 1, all paths stored in nonvolatile memory will be recalled and
available for use upon the 1257A power-on. If the state is OFF, no paths will be
defined when the 1257A is powered-on.

The present state of the automatic recall of paths from nonvolatile memory can be
read with the query:

```
PATH:RECALL:AUTO?
```

This will return "1" if the recall is enabled or "0" if the recall is disabled.

Exclude Lists

A set of channels may be grouped together so that when one of the channels in
the set is closed, all other channels in the set are opened. This mutually exclusive
group is called an Exclude List.

Each channel can be on at most one exclude list.

Defining Exclude Lists

The PATH:EXCLUDE command may be used to identify a set of channels on an
exclude list. The format for this command is

```
[:ROUTe]
        :EXCLude <channel list>
```

The <channel list> identifies each channel in the exclude list.

The following example puts channel 1000 through 1003 of the adapter board in
slot 3 into an exclude list:

```
EXCL (@3(1000:1003))
```

The following example puts channel 1015 and 2011 from slot 1 and channels 1011
through 1015 from slot 2 into an exclude list:

```
EXCL (@1(1015,2011),2(1011:1015))
```

Removing Exclude Lists

The EXCLUDE:CLEAR command may be used remove the exclude lists for specific channels. The EXCLUDE:CLEAR:ALL command may be clear all defined exclude lists.

The syntax for these commands is:

```
[:ROUTe]
      :EXCLude
            :CLEar <channel list>
                  :ALL
```

The following examples illustrate the use of these commands.

Removes any exclude list for channel 1000 in slot 1:

```
EXCL:DEL (@1(1000))
```

Removes the exclude lists for all channels 3000 through 3015 for the adapter board in slot 6:

```
EXCL:DEL (@6(3000:3015))
```

Removes all exclude lists:

```
EXCL:DEL:ALL
```

Reading the Presently Defined Exclude List for a Channel

The EXCLUDE? command may be used to read back the channels that are in an exclude list with a one or more channels: The format for this query is:

```
[:ROUTe]
      :EXCLude? <channel list>
```

The reply to this command consists of a channel list or the keyword NONE for each channel in the <channel list>. If you specify one channel in the channel list, you will receive a single channel list (or the keyword NONE) in the reply. If you specify two channels in the channel list, you will receive two channel lists, separated by a comma, in the reply.

For example, suppose the following commands have been executed previously

```
EXCL:CLEAR:ALL
EXCL (@3(1000:1003))
EXCL (@1(1015,2011),2(1011:1013))
```

The query:

```
EXCL? (@3(1002))
```

returns the reply:

```
(@3(1000),3(1001),3(1002),3(1003))
```

The query

```
EXCL? (@2(1012))
```

returns the reply:

```
(@1(1015),1(2011),2(1011),2(1012),2(1013))
```

The query

```
EXCL? (@3(1000,1015))
```

returns the reply:

```
(@3(1000),3(1001),3(1002),3(1003)),NONE
```

Storing Exclude Lists to Non-Volatile Memory

All presently defined exclude lists may be stored to nonvolatile memory by using the EXCLUDE:STORE command.

Recalling Exclude Lists from Non-Volatile Memory

All exclude lists stored in nonvolatile memory may be recalled by using the EXCLUDE:RECALL command.

Enabling/Disabling Automatic Recall of Exclude Lists when the 1257A is Powered On

All exclude lists may be recalled at power-on of the 1257A. by using the EXCLUDE:RECALL:AUTO command. The syntax for this command is

```
[:ROUTe]
        :EXCLude
            :RECall
                :AUTO { ON | OFF | 1 | 0 }
```

If the state is ON or 1, all exclude lists stored in nonvolatile memory will be recalled and available for use upon the 1257A power-on. If the state is OFF, no paths will be defined when the 1257A is powered-on.

The present state of the automatic recall of exclude lists from nonvolatile memory can be read with the query:

```
EXCLUDE:RECALL:AUTO?
```

This will return "1" if the recall is enabled or "0" if the recall is disabled.

Include Lists

A set of channels may be grouped together so that when one of the channels in the set is closed, all other channels in the set are also closed, and when any of the channels in the set are opened, all other channels in the set are opened. This grouping is called an Include List.

Each channel can be on at most one include list.

Defining Include Lists

The PATH:INCLUDE command may be used to identify a set of channels on an include list. The format for this command is

```
[:ROUTe]
     :INCLude <channel list>
```

The <channel list> identifies each channel in the include list.

The following example puts channel 2000 through 2003 of the adapter board in slot 1 into an exclude list:

```
INCL (@2(2000:2003))
```

The following example puts channel 1015 and 2011 from slot 1 and channels 1011 through 1015 from slot 2 into an include list:

```
INCL (@1(1015,2011),2(1011:1015))
```

Removing Include Lists

The INCLUDE:CLEAR command may be used remove the include lists for specific channels. The INCLUDE:CLEAR:ALL command may be clear all defined include lists.

The syntax for these commands is:

```
[:ROUTe]
        :INCLude
             :CLEar <channel list>
                    :ALL
```

The following examples illustrate the use of these commands.

Removes any include list for channel 1000 in slot 1:

```
INCL:DEL (@1(1000))
```

Removes the include lists for all channels 3000 through 3015 for the adapter board in slot 6:

```
INCL:DEL (@6(3000:3015))
```

Removes all exclude lists:

```
INCL:DEL:ALL
```

Reading the Presently Defined Include List for a Channel

The INCLUDE? query may be used to read back the channels that are in an include list with a one or more channels: The format for this query is:

```
[:ROUTe]
        :INCLude? <channel list>
```

The reply to this command consists of a channel list or the keyword NONE for each channel in the <channel list>. If you specify one channel in the channel list,

you will receive a single channel list (or the keyword NONE) in the reply. If you specify two channels in the channel list, you will receive two channel lists, separated by a comma, in the reply.

For example, suppose the following commands have been executed previously

```
INCL:CLEAR:ALL
INCL (@3(1000:1003))
INCL (@1(1015,2011),2(1011:1013))
```

The query:

```
INCL? (@3(1002))
```

returns the reply:

```
(@3(1000),3(1001),3(1002),3(1003))
```

The query

```
INCL? (@2(1012))
```

returns the reply:

```
(@1(1015),1(2011),2(1011),2(1012),2(1013))
```

The query

```
INCL? (@3(1014,1000))
```

returns the reply:

```
NONE,(@3(1000),3(1001),3(1002),3(1003))
```

Storing Include Lists to Non-Volatile memory

All presently defined include lists may be stored to nonvolatile memory by using the INCLUDE:STORE command.

Recalling Include Lists from Non-Volatile Memory

All include lists stored in nonvolatile memory may be recalled by using the INCLUDE:RECALL command.

Enabling/Disabling Automatic Recall of Include Lists When the 1257A is Powered On

All include lists may be recalled at power-on of the 1257A. by using the INCLUDE:RECALL:AUTO command. The syntax for this command is

```
[:ROUTe]
      :INCLude
            :RECall
                  :AUTO { ON | OFF | 1 | 0 }
```

If the state is ON or 1, all include lists stored in nonvolatile memory will be recalled and available for use upon the 1257A power-on. If the state is OFF, no include

lists will be defined when the 1257A is powered-on.

The present state of the automatic recall of include lists from nonvolatile memory can be read with the query:

```
INCLUDE:RECALL:AUTO?
```

This will return "1" if the recall is enabled or "0" if the recall is disabled.

Using Aliases

The 1257A supports an alias feature. An alias is a string that can be associated with a single 1257A channel. An alias may also be associated with a relay component, an attenuator component, or a digital I/O bank.

An alias mainly serves to associate names that are displayed on web pages. An alias for a single channel will be displayed along with the physical channel on the web page. An alias for a component will be displayed along with the overall component on the web page.

The format of the command to associate an alias with a channel is:

```
[:ROUTe]
        :ALIAS  <alias>,<channel list>
```

The <alias> is a name enclosed in quotes (single or double). The rules for a valid alias name are the same as for a valid path name.

As an example, the command

```
ALIAS ''P7'',(@2(1008))
```

Associates the alias name "P7" with the channel 1008 in slot 2. This would now appear along with the channel the next time the web page is displayed.

Note that since the alias name is within quotes, it can be specified in upper, lower, or mixed cases and the case will be maintained. Also, the empty string ("") can be specified for the <alias name> to clear the current alias.

The presently defined alias name can be queried with the "ALIAS?" query. For example, the query:

```
ALIAS? (@2(1008))
```

Will return the reply

```
''p7''
```

Note that the double quotes are included in the reply to allow lower-case characters to be returned.

The command

```
[:ROUTe]
        ALIAS
                :DELete <channel list>
```

can be used to delete the alias names from all channels on the <channel list>.

In addition to individual channels, a relay, attenuator, or digital I/O component can also be assigned an alias. The component alias will be displayed on the system

web page the next time the web page is displayed. The command:

```
[:ROUTe]
        :ALIAS
                :DATA <slot>,<component>,<alias>
```

Is used to assign an alias name to a component. The <slot> is an integer in the range 1 through 10 that selects the slot of the component. The <component> is an integer that was assigned to the component with the "DRIVERBOARD:CONFIGURE:RELAY:ADD" or "DRIVERBOARD:CONFIGURE:ATTENUATOR:ADD" command. To see the list of components for a slot, the "DRIVERBOARD:CONFIGURE:RELAY:LIST?" query and the "DRIVERBOARD:CONFIGURE:ATTENUATOR:LIST?" query.

For example, if component 6 on slot 2 was a 1P6T relay, the command:

```
ALIAS:DATA 2,6,''SW1''
```

Could be used to associate the alias "SW1" with the 1P6T relay. The next time the system web page was displayed, the alias would appear along with the 1P6T relay. As with the assignment of an alias to an individual channel, the empty string ("") can be assigned to the component to clear the alias data.

The presently assigned alias for a component can be read back with the command:

```
[:ROUTe]
        :ALIAS
                :DATA? <slot> , <component>
```

From the previous example, the query

```
ALIAS:DATA? 2,6
```

Would return

```
''SW1''
```

Alias names are stored to nonvolatile memory and recalled at power-up automatically.

Using Aliases to Emulate a Model 1256 and 1260-115 Relay Driver Combination

The ALIAS command can also be used to emulate a Racal Instruments 1256 and 1260-115 Relay Driver in conjunction with CLOSE and OPEN commands.

An alias name that uses the form "<slot>(<channel>)" can be used to map a 1260-115 channel to a 1257A channel. So, for example, the command:

```
ALIAS ''6(103)'',(@2(1008))
```

Tells the 1257A to map the channel 1256 channel (@6(103)) to the 1257A channel (@2(1008)). The next time a CLOSE or OPEN command is executed, and channel 103 in slot 6 is specified, the 1257A will actually operate channel 1008 in slot 2. Whenever a channel is detected in a SCPI command, the 1257A will look to see if there is an alias that matches the slot and channel. If one is found, the alias will be used to map to a 1257A channel. This means that if you assign an alias name to any channel that matches a (different) valid 1257A channel, you will

no longer be able to operate the 1257A channel directly. You would have to create a second alias that maps to the underlying 1257A channel you wish to operate in order to access it in CLOSE and OPEN commands.

Checking for Installed Adapter Boards

The DRIVerboard:ADAPter:ID? command may be used to query the 1257A for which types of adapter boards are installed in each bank of a slot with a driver board. Each driver board can support up to 3 plug in adapter boards.

```
:DRIVerboard
    :ADAPterboard
        :ID? <slot>,<bank>
```

The <bank> value ranges from 0 to 2. The query returns the type of adapter board installed in the card. The adapter board identifiers are:

  1 : The adapter supports 2 SP6T and 1 SP4T or 2 SP4T and 1 SP6T

  2 : The adapter supports 8 SPDT or 8 DPDT or 8 DP3T or 16 SPST

  3 : The adapter supports 8 SPDT or 8 DPDT or 8 DP3T or 16 SPST

  4 : 2 x SP8T

  5 : 1 x SP10T

  6 : SPDT, DPDT, SP4T, SP6T

  13 : 2 x (3-step or 4-step attenuator) or 2 x 8-step attenuator

  14 : 4 x (3-step or 4-step attenuator) or 3 x (3-step or 4-step or 5-step attenuator)

  15 : There is no adapter board installed in the bank

Note: If there is no driver board installed in the slot, this command will not return a reply and an error indicating that no module is installed in the specified slot will be added to the SCPI error queue.

System Commands

The 1257A instrument supports several system commands. These commands generally access errors, SCPI standard information, as well as user-interface issues. Additionally, the 1257A uses a special command to update non-volatile memory.

Reading Error Messages

Whenever an error is encountered by the 1257, it will perform two actions:

1. One of the bits of the Standard Event Status Register will be   set. This register may be read using the *ESR? query

2. An error message will be added to the error message queue.   The error message queue may be read using the   SYSTEM:ERROR? query (or SYST:ERR?).

Consult Appendix B for a list of error messages and more detailed explanations.

The error queue holds up to 15 error messages. Each time an error is detected by the 1257A, it adds a new error to the error queue. Each time the SYSTEM:ERROR? query is received, the oldest (least recent) error message is returned.

The syntax for this command is shown below:

```
:SYSTem
    :ERRor?
```

The reply to the SYSTEM:ERROR? query uses the format:

```
<error code> , ''<error message>''
```

where:

    <error code>   This parameter is a numeric value. This value is "0" if there are no errors remaining in the error queue. This value is negative when an error exists on the error queue.

    <error message>   This parameter is a string enclosed in double quotes. The error message provides some additional information about the error.

For example, the reply:

```
0,''No error''
```

indicates that no errors remain on the error queue, while the reply:

```
-102,''Syntax error ; missing @ sign''
```

indicates that a syntax error was detected in a previous command to the 1257. The reply:

```
-350,''Queue overflow''
```

is returned to indicate that all 15 places in the queue have been occupied and that the error queue is full.

SCPI Version Information

The SYSTEM:VERSION? query provides information about which SCPI standard the 1257A references and is required for compliance with SCPI.

The syntax for this command is shown below:

```
:SYSTem
    :VERSion?
```

For example, the query:

```
SYST:VERS?
```

always returns the following:

```
1994.0
```

Configuration Commands

The 1257A switch system is typically configured at the Astronics Test Systems facility. However, there are several configuration commands that allow new relays and attenuators to be added to the system. This section of the manual describes how to add new relays and attenuators to a 1257A system.

Specifying New Relay Types

The "PARAMETERS:RELAY:ADD" command is used to add new types of relays in the system. This command associates an integer number with a set of attributes for the relay.

The syntax for this command is shown below:

```
:PARameters
     :RELay
          :ADD <parameters>
```

The <parameters> are defined below, in the order they appear.

Each parameter is separated from the next by a comma.

<relay id> : This is the first parameter for the command. This is an integer from 1 to 32767. This value must not be in use for other relay types already. You can see if a value is already in use by sending the "PARAMETERS:RELAY? <relay id>" query. For example to see if relay id #100 is available, send the query:

```
PARAMETERS:RELAY? 100
```

 If the response is not returned, the <relay id> is not in use.

Alternatively, you can use the "PARAMETERS:RELAY:LIST?" query to return a list of <relay id> that are currently defined.

<manufacturer> : This is the second parameter and must be enclosed in quotes (single or double). Examples include "Agilent", "Radiall", and so on. This is effectively a comment and have no specific use other than for reporting in response to a query.

<model> : This is the third parameter and must be enclosed in quotes (single or double). Examples include "8762A", "87106A", and so on. This is effectively a comment and have no specific use other than for reporting in response to a query.

<mechanical type> : this is the fourth parameter and is an integer with one of the following values:

> 0 = latching

> 1 = non-latching

<relay drive type> : this is the fifth parameter and is an integer with one of the following values:

> 0 = TTL

> 1 = Open collector

<number of throws> : this is the sixth parameter and is an integer that identifies the number of throws (1 to 16 maximum)

<number of poles> : this is the seventh parameter and is an integer that identifies the number of poles (1 or 2)

<warn threshold> : this is the eighth parameter and is an integer value in the range 1 to 2000000000. This identifies the number of relay closures that can occur before a warning is generated.

<EOL threshold> : this is the ninth parameter and is an integer value in the range 1 to 2000000000. This identifies the number of relay closures that can occur before the relay must be replaced (end of life).

<Reset Type> : This is the tenth parameter and has a value of 0 for "No Reset", 1 for "Global Channel Reset" and 2 for "Global and Individual Channel Reset". If the value is 0, the relay does not have a reset input. If the value is 1, the relay has a global reset input that can be controlled with one of the global reset channels in the 1257A. If the value is 2, the relay has both - a global reset input that can be controlled with one of the global reset channels in the 1257A and an individual reset channel for each drive channel of the relay.

<Reset Time> : This is the eleventh parameter and is an integer value that identifies the number of milliseconds that the reset channel must be pulsed to reset the relay.

<Strobe Time> : This is the twelfth parameter and is an integer value that identifies the minimum strobe time that a channel must be pulsed for actuating (a latched) relay channel.

<Settlilng Time> : This is the thirteenth parameter and is an integer value that identifies the relay settling time required for the worst case change in position from one throw to another.

<Has Read-back> : This is the fourteenth parameter and has a value of 0 to indicate that the relay does NOT support position read-back or 1 to indicate that the relay does support position read-back.

<Read-back Polarity> : This is the fifteenth parameter and has a value of 0 indicates the read-back is active low and a value of 1 indicates the read-back is active high.

<Is Transfer Switch> : this is the sixteenth parameter and has a value of 0 to indicate that the switch is not a transfer switch and a value of 1 to indicate that the switch is a transfer switch. In general, this should only be 1 when the relay has 2 poles and 2 throws.

<Is Miniature Switch> : this is the seventeenth parameter and has a value of 0 to indicate that the switch is a normal-sized switch or a value of 1 to indicate that the switch is a miniature switch.

<Supports Non-Blocking Mode> : this is the eighteenth and last parameter. This has a value of 0 to indicate that the relay is blocking, meaning at most one throw can be connected to the common input at any one time. This has a value of 1 to indicate that non-blocking is supported by the relay so that multiple throws can be connected to the COM input simultaneously.

An example of a command would be:

```
PARAMETERS:RELAY:ADD 22,"Agilent","86104A",
     0, 0, 4, 1, 1999000000, 2000000000, 0,
     30, 30, 50, 0, 0, 0, 0, 1
```

The example associates relay id # 22 with an Agilent 86104A relay that has 4 throws, 1 pole, is latching, has TTL control levels, has a warning threshold of 1999000000 and an end-of-life threshold of 2000000000, with no reset connection,

and a 30 millisecond strobe time and 50 millisecond relay settling time. The relay is NOT a transfer switch, is a normal-sized switch, and supports non-blocking mode.

The parameters associated with a relay can be read back with the "PARAMETERS:RELAY? <relay id>" query.   This query will return 17 parameters. Each parameter will be separated by a comma from the next. The parameters are returned in the same order as they are specified in the "PARAMETERS:RELAY:ADD" command. The only difference is the <relay id> is NOT returned.

For example, the query

```
PARAMETERS:RELAY? 22
```

Would return

```
''Agilent'',''86104A'',
0, 0, 4, 1, 1999000000, 2000000000, 0, 30, 30, 50, 0, 0, 0, 0, 1
```

Deleting Relay Types

Caution

This command is documented for completeness. Under most circumstances, this command should NOT be sent to the 1257A as it will delete the specified relay component from the library of known components. If you delete a relay type that is used in your system, you will not be able to continue to use those relays anymore.

The "PARAMETERS:RELAY:DELETE" command is used to delete one specific relay type or all relay types from the list of known relay types.

```
:PARameters
      :RELay
            :DELete { <relay id> | ALL }
```

The following command deletes the relay information associated with relay id # 22:

```
PARAMETERS:RELAY:DELETE 22
```

The following command deletes all relay information:

```
PARAMETERS:RELAY:DELETE  ALL
```

Configuring New Relays into the System

Once the library of relay types is complete, the 1257A can be configured to control the relays installed in the system. The DRIVERBOARD:CONFIGURE:RELAY command is used to "install" a single relay into the system and to configure the control and read-back channels. The format of the command is:

```
:DRIVerboard
      :CONFigure
            :RELay
```

```
:ADD <parameters>
```

The number of <parameters> is variable. The number of parameters is determined by the number of throws in relay. The number of parameters is always 6 + <number of throws>.

The <parameters> for this command are:

<slot> : This is the first parameter for the command. This is the slot of the driver card that controls the channels for this relay. This is an integer in the range 1 to 10.

<relay number> : This is the second parameter for the command. This is a unique relay number that uniquely identifies the relay in the system. This relay number cannot already be in use from a previous DRIVERBOARD:CONFIGURE:RELAY:ADD command. This parameter is an integer number with a minimum value of 1.

<relay id> : This is the third parameter in the command. This selects the relay from the by using an integer relay id that was specified in a previous PARAMETERS:RELAY:ADD command.

<non blocking mode> : This is the fourth parameter in the command. This has a value of 0 to indicate the relay should be blocking and prevent more than one throw from being connected to the common input at one time. This should normally be 0.   A value of 1 indicates that the non-blocking mode is active and the common input can be connected to two or more throws simultaneously. This can have a value of 1 only if the relay selected by <relay id> supports non-blocking mode.

<reset channel> : This is the fifth parameter in the command. This is channel number associated with the reset line of the relay (if the relay has a reset line). This can be a value of 0 if there is no reset line for the relay. Typically reset channels will be 1300 – 1303, 2300 – 2303, or 3300 - 3303 (or 0 if not used).

<number of drive channels> : This is the sixth parameter in the command. This is the number of drive channels that control the individual throws of the relay. This has a minimum value of 1 and a maximum value of 16.

<drive channel 1> : This is the seventh parameter in the command. This identifies the channel number of the 1257A that controls the first throw of the relay. Channel numbers are in the range 1000 to 1015, 2000 to 2015, and 3000 to 3015.

<drive channel 2> : This is the eighth parameter in the command. This is the first optional parameter and should not be sent if the number of throws for the relay is 1. This identifies the 1257A channel that controls the second throw of the relay.

<drive channel N> : This is the (6 + <number of throws>) parameter. It identifies the channel number of the 1257A that controls the Nth throw of the relay.

[Optional] <drive reset channel N> : This is the (6 + <drive channel N> + <number of throws>) [Optional] parameter. It identifies the channel number of the 1257A that controls the Nth throw "reset" for the relay. Parameter is (required) only when the <relay id> parameter has its "Relay Type" configured as "Global and Individual Channel Reset".

The following example shows a sample use of the command:

```
DRIVERBOARD:CONF:RELAY:ADD 3,32,22,0,2302,6,
    2010,2011,2012,2013,2014,2015
```

The example shows the addition of a relay into slot 3. The unique relay number is 32. The relay id associated with this relay is 22, which from the previous examples was an "Agilent 86104A". The relay is being configured for blocking mode so that at most one throw can be connected at one time. The reset channel is 2302. There are 6 throws in the relay. The channels to controls throws 1 through 6 are 2010, 2011, 2012, 2013, 2014, and 2015 respectively.

Deleting Configured Relays

Once a relay has been added, it can be removed through the "DRIVERBOARD:CONFIGURE:RELAY:DELETE" command. This is not typically required but may be useful in changing the type of relay installed in the system. The syntax for this command is

```
:DRIVerboard
    :CONFigure
        :RELay
            :DELete  <slot> , <relay #>
```

The following command deletes relay number 32 from slot 3.

```
DRIVERBOARD:CONFIGURE:RELAY:DELETE 3,32
```

Reading the Relays that are Configured

The list of relay numbers that have been configured for a slot can be read back with the "DRIVERBOARD:CONFIGURE:RELAY:LIST?" query. The syntax for this query is

```
:DRIVerboard
    :CONFigure
        :RELay
            :LIST?  <slot>
```

The query returns a list of the unique relay numbers associated with the specified slot.

The following example shows the request for the list of relays configured for slot 1:

```
DRIVERBOARD:CONFIGURE:RELAY:LIST? 1
```

A typical reply would be:

```
16,19,32,36,37
```

Indicating the unique relay numbers 16, 19, 32, 36, and 37 have been configured for slot 1.

Once the unique relay numbers are known, the "DRIVERBOARD:CONFIGURE:RELAY?" query can be used to retrieve the information about a single relay. The format for this query is:

```
:DRIVerboard
```

```
:CONFigure

    :RELay? <slot> , <relay #>
```

As an example, the following query requests the information for relay number 32 that has been configured for slot 1:

```
:DRIVERBOARD:CONF:RELAY? 1,32
```

The reply consists of the following information:

<relay id> , <non-blocking operation>,<reset channel>,

<number of throws> , <throw 1 channel> , ... ,

<throw N channel> , [<throw 1 channel reset> , ... , <throw N channel reset>]

Note that the number of items in the reply = 4 + <number of throws> + [<number of reset throws>]

Note that optional parameter(s) above, [<number of reset throws>] are returned only when the <relay id> parameter has its "Relay Type" configured as "Global and Individual Channel Reset".

So a typical reply might be:

```
4, 0, 2302, 6, 2010, 2011, 2012, 2013, 2014, 2015
```

Specifying New Attenuator Types

The "PARAMETERS:ATTENUATOR:ADD" command is used to add new types of attenuators in the system. This command associates an integer number with a set of attributes for the attenuator.

The syntax for this command is shown below:

```
:PARameters

    :ATTenuator

        :ADD <parameters>
```

The <parameters> are defined below, in the order they appear.

Each parameter is separated from the next by a comma.

There is a minimum of 13 parameters and a maximum of 28 parameters for this command. The number of parameters = 12 + <number of cells> in the attenuator.

<attenuator id> : This is the first parameter for the command. This is an integer from 1 to 32767. This value must not be in use for other attenuators already. You can see if a value is already in use by sending the "PARAMETERS:ATTENUATOR? <attenuator id>" query. For example to see if relay id #100 is available, send the query:

```
PARAMETERS:ATTENUATOR? 77
```

 If the response is not returned, the <attenuator id> is not in use.

Alternatively, you can use the "PARAMETERS:ATTENUATOR:LIST?" query to return a list of <attenuator id> that are currently defined.

<manufacturer> : This is the second parameter and must be enclosed in quotes

(single or double). Examples include "Aeroflex", "Agilent", and so on. This is effectively a comment and have no specific use other than for reporting in response to a query.

<model> : This is the third parameter and must be enclosed in quotes (single or double). Examples include "8494G", "8496H", and so on. This is effectively a comment and have no specific use other than for reporting in response to a query.

<attenuator type> : this is the fourth parameter and is an integer with one of the following values:

       0 = Latching attenuator

       1 = Fail-safe attenuator

<attenuator drive type> : this is the fifth parameter and is an integer with one of the following values:

       0 = TTL

       1 = Open collector

<Strobe Time> : This is the sixth parameter and is an integer value that identifies the minimum strobe time, in milliseconds, that the channel must be strobed to actuate.

<Has Reset> : this is the seventh parameter and has a value of 0 for FALSE or 1 for TRUE. If the value is 0, the attenuator does not have a reset input. If the value is 1, the attenuator has a reset input that can be controlled with one of the channels in the 1257A.

<Has Read-back> : This is the eighth parameter and has a value of 0 to indicate that the attenuator does NOT support position read-back or 1 to indicate that the attenuator does support position read-back.

<Read-back Polarity> : This is the ninth parameter and has a value of 0 indicates the read-back is active low and a value of 1 indicates the read-back is active high.

<Warn threshold> : this is the tenth parameter and is an integer value in the range 1 to 2000000000. This identifies the number of attenuator (relay) closures that can occur before a warning is generated.

<EOL threshold> : this is the eleventh parameter and is an integer value in the range 1 to 2000000000. This identifies the number of attenuator (relay) closures that can occur before the attenuator must be replaced (end of life).

<attenuator pin count> : this is the twelfth parameter and is an integer that identifies the number of pins or cells that are used by the attenuator. This has a minimum value of 1 and a maximum value of 16. The number of parameters that follow must equal this value.

<attenuator pin level 1> : this is the thirteenth and last required parameter. This is an integer value and represents the attenuation value in dB for the first pin (cell) in the attenuator.

<attenuator pin level 2> : this is an optional parameter and must be present if the <attenuator pin count> is >= 2. This is an integer value and represents the attenuation value in dB for the second pin (cell) in the attenuator.

<attenuator pin level 3> : this is an optional parameter and must be present if the

<attenuator pin count> is >= 3. This is an integer value and represents the attenuation value in dB for the third pin (cell) in the attenuator.

<attenuator pin level 4> : this is an optional parameter and must be present if the <attenuator pin count> is >= 4. This is an integer value and represents the attenuation value in dB for the fourth pin (cell) in the attenuator.

<attenuator pin level 16> : this is an optional parameter and must be present if the <attenuator pin count> is = 16. This is an integer value and represents the attenuation value in dB for the sixteenth pin (cell) in the attenuator.

An example of a command would be:

```
PARAMETERS:ATTENUATOR:ADD 37,
     'Agilent','8494G',
     0, 0, 30, 0, 0, 0,
     4999000, 5000000,
     4, 1, 2, 4, 8
```

The example associates attenuator id # 37 with an Agilent 8494G attenuator is latching, has TTL control levels, has a strobe time of 30 milliseconds, has no reset or read-back, has a warning threshold of 4999000 and an end-of-life threshold of 5000000. The attenuator consists of 4 cells with attenuation values of 1 dB, 2 dB, 4 dB, and 8 dB.

The parameters associated with a relay can be read back with the "PARAMETERS:ATTENUATOR? <attenuator id>" query.   This query will return 17 parameters. Each parameter will be separated by a comma from the next. The parameters are returned in the same order as they are specified in the "PARAMETERS:ATTENUATOR:ADD" command. The only difference is the <attenuator id> is NOT returned.

For example, the query

```
PARAMETERS:ATTENUATOR? 37
```

Would return

```
"Agilent","8494G",0,0,30,0,0,0,4999000,
5000000,4,1,2,4,8
```

Deleting Attenuator Types

Caution

This command is documented for completeness.   Under most circumstances, this command should NOT be sent to the 1257A as it will delete the specified attenuator component from the library of known components. If you delete an attenuator type that is used in your system, you will not be able to continue to use those attenuators anymore.

The "PARAMETERS:ATTENUATOR:DELETE" command is used to delete one specific relay type or all relay types from the list of known relay types.

:PARameters

:ATTenuator

:DELete { <attenuator id> | ALL }

The following command deletes the attenuator information associated with attenuator id # 37:

```
PARAMETERS:ATTENUATOR:DELETE 37
```

The following command deletes all attenuator information:

```
PAR:ATT:DEL  ALL
```

Configuring New Attenuators into the System

Once the library of attenuator types is complete, the 1257A can be configured to control the attenuators installed in the system. The DRIVERBOARD:CONFIGURE:ATTENUATOR command is used to "install" a single relay into the system and to configure the control and read-back channels. The format of the command is:

```
:DRIVerboard

    :CONFigure

        :ATTenuator

            :ADD <parameters>
```

The number of <parameters> is variable. The number of parameters is determined by the number of cells in the attenuator. The number of parameters is always 4 + (2 * <number of cells>).

The <parameters> for this command are:

<channel list> : This is the first parameter for the command. Each channel in the channel list is the FIRST 1257A channel that controls an attenuator of the specified type.

<attenuator id> : This is the second parameter in the command. This selects the attenuator from the by using an integer attenuator id that was specified in a previous PARAMETERS:ATTENUATOR:ADD command.

<global reset channel> : This is the 1257A channel that controls the global reset for the attenuator. Typically reset channels will be 1300 – 1303, 2300 – 2303, or 3300 - 3303 (or 0 if not used).

<cell count> : This is the fourth parameter in the command. This specifies the number of cells in the attenuator. This must match the number of pins (cells) that was configured for the attenuator with <attenuator id> when the "PARAMETERS:ATTENUATOR:ADD" command was used.

<start channel 1> : This is the fifth parameter in the command. This identifies the starting channel number of the 1257A that controls the first attenuator specified in the <channel list>.   Channel numbers are in the range 1000 to 1015, 2000 to 2015, and 3000 to 3015.

<through channel 1> : This is the sixth parameter in the command. This identifies the end of the range of channels in the 1257A channel that controls the first

attenuator identified by the <channel list>.

<start channel N> : This is the (3 + (<number of cells> * 2)) parameter. It identifies the starting 1257A channel that controls the Nth attenuator specified in the <channel list>.

<through channel N> : This is the (4 + (<number of cells> * 2)) parameter in the command. This identifies the end of the range of channels in the 1257A channel that controls the Nth attenuator identified by the <channel list>.

The following example shows a sample use of the command:

```
DRIVERBOARD:CONF:RELAY:ADD (@2(1008)),
        37,1202,4,1008,1011
```

The example shows the addition of an attenuator into slot 2, channel 1008. The attenuator type (attenuator id) associated with this attenuator is 37, which from the previous examples was an "Agilent 8494G". The relay has reset channel 1202. There are 4 pins to control the attenuator, these are channels 1008 through 1011 in slot 2.

Deleting Configured Attenuators

Once an attenuator has been added, it can be removed through the "DRIVERBOARD:CONFIGURE:ATTENUATOR:DELETE" command. This is not typically required but may be useful in changing the type of attenuators installed in the system. The syntax for this command is

```
:DRIVerboard
     :CONFigure
          :ATTenuator
               :DELete  <channel list>
```

The following command deletes attenuator from slot 2, channel 1008.

```
DRIV:CONF:ATT:DEL (@2(1008))
```

Reading the Relays that are Configured

The list of relay numbers that have been configured for a slot can be read back with the "DRIVERBOARD:CONFIGURE:ATTENUATOR:LIST?" query. The syntax for this query is

```
:DRIVerboard
     :CONFigure
          :ATTenuator
               :LIST?  <slot>
```

The query returns a list of the unique relay numbers associated with the specified slot.

The following example shows the request for the list of relays configured for slot 2:

```
DRIVERBOARD:CONFIGURE:RELAY:LIST? 2
```

A typical reply would be:

```
1008
```

Indicating the attenuator has been configured and the first control channel is 1008 (of slot 2). If no attenuators have been configured for the specified slot, the reply will be

```
NONE
```

Chapter 6
SCPI Command Reference

This section contains a detailed description of each command that is either specific to the 1257A or common to multiple switch modules. The commands are presented in **Table 6-1**.

The commands accepted by the 1257A are shown using the SCPI syntax notation. The command syntax in **Table 6-1** is displayed using the following conventions:

Square Brackets ([ ])	Enclose optional keywords or parameters
Braces ({ })	Enclose possible parameters within a command
Triangle Brackets (< >)	Substitute a value for the enclosed parameter
Vertical Bar (|)	Separate multiple parameter choices
Bold Typeface Letters	Designate factory default values
(Command Only)	Indicates the command cannot be used in query form
(Query Only)	Indicates the command can only be used as a query. The command form, without the question mark is not allowed.

To illustrate the SCPI notation, a part of the ROUTE command subsystem is shown below:

[:ROUTe]

      :CLOSe <channel list>

      :OPEN <channel list>

         :ALL   (Command Only)

This example shows the following:

The ROUTE command keyword is optional, since it is enclosed in square braces

The ROUTE command keyword may be specified as ROUTE (long form) or as ROUT (short form), since the last character is displayed in lower-case

The CLOSE keyword must be followed by a <channel list>. A <channel list> represents any of a number of possible channels in a variety of formats. The format for the <channel list> and other command parameters are described in **Chapter 5: SCPI Command Basics**.

Since the ROUTE keyword is optional, and the CLOSE keyword is shown indented from the ROUTE keyword, valid commands consist of:

```
ROUTE:CLOSE
        CLOSE
```

The command ROUTE:OPEN:ALL is a command only. Therefore, the command ROUTE:OPEN:ALL? is not allowed. Since they are NOT shown as command-only, the commands ROUTE:CLOSE? and ROUTE:OPEN? are valid commands.

Table 6-1 contains a synopsis of the commands implemented by the 1257A. The maximum and minimum values and resolution are shown for numeric parameters. The commands marked with an asterisk (*) are NOT defined in the SCPI language. These commands follow the SCPI syntax rules for implementing the operation of the command.

Table 6-1, Commands Implemented by the 1257A

Command	Max	Min	Default	Notes
ROUTE Subsystem:				
[:ROUTe]				
:CLOSe <channel list>				
:CLOSe?				
:OPEN <channel list>				
:OPEN?				
:ALL (Command Only)				
:PATH				
:CATalog? (Query Only)				
:DEFine <path name> , <close channel list>				
[, <open channel list>]				
:DEFine? <path name>				no chan list for query
:DELete				
[:NAME] <path name> (Command Only)				
:ALL (Command Only)				
:STORe* (Command Only)				
:AUTO	**ON**	**OFF**	**OFF**	
:AUTO?				
:RECall* (Command Only)				
:AUTO	**ON**	**OFF**	**OFF**	
: AUTO?				
:EXCLude* <channel list>				no exclude list
:EXCLude?				
:CLEar <channel list>				
:STORe* (Command Only)				
:AUTO	**ON**	**OFF**	**OFF**	
:AUTO?				
:RECall* (Command Only)				
:AUTO	**ON**	**OFF**	**OFF**	
:AUTO?				
:INCLude* <channel list>				
:INCLude?				
:CLEar <channel list>				
:ALL				
:STORe* (Command Only)				
:AUTO	**ON**	**OFF**	**OFF**	
:AUTO?				
:RECall* (Command Only)				
:AUTO	**ON**	**OFF**	**OFF**	
:AUTO?				

Table 6-1, Commands Implemented by the 1257A - Continued

Command		Max	Min	Default	Notes
:VERify*					
:MASK <channel list>, 0 | 1 | X					
:MASK? <channel list>					
:VERify? <channel list or path name>					
:VERify:ALL?	(Query Only)				
:CONFidence					
:STATE 0 | 1 | OFF | ON					
:STATE?					
SYSTEM Subsystem:					
:SYSTem					
:ERRor?	(Query Only)				
:VERSion?	(Query Only)				1994.0
:EVENT?					
:EVENT					
:COUNt?					
:CLEar <0 … N>					
:ALL					
:PRESet					
STATUS Subsystem:					
:STATus					
:PRESet	(Command Only)				
:OPERation					
[:EVENt]?	(Query Only)				
:CONDition?	(Query Only)	255	0	0	
:ENABle <enable mask>					
:ENABle?					
:QUEStionable					When the register or status
[:EVENt]?	(Query Only)				event are read, they will
:CONDition?	(Query Only)				return a value of zero (0).
:ENABle <enable mask>					These commands are not
:ENABle?					used with the 1257
Power On:					
:POWeron					
:RECall					
:STATe 0 | 1					
:STATe?					
Reset/(Preset) Recall: Power On State:					
:RECall					
:POWeron					
:RESET					
:STATe 0 | 1					
:STATe?					

Table 6-1, Commands Implemented by the 1257A - Continued

Command	Max	Min	Default	Notes
DIAGnostic Subsystem:				
:DIAGnostic				
:RELay				
:CYCLes? <channel list>				**(Query Only)**
:ENDoflife				
:STATus? <channel list>				**(Query Only)**
Configuration Commands				
::DRIVerboard				
:ADAPterboard				
:ID?				**(Query Only)**
:CONFigure				
:RELay				
:ADD <parameters>				**(Command Only)**
:DELete <slot> , <relay id>				**(Command Only)**
:LIST? <slot>				**(Query Only)**
:RELay? <slot> , <relay id>				
:ATTenuator				
:ADD <parameters>				
:DELete <slot> , <attenuator id>				
:LIST? <slot>				
:ATTenuator? <channel list>				
:RESET { ALL	<slot> }				
:LED				
:MAXCURrent <slot> , <curr. multiplier>				
:MAXCURrent? <slot>				
:DESCriptor <slot>, <descriptor>				
:DESCriptor? <slot>				
:DIGital				
:OUTPut <pattern> , <channel list>				
:OUTPut? <channel list>				
:CONFigure				
:WIDTh { 8	16 }, <channel list>				
:WIDTh? <channel list>				
:POLarity { NORMal	INVerted } ,<ch list>				
:POLarity? <channel list>				
IEEE-488.2 Common Commands:				
*IDN? (Query Only)				
*RST (Command Only)				
*TST? (Query Only)				
*CLS (Command Only)				
*ESE <register value>	255	0	0	
*ESE?				
*ESR? (Query Only)				
*SRE <register value>	255	0	0	
*SRE?				
*STB? (Query Only)				
*OPC				
*OPC?				
*SAV [<state>] (Command Only)	0	100	100	
*RCL [<state>] (Command Only)	0	100	100	
*OPT? (Query Only)				
*WAI				no function in the 1257A

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Appendix A
Troubleshooting

If you have difficulty using the 1257A, refer to the following troubleshooting tables. If you are still unable to resolve the problem, contact Customer Support.

Power Up

Problem	Possible Causes	What to do
Unit does not turn on (fan not running and no indicator lights).	Not connected to proper power source.	Make sure that the 1257A is connected to a live power source, using a functional power cord. For input power requirements, see **Chapter 1, Overview and Features**.
	Fuse missing, blown, or incorrectly installed.	Check fuses, verifying that they have the required capacity as marked on the 1257A rear panel. Make sure the fuse is properly installed (see **Chapter 1, Overview and Features**.

Power Indicator Light

Problem	Possible Causes	What to do
Indicator lights go blank (was working).	Power has been interrupted.	Verify that the AC input is connected to a live power outlet.
		Verify that the power switch is still in the ON position.
		Check fuses, verifying that they have not blown.

LAN Indicator Light

Problem	Possible Causes	What to do
LAN light is red	Failure to acquire a valid IP address.	Disconnect and reconnect LAN cable. Confirm LAN cable is connected to network properly. Press the LAN reset button for 10 seconds.
	Detection of a duplicate IP address.	Change the IP address of the other device. If in manual setting, change the IP address of the 1257A. Press the LAN reset button for 10 seconds.
	LAN cable disconnected.	Reconnect LAN cable.

Password

Problem	Possible Causes	What to do
Password does not work or you have forgotten the password.	-----	Press the LAN reset button for 10 seconds and your password returns to being blank. Reset the password in your system with the web-page interface.

IEEE-488.2 (GPIB) Remote Interface

Problem	Possible Causes	What to do
1257A does not respond to GPIB commands.	Commands sent to wrong GPIB address.	Make sure that the commands are sent to the GPIB address currently assigned to the 1257A.
	The 1257A is set to the same GPIB address as another device in the system.	Make sure that the 1257A is set to a unique GPIB address.
	GPIB may have been disabled with a SCPI command.	Use the web-page interface to enable the GPIB. Send a SCPI command to enable the GPIB.

Switches

Problem	Possible Causes	What to do
Slot containing a switch is indicated as an "empty slot" when using the web-page interface.	Switch not properly connected.	Remove top cover of 1257A and reconnect switch to electronic connection.

USB Flash (Thumb) Drive

Problem	Possible Causes	What to do
Immediately after startup, the flash drive is not recognized by the web-page interface.	Flash drive format.	The 1257A system supports both FAT32 (Windows) and VFAT (Linux). Reformat the flash drive and reinstall it.
	During the startup routine, the system may not see a previously installed flash drive.	Remove and reinstall the flash drive.

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Appendix B
Mounting Hardware and Cables

This appendix includes information on the adapter board, mounting brackets/plates, and cable assemblies needed to install components in your 1257A-D.

The tables list the currently supported components (both OEM and Astronics part numbers), the associated adapter board kit, the interior (rail) or exterior (panel) mounting hardware kit, and the associated cable kit. Mounting screws are included with the various kits.

The figures show illustrations of the various available brackets as well as the mounting hole specifications should you desire to customize your own front and rear panels.

Note: If you have questions about the use of other components not listed in these tables, please contact our sales and support team. Contact information is on the front cover of this manual.

Additional information on the Adapter Boards is contained in **Appendix C, Adapter Board Configuration**.

Supported Components

The following components are all compatible with the 405342-101 Driver Board.

Agilent Switches and Relays

Table B-1, Agilent Supported Switches/Relays

Switch Model	Astronics Part No.	Freq. Range GHz	Type	Switch Details (Coil Volt) (Position Ind.) (DC Conn.)	Adapter Board Kit	Panel Mount Kit	Internal Mount Kit	Cable Kit
8762B-024	310429-001	20 26.5	SPDT, Terminated, LC	-024 (24 Volt) (n/a) (Solder Term)	408435-001 *(Compatible with N1810 read-back)*	(1x) 408494-001 [1] (2x) 408495-001 [1] (3x) 408496-001 [1] (1x) 408434-001 [4]	408499-001 [1] 408436-001 [4]	408446-001 [2]
8762C-024	310429-002							
8762B-024	310429-001	20 26.5	SPDT, Terminated, LC	-024 (24 Volt) (n/a) (Solder Term)	408442-001 *(Drive-out and read-back connected)*	(1x) 408494-001 [1] (2x) 408495-001 [1] (3x) 408496-001 [1] (4x) 408434-001 [4]	408499-001 [1] 408436-001 [4]	408509-001 [1]
8762C-024	310429-002							
8765B-024	922716	20 26.5	SPDT, LC	-024 (24 Volt) (n/a) (5 Pin SIL)	408442-001	(1x) 408494-001 [1] (2x) 408495-001 [1] (3x) 408496-001 [1] (1x) 408434-001 [4]	408499-001 [1] 408436-001 [4]	408509-001 [1]
8765C-024	922716-002							
8765C-024	922716-002							

Switch Model	Astronics Part No.	Freq. Range GHz	Type	Switch Details (Coil Volt) (Position Ind.) (DC Conn.)	Adapter Board Kit	Panel Mount Kit	Internal Mount Kit	Cable Kit
N1810UL-20	310432-001	20	SPDT, EL	-124 -402 -201 (24 Volt) (Position Ind) (DB9F)	408435-001	(1x) 408494-001 [1] (2x) 408495-001 [1] (3x) 408496-001 [1] (1x) 408434-001 [4]	408499-001 [1] 408436-001 [4]	408433-001 [2]
N1810UL-26.5	310432-002	26.5						
N1810UL-40	310432-003	40						
N1810TL-20	310431-001	20	SPDT, Terminated, EL	-124 -402 -201 (24 Volt) (Position Ind) (DB9F)	408435-001	(1x) 408497-001 (2x) 408498-001	408499-001 [1] 408436-001 [4]	408433-001 [2]
N1810TL-26.5	310431-002	26.5						
N1810TL-40	310431-003	40						
L7222C	310443-001	26.5	DPDT	-161 (24 Volt) (Position Ind) (10-pin Header)	408435-001	N/A *	408500-001 [1] 408444-001 [2]	408447-001 [2]
87222C	310443-002	26.5	DPDT	(24 V) (Position Ind) (10-pin Header)	408435-001	N/A *	408500-001 [1] 408444-001 [2]	408447-001 [2]
87222D	310443-003	40	DPDT	(24 V) (Position Ind) (10-pin Header)	408435-001	N/A *	408500-001 [1] 408444-001 [2]	408447-001 [2]
87104B-024	310427-001	20	SP4T, Terminated, EL	-024, -161 (24 Volt) (Position Ind) (16-pin header)	408431-001	N/A *	408501-001 [1] 408430-001 [4]	408432-001 [2]
87104C-024	310427-002	26.5						
87104D-024	310427-003	40						
87106B-024	310428-001	20	SP6T, Terminated, EL	-024, -161 (24 Volt) (Position Ind) (16-pin header)	408431-001	N/A *	408501-001 [1] 408430-001 [4]	408432-001 [2]
87106C-024	310428-002	26.5						
87106D-024	310428-003	40						
87204A	310451-001	4	SP4T, Terminated EL	-161 (24 Volt) (16-pin header)	408431-001	N/A *	408501-001 [1] 408430-001 [4]	408580-001
87204B	310451-002	20						
87204C	310451-003	26.5						
87206A	310452-001	4	SP6T, Terminated EL	-161 (24 Volt) (16-pin header)	408431-001	N/A *	408501-001 [1] 408430-001 [4]	408580-001
87206B	310452-002	20						
87206C	310452-003	26.5						
L7104B	922718	20	SP4T Terminated, LC	-024, -161 (24 Volt) (Position Ind) (16-pin header)	408431-001	N/A *	408501-001 [1] 408430-001 [4]	408432-001 [2]
L7104C	922718-002	26.5						
L7106B	922719	20	SP6T, Terminated, LC	-024, -161 (24 Volt) (Position Ind) (16-pin header)	408431-001	N/A *	408501-001 [1] 408430-001 [4]	408432-001 [2]
L7106C	922719-002	26.5						
L7204B	922717	20	SP4T, LC	-024, -161 (24 Volt) (Position Ind) (16-pin header)	408431-001	N/A *	408501-001 [1] 408430-001 [4]	408432-001 [2]
L7204C	922717-002	26.5						
L7206B	310430-001	20	SP6T, LC	-024, -161 (24 Volt) (Position Ind) (16-pin header)	408431-001	N/A *	408501-001 [1] 408430-001 [4]	408432-001 [2]
L7206C	310430-002	26.5						

See Notes on next page.

Notes:

- *For Panel Mounting Kits: The number in parentheses before the part number (i.e. (2x)) indicates the quantity of switches that particular mounting bracket will hold. No notation indicates the bracket will hold a single switch.*

- **N/A *:** An extra panel mount kit is not required. These switches already have an inbuilt flange to mount to the panel.

- *For Internal Mount Kits: The number in brackets after the part number (i.e. [1]) indicates the quantity of parts in that kit.*

Radiall Switches and Relays

Table B-2, Radiall Supported Switches/Relays

Switch Model	Astronics Part No.	Freq. Range GHz	Type	Switch Details (Coil Volt) (Position Ind.) (DC Conn.)	Adapter Board Kit	Panel Mount Kit	Internal Mount Kit	Cable Kit
R570433010	310433-001	18	SPDT	(28 Volt) (No Ind) (Solder Term.)	408442-001	(1x) 408494-001 (2x) 408495-001 (3x) 408496-001	408499-001 [1] 408436-001 [4]	408509-001
R570F33010	310433-002	26.5						
R570833010	310433-003	40						
R585433210	310434-001	18	SPDT, Terminated	(28 Volt) (No Ind) (Solder Term.)	408442-001	(1x) 408497-001 (2x) 408498-001	408499-001 [1] 408436-001 [4]	408509-001
R585F33210	310434-002	26.5						
R585833210	310434-003	40						
R573433415	310435-001	18	SP4T	(28 Volt) (Position Ind) (DSUB 25)	408431-001	N/A *	408506-001	408511-001
R573F33415	310435-002	26.5						
R573833415	310435-003	40						
R574433415	310436-001	18	SP4T, Terminated	(28 Volt) (Position Ind) (DSUB 25)	408431-001	N/A *	408506-001	408511-001
R574F33415	310436-002	26.5						
R574833415	310436-003	40						
R573433615	310437-001	18	SP6T	(28 Volt) (Position Ind) (DSUB 25)	408431-001	N/A *	408507-001	408512-001
R573F33615	310437-002	26.5						
R573833615	310437-003	40						
R574433615	310438-001	18	SP6T, Terminated	(28 Volt) (Position Ind) (DSUB 25)	408431-001	N/A *	408507-001	408512-001
R574F33615	310438-002	26.5						
R574833615	310438-003	40						
R573433815	310439-001	18	SP8T	(28 Volt) (Position Ind) (DSUB 25)	408464-001	N/A *	408502-001	408513-001
R573F33815	310439-002	26.5						
R573833815	310439-003	40						
R574433815	310440-001	18	SP8T, Terminated	(28 Volt) (Position Ind) (DSUB 25)	408464-001	N/A *	408502-001	408513-001
R574F33815	310440-002	26.5						
R574833815	310440-003	40						
R573433015	310441-001	18	SP10T	(28 Volt) (Position Ind) (DSUB 25)	408524-001	N/A *	408503-001	408514-001
R573F33015	310441-002	26.5						
R573833015	310441-003	40						
R574433015	310442-001	18	SP10T, Terminated	(28 Volt) (Position Ind) (DSUB 25)	408524-001	N/A *	408503-001	408514-001
R574F33015	310442-002	26.5						
R574833015	310442-003	40						
R577434017	310444-001	18	DPDT	(28 Volt) (Position Ind) (DSUB 9)	408435-001	N/A *	408500-001 [1] 408444-001 [2]	408515-001
R577F34017	310444-002	26.5						
R577834017	310444-003	40						

Notes:

- *For Panel Mounting Kits: The number in parentheses before the part number (i.e. (2x)) indicates the quantity of switches that particular mounting bracket will hold. No notation indicates the bracket will hold a single switch.*

- **N/A *:** An extra panel mount kit is not required. These switches already have an inbuilt flange to mount to the panel.
- *For Internal Mount Kits: The number in brackets after the part number (i.e. [1]) indicates the quantity of parts in that kit.*

Supported Attenuators

Table B-3, Supported Attenuators

Manufacturer	Model	Type	Astronics Part No.	Adapter Board Kit	Mounting Bracket Kit	Cable Kit
Agilent	8494G	11/1 dB	8494G	408443-001	408445-001	408448-001 [2]
Agilent	8494H	11/1 dB	921847	408443-001	408445-001	408448-001 [2]
Agilent	8496H	11/1 dB	921848	408443-001	408445-001	408448-001 [2]
Agilent	8596G	110/10 dB	921849	408443-001	408445-001	408448-001 [2]

*Note: **For Internal Mount Kits:** The number in brackets after the cable kit part number (i.e. [2]) indicates the quantity of cables in that kit.*
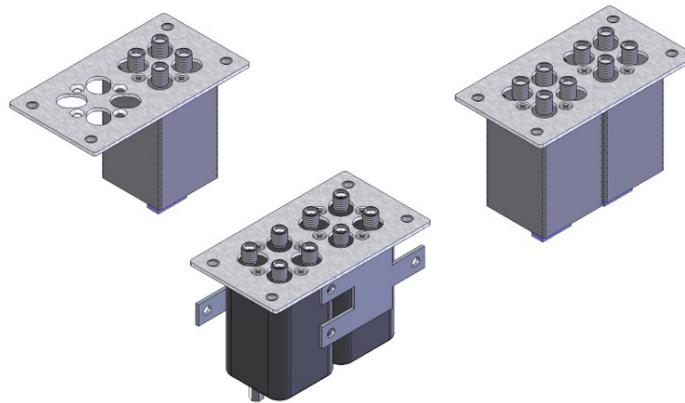
408500-001
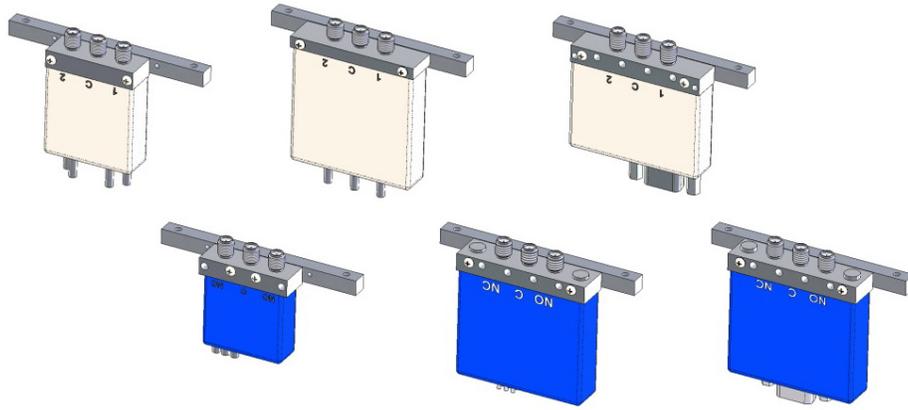
408499-001

Figure B-1, Internal Mounting Brackets

408501-001

408502-001

408503-001

408506-001

408507-001
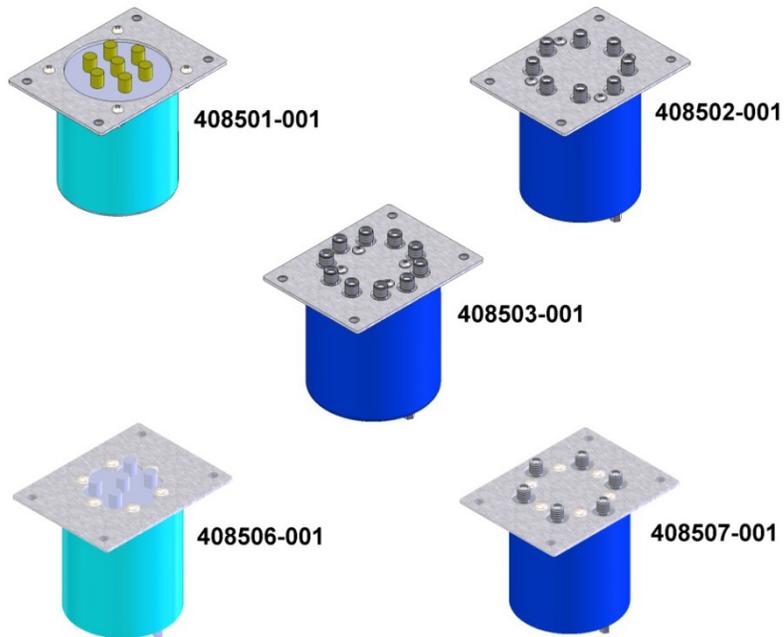
Figure B-1, Internal Mounting Brackets (Continued)
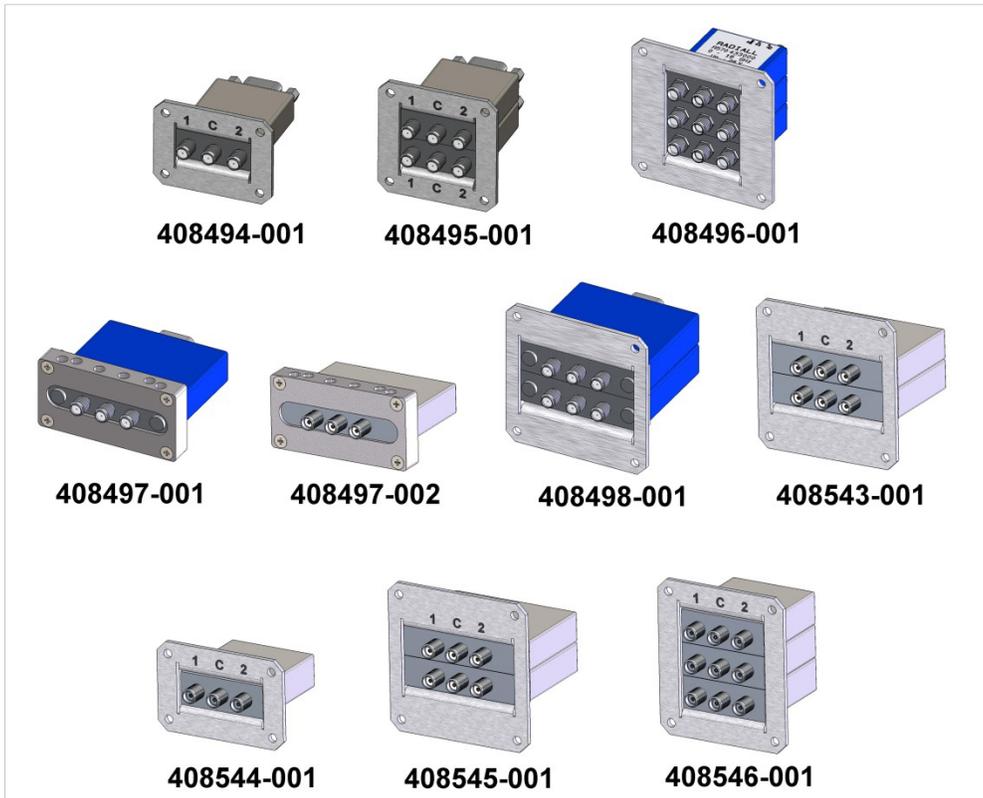
Figure B-2, External Mounting Brackets and Plates
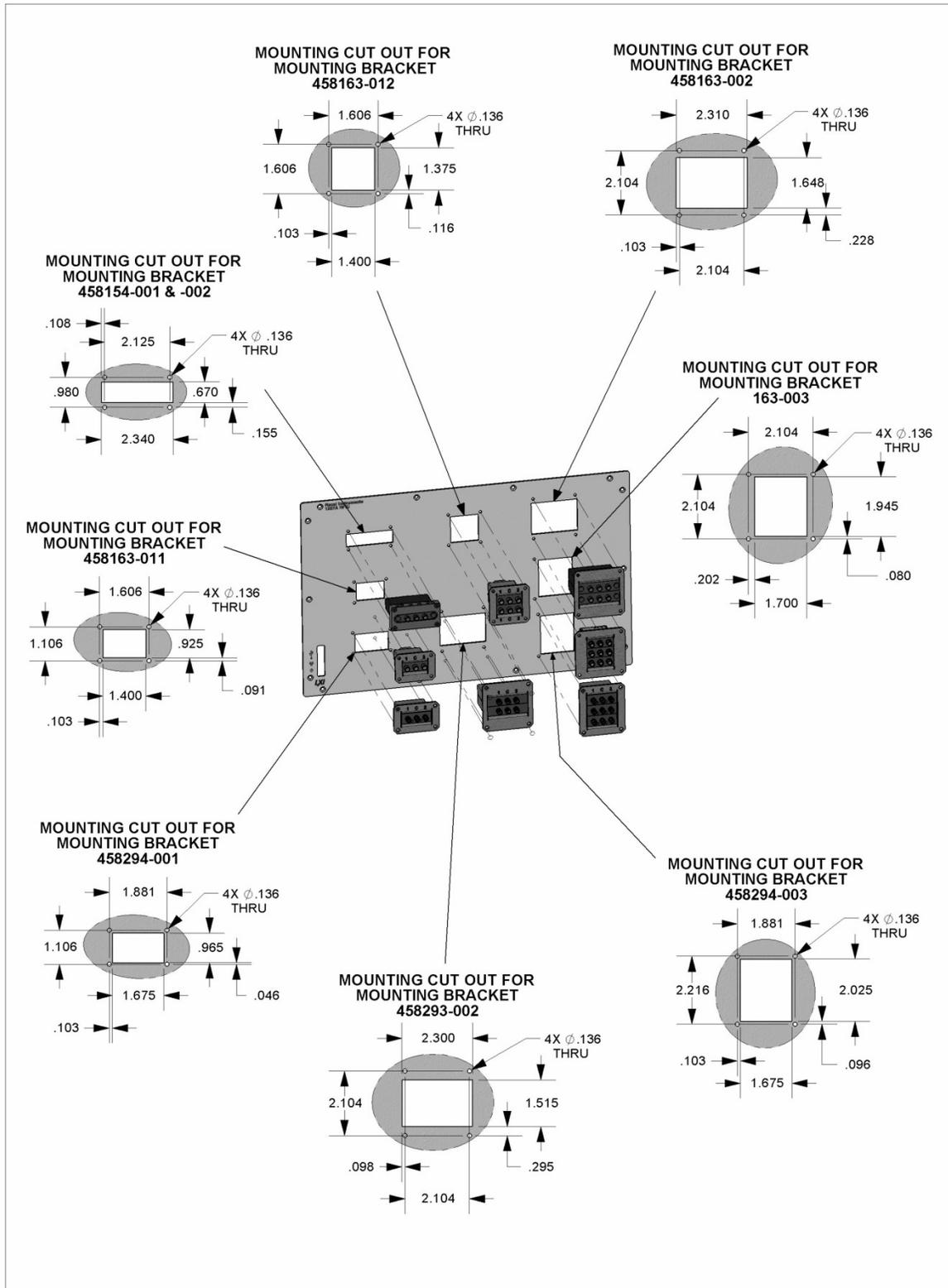
Figure B-3, Exterior Mounting Bracket Cut Out Specifications

KIT P/N 408433-001

KIT P/N 408443-001

KIT P/N 408446-001

KIT P/N 408447-001

KIT P/N 408448-001

KIT P/N 408456

KIT P/N 408509-001 & -002

KIT P/N 408511-001 & -002

KIT P/N 408512-001 & -002

Figure B-4, Cable Kits

KIT P/N 408513-001 & -002

KIT P/N 408513-001 & -002

KIT P/N 408514-001 & -002

KIT P/N 408515-001 & -002

KIT P/N 408522-001

KIT P/N 408530-001

KIT P/N 408533-001 & -002

KIT P/N 408535-001 & -002

KIT P/N 408537-001 & -002

KIT P/N 408534-001 & -002
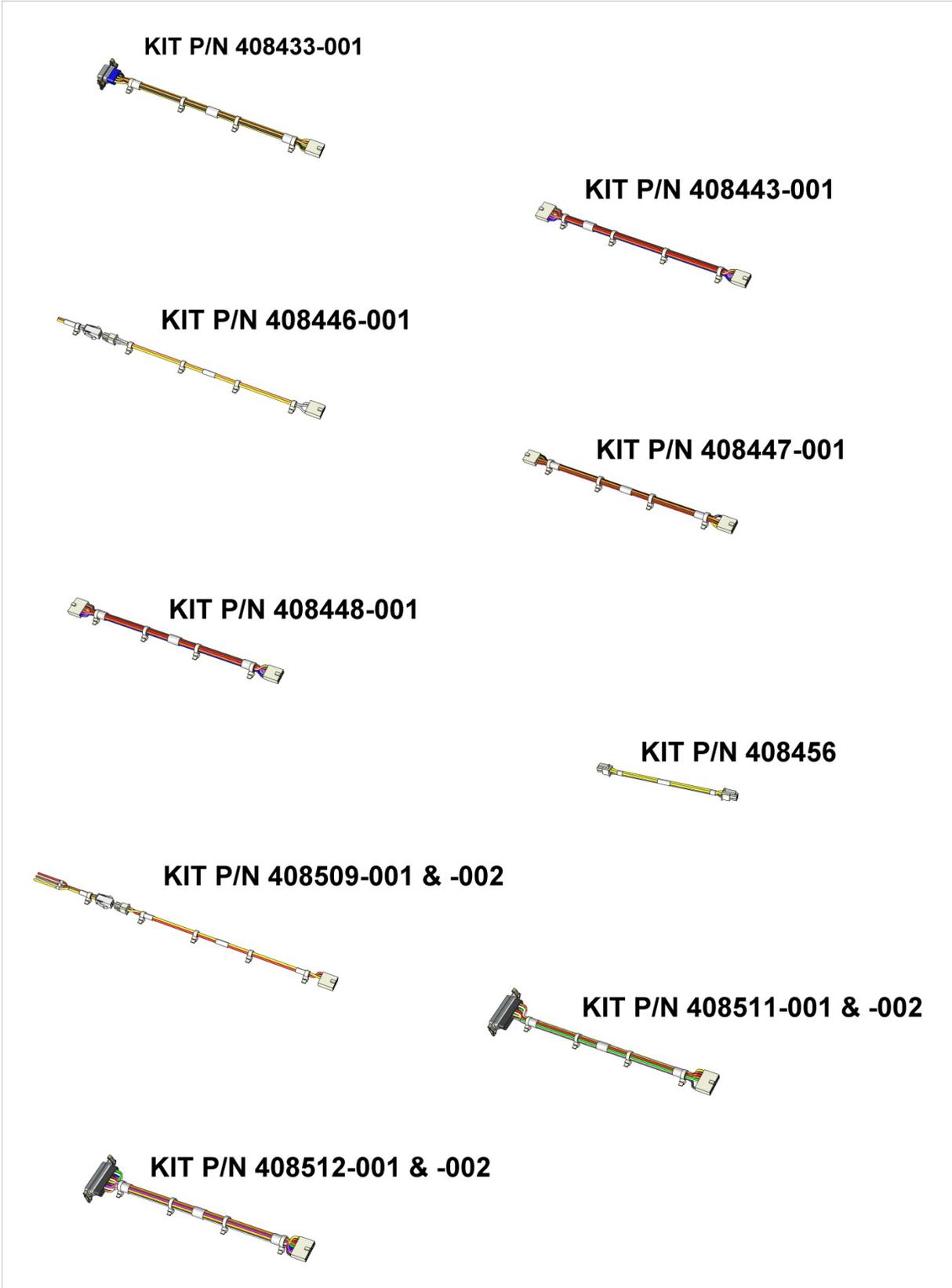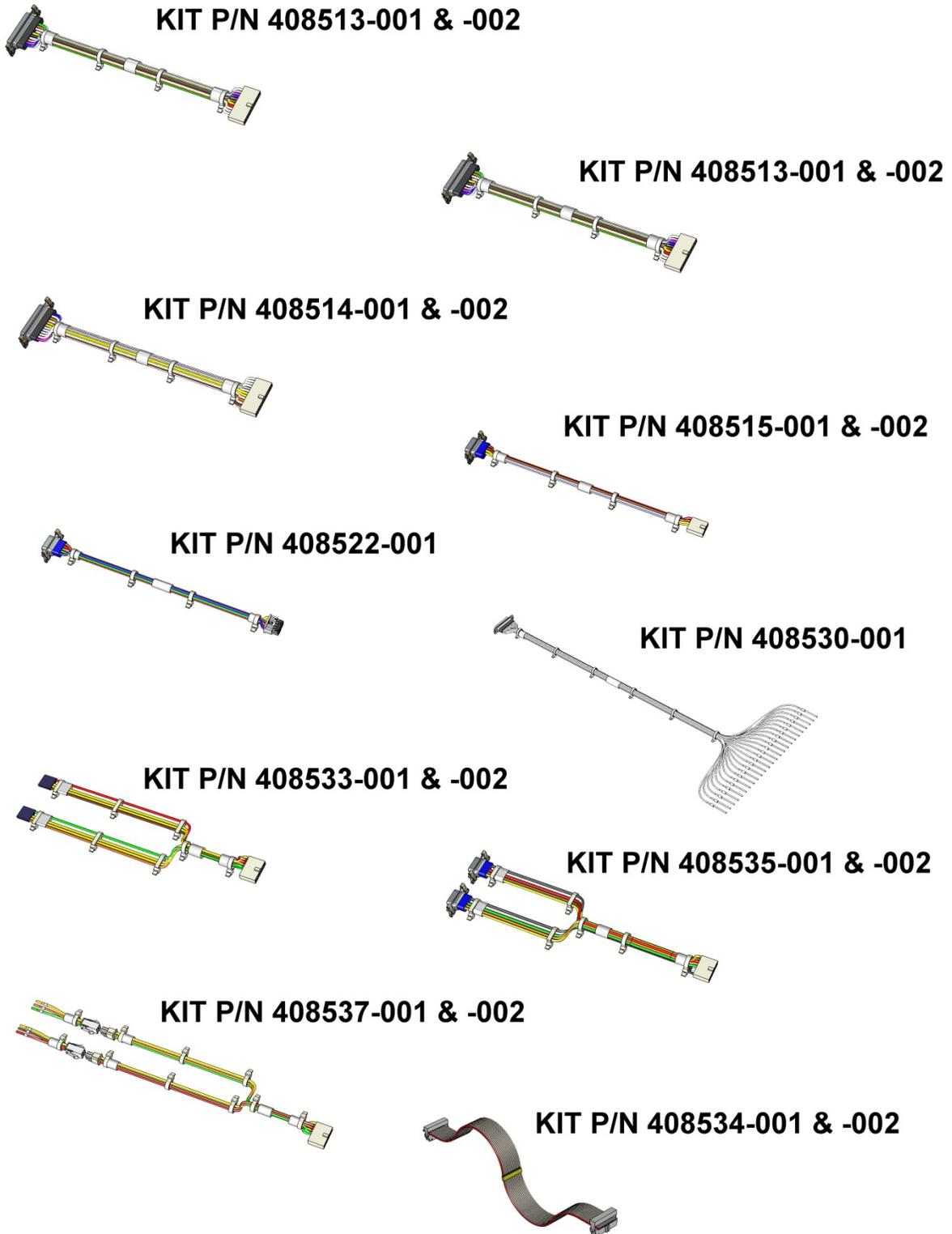
Figure B-4, Cable Kits (Continued)

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Appendix C
Adapter Board Configuration

This appendix provides detailed information on the various adapter board types available, board connector pinouts, configuration requirements, and supported components.

The tables in **Appendix B** refer to Adapter Board Kits. This includes the adapter board and mounting hardware. The part number of the adapter board contained in the kit is listed in the table below.

Table C-1, Adapter Board Kit Reference Table

Adapter Board Kit	Adapter Board	Supported Switches	Adapter Board ID
408525-001	405365	(Terminal blocks) 16 x Drive 16 x Indicator 4 x Reset	0
408431-001	405343	4 x SP4T 2 x SP6T, 1 x SP4T 1 x SP6T, 2 x SP4T	1
408435-001	405366	8 x SPDT or DPDT	2
408442-001	405372	8 x SPDT or DPDT	3
408464-001	405367	2 x SP8T	4
408524-001	405373	1 x SP10T	5
408523-001	405405	2 x SP6T, 1 x SP4T	6
408443-001	405370	2 x 4 cell Atten	13

Adapter Board Types

The various adapter board types are shown in **Figure C-1** through **Figure C-8**. If necessary, refer to the tables in **Appendix B, Mounting Hardware and Cables** to help determine what boards are used with your particular components. Pinouts of the adapter boards are shown in **Table C-2** through **Table C-9**.
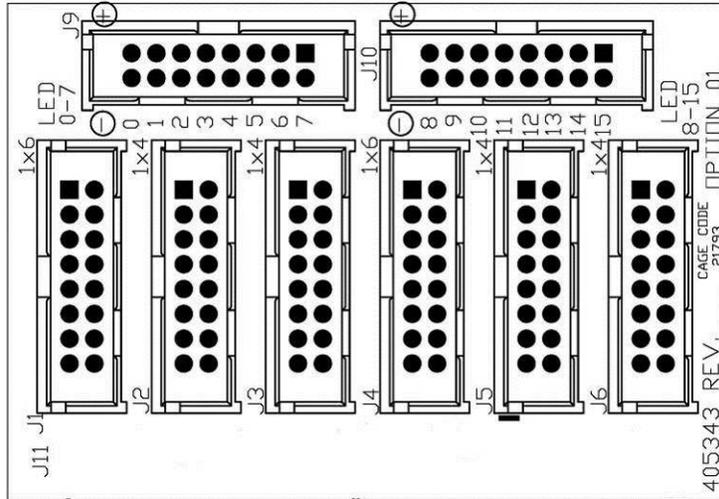
Figure C-1, Adapter Board: 405343

Table C-2, Adapter Board 405343 Pinout

Pin	Connector					
	J1	**J2**	**J3**	**J4**	**J5**	**J6**
1	VCOM	VCOM	VCOM	VCOM	VCOM	VCOM
2	VCOM	VCOM	VCOM	VCOM	VCOM	VCOM
3	DR0	N/C	N/C	DR06	N/C	N/C
4	RDBK0	N/C	N/C	RDBK6	N/C	N/C
5	DR1	DR0	DR4	DR7	DR8	DR12
6	RDBK1	RDBK0	RDBK4	RDBK7	RDBK8	RDBK12
7	DR2	DR1	DR5	DR8	DR9	DR13
8	RDBK2	RDBK1	RDBK5	RDBK8	RDBK9	RDBK13
9	DR3	N/C	N/C	DR9	N/C	N/C
10	RDBK3	N/C	N/C	RDBK9	N/C	N/C
11	DR4	DR2	DR06	DR10	DR10	DR14
12	RDBK4	RDBK2	RDBK6	RDBK10	RDBK10	RDBK14
13	DR5	DR3	DR7	DR11	DR11	DR15
14	RDBK5	RDBK3	RDBK7	RDBK11	RDBK11	RDBK15
15	GND	GND	GND	GND	GND	GND
16	RST0	RST0	RST1	RST1	RST2	RST3
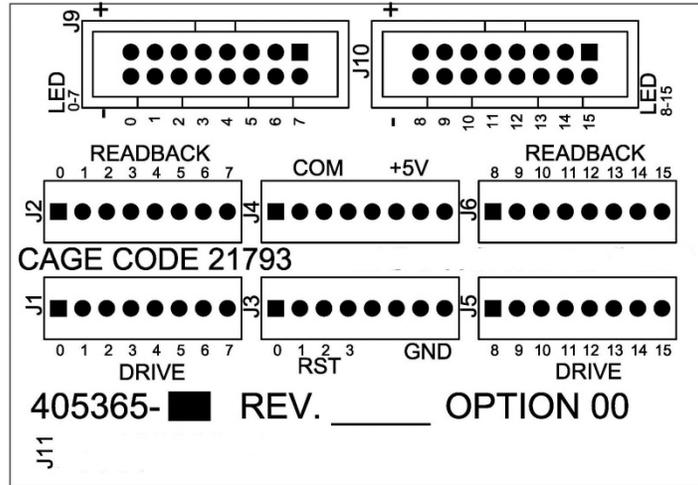
Figure C-2, Adapter Board: 405365

Table C-3, Adapter Board 405365 Pinout

Pin	Connector					
	J1	**J2**	**J3**	**J4**	**J5**	**J6**
1	DR0	RDBK0	RST0	VCOM	DR8	RDBK8
2	DR1	RDBK1	RST1	VCOM	DR9	RDBK9
3	DR2	RDBK2	RST2	VCOM	DR10	RDBK10
4	DR3	RDBK3	RST3	VCOM	DR11	RDBK11
5	DR4	RDBK4	GND	+5V	DR12	RDBK12
6	DR5	RDBK5	GND	+5V	DR13	RDBK13
7	DR6	RDBK6	GND	+5V	DR14	RDBK14
8	DR7	RDBK7	GND	+5V	DR15	RDBK15
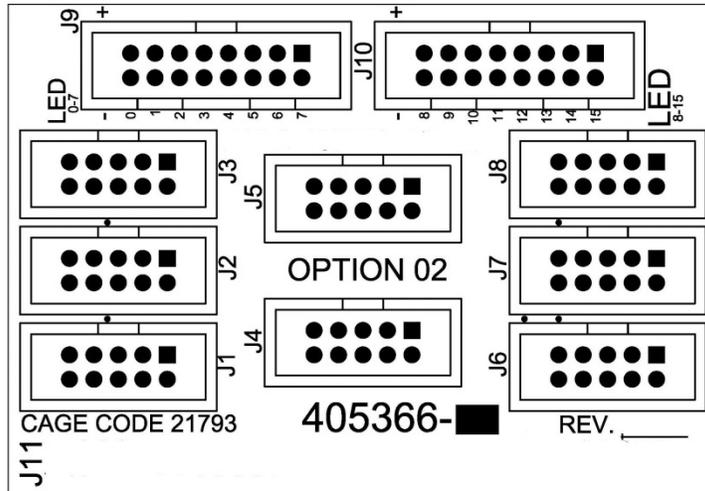
Figure C-3, Adapter Board: 405366

Table C-4, Adapter Board 405366 Pinout

Pin	Connector							
	J1	**J2**	**J3**	**J4**	**J5**	**J6**	**J7**	**J8**
1	GND	GND	GND	GND	GND	GND	GND	GND
2	VCOM	VCOM	VCOM	VCOM	VCOM	VCOM	VCOM	VCOM
3	N/C	N/C	N/C	N/C	N/C	N/C	N/C	N/C
4	RDBK1	RDBK3	RDBK5	RDBK7	RDBK9	RDBK11	RDBK13	RDBK15
5	DR1	DR3	DR5	DR7	DR9	DR11	DR13	DR15
6	VCOM	VCOM	VCOM	VCOM	VCOM	VCOM	VCOM	VCOM
7	DR0	DR2	DR4	DR6	DR8	DR10	DR12	DR14
8	RDBK0	RDBK2	RDBK4	RDBK6	RDBK8	RDBK10	RDBK12	RDBK14
9	VCOM	VCOM	VCOM	VCOM	VCOM	VCOM	VCOM	VCOM
10	N/C	N/C	N/C	N/C	N/C	N/C	N/C	N/C
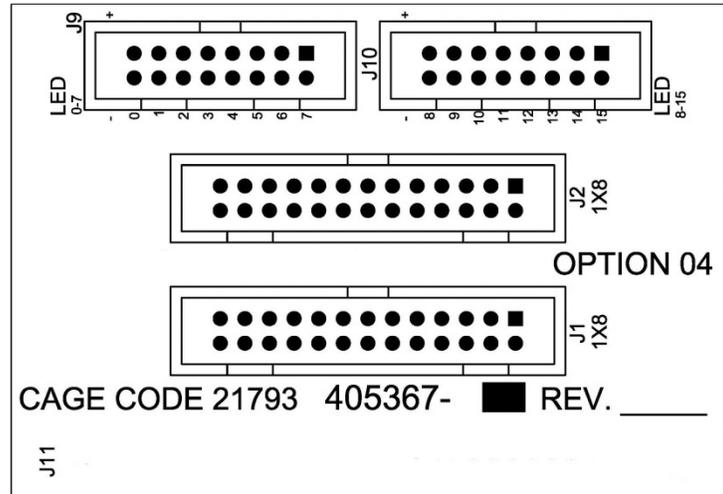
Figure C-4, Adapter Board: 405367

Table C-5 Adapter Board 405367 Pinout

Pin	Connector	
	J1	**J2**
1	N/C	N/C
2	N/C	N/C
3	DR7	DR15
4	RDBK7	RDBK15
5	DR6	DR14
6	RDBK6	RDBK14
7	DR5	DR13
8	RDBK5	RDBK13
9	DR4	DR12
10	RDBK4	RBDK12
11	RST0	RST1
12	DR0	DR8
13	VCOM	VCOM
14	VCOM	VCOM
15	N/C	N/C
16	DR1	DR9
17	RDBK0	RDBK8
18	RDBK1	RDBK9
19	DR3	DR11
20	RDBK3	RDBK11
21	DR2	DR10
22	RDBK2	RDBK10

	Connector	
Pin	**J1**	**J2**
23	N/C	N/C
24	N/C	N/C
25	N/C	N/C
26	N/C	N/C
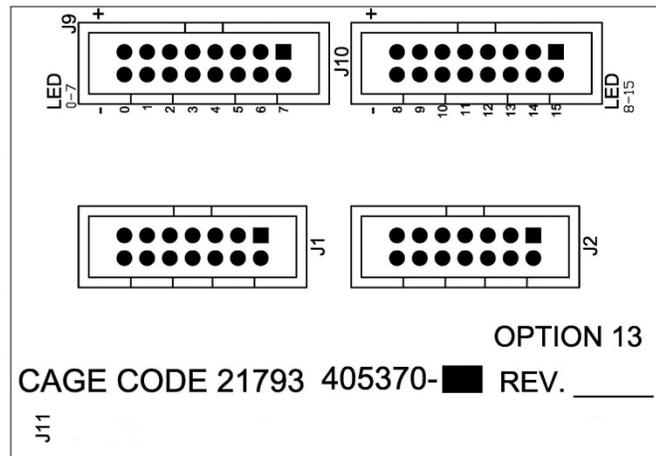
Figure C-5, Adapter Board: 405370

Table C-6, Adapter Board 405370 Pinout

	Connector	
Pin	**J1**	**J2**
1	N/C	N/C
2	DR0	DR8
3	DR6	DR14
4	DR7	DR15
5	DR1	DR9
6	VCOM	VCOM
7	N/C	N/C
8	N/C	N/C
9	DR2	DR10
10	DR3	DR11
11	DR5	DR13
12	N/C	N/C
13	DR4	DR12
14	N/C	N/C
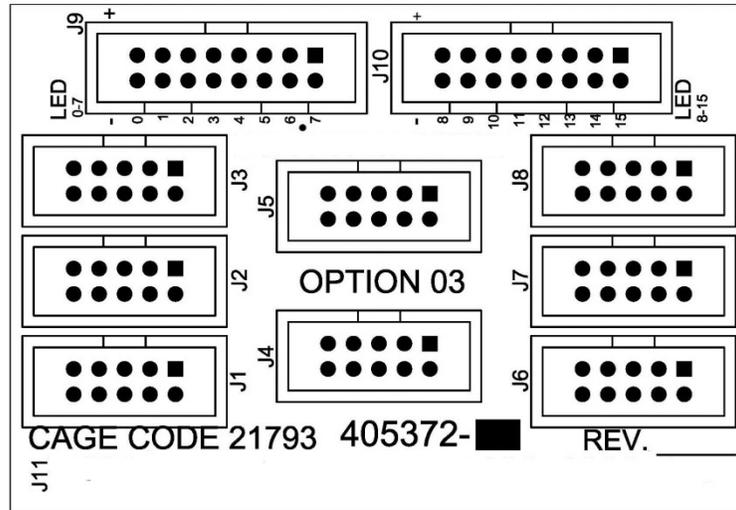
Figure C-6, Adapter Board: 405372

Table C-7, Adapter Board 405372 Pinout

Pin	Connector							
	J1	**J2**	**J3**	**J4**	**J5**	**J6**	**J7**	**J8**
1	DR1	DR3	DR5	DR7	DR9	DR11	DR13	DR15
2	RDBK1	RDBK3	RDBK5	RDBK7	RDBK9	RDBK11	RDBK13	RDBK15
3	DR0	DR2	DR4	DR6	DR8	DR10	DR12	DR14
4	RDBK0	RDBK2	RDBK4	RDBK6	RDBK8	RDBK10	RDBK12	RDBK14
5	VCOM	VCOM	VCOM	VCOM	VCOM	VCOM	VCOM	VCOM
6	VCOM	VCOM	VCOM	VCOM	VCOM	VCOM	VCOM	VCOM
7	VCOM	VCOM	VCOM	VCOM	VCOM	VCOM	VCOM	VCOM
8	N/C	N/C	N/C	N/C	N/C	N/C	N/C	N/C
9	DR0	DR2	DR4	DR6	DR8	DR10	DR12	DR14
10	N/C	N/C	N/C	N/C	N/C	N/C	N/C	N/C
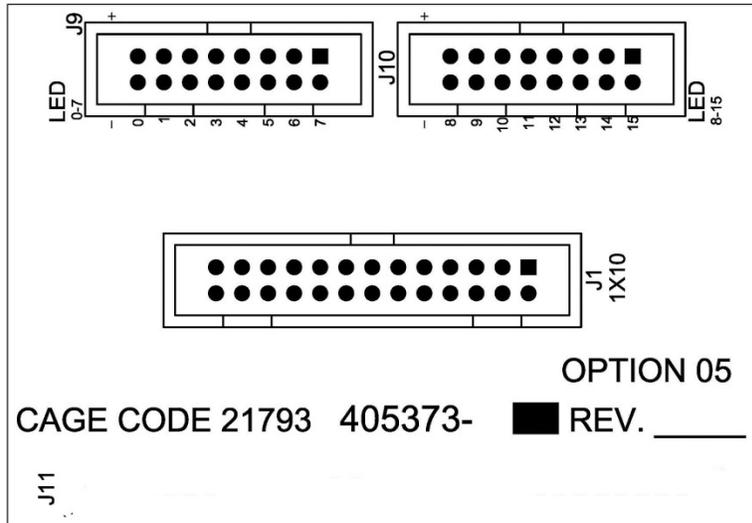
Figure C-7, Adapter Board: 405373

Table C-8, Adapter Board 405373 Pinout

	Connector		Connector
Pin	**J1**	**Pin**	**J1**
1	DR9	14	DR4
2	RDBK9	15	VCOM
3	RDBK8	16	RDBK4
4	DR8	17	RDBK3
5	DR7	18	DR3
6	RDBK7	19	DR2
7	RDBK6	20	RDBK2
8	RST0	21	RDBK1
9	DR6	22	DR1
10	DR5	23	DR0
11	RDBK5	24	RDBK0
12	N/C	25	N/C
13	VCOM	26	N/C
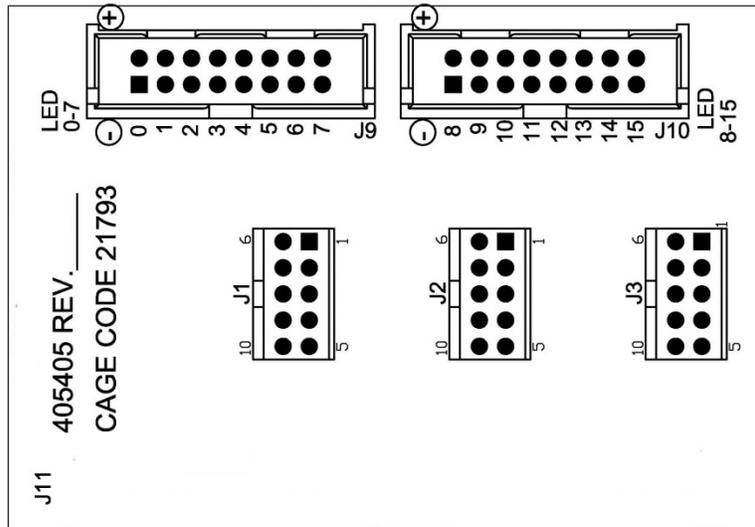
Figure C-8, Adapter Board: 405405

This board is intended for fail-safe relays with no indicators and discrete wiring.

Table C-9, Adapter Board 405405 Pinout

	Connector		
Pin	**J1**	**J2**	**J3**
1	VCOM	VCOM	VCOM
2	VCOM	VCOM	VCOM
3	VCOM	VCOM	VCOM
4	VCOM	VCOM	VCOM
5	DR0	DR6	DR12
6	DR1	DR7	DR13
7	DR2	DR8	DR14
8	DR3	DR9	DR15
9	DR4	DR10	N/C
10	DR5	DR11	N/C

Connector Pinouts

The ribbon connectors on the adapter boards are illustrated in Figure C-9.

The white areas in the illustrations indicate cutout positions in the actual connector.
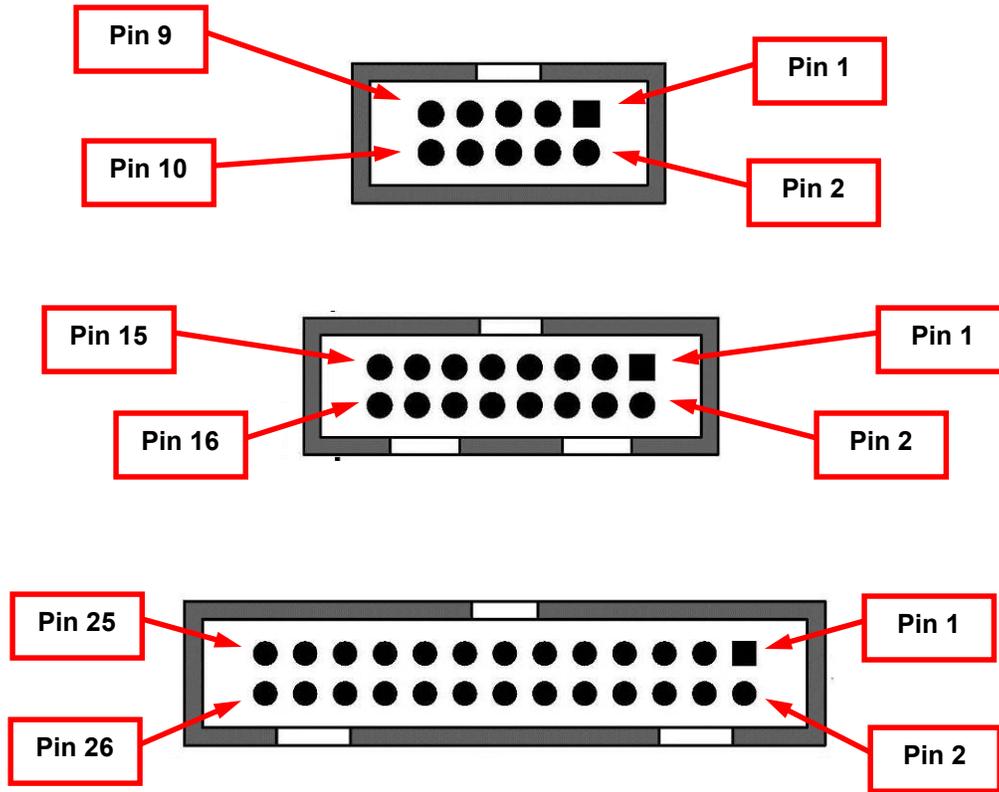
Figure C-9, Adapter Board Connectors and Pin Numbering

Appendix D
Component Configuration Example

In this chapter, we're providing a specific example of how to configure the system with four specific relays (noted below). We will assume you have assembled the relays into their brackets and mounted them into the system.

Also, we will assume you have installed the adapter boards to the driver board (in the position noted in the table below) and then attached the relay cables to the appropriate adapter board.

Be sure to record which adapter board connector number (i.e. J1) you have connected the relay cables to. This information is needed later in this procedure.

See **Chapter 2** and **Appendices B** and **C** for assistance on any of these steps.

For this example, we have chosen to configure the following components:

Relay Type	Driver Board	Adapter Board Kit	Bank (Driver Board Position)
Agilent SP6T	Slot 2	408431-001	A
Agilent SP4T	Slot 2	408431-001	B
Agilent SPDT	Slot 2	40835-001	C
Agilent SPDT	Slot 2	40835-001	C

Note: The two SPDTs listed above are attached to the same adapter board.

After discovering the instrument in "Discovering and Configuring Your 1257A on a LAN" in **Chapter 3**, you can gain access to the system and the initial web page.

1. After you have selected your 1257A from the LXI discovery tool, the following Home Page screen appears. The page appearance is typical.

2. Click **Slot 2 – Driver Card** under Web Controls to verify the installed driver boards.

   Note: Full-feature functionality of the web page interface relies on the Java platform. This interface may cause Java security messages (similar to what is shown following) to appear.

   a. If a Windows Security and/or Java sign-on screen appears, use the factory default of "**admin**" as user name and no password (unless you have changed the user name and password) and click **OK**.

   b. The following screens are typical of what may appear depending on your installed version of Java. Choose the action as noted or similar.
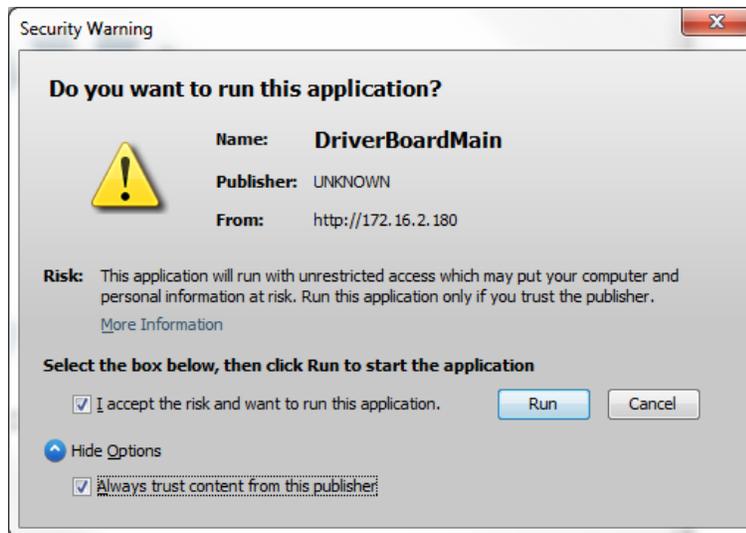
   • Click **Show Options** on the lower part of the screen, click the checkboxes of "**I Accept**…" and "**Always trust**…", and then **Run**.

- Click **Run with the latest version**.
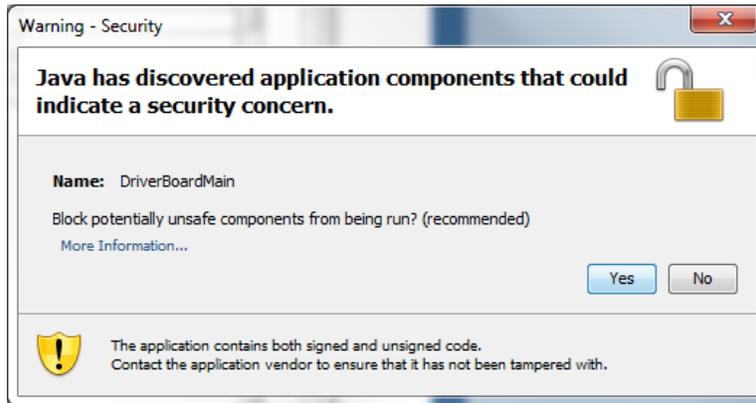
- Click **No**.

    *Important Note: If you click **Yes** in error, you will **not** be able to configure your system without going into your browser and deleting/reseting your saved security and browser settings.*

3. The **Main** tab appears. In our example, the driver board is not yet configured with any components.
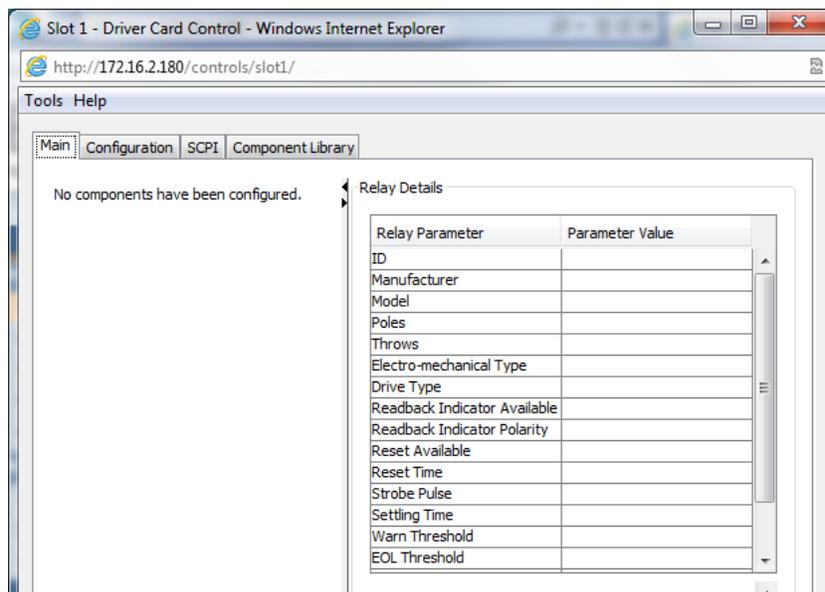
Adding Components to the Component Library

This next section is used only if your component is not already listed in your library. To check, click the **Component Library** tab and check the **Models** drop-down menu. (See following figure.)

If the component is listed, skip this section and proceed to **Configuring the Component to the Adapter Board**.
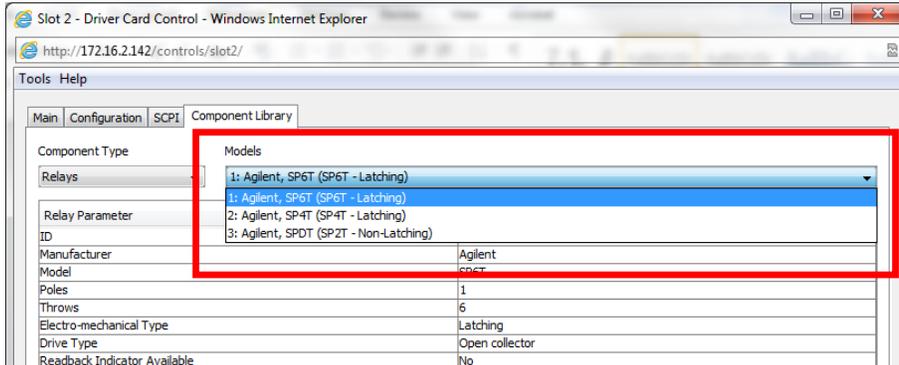
For our example, we will assume that the SP6T component we are installing is not yet in the library and needs to be added.

1. Select the **Component Library** tab, select Relays under **Component Type**, and click **Add**.
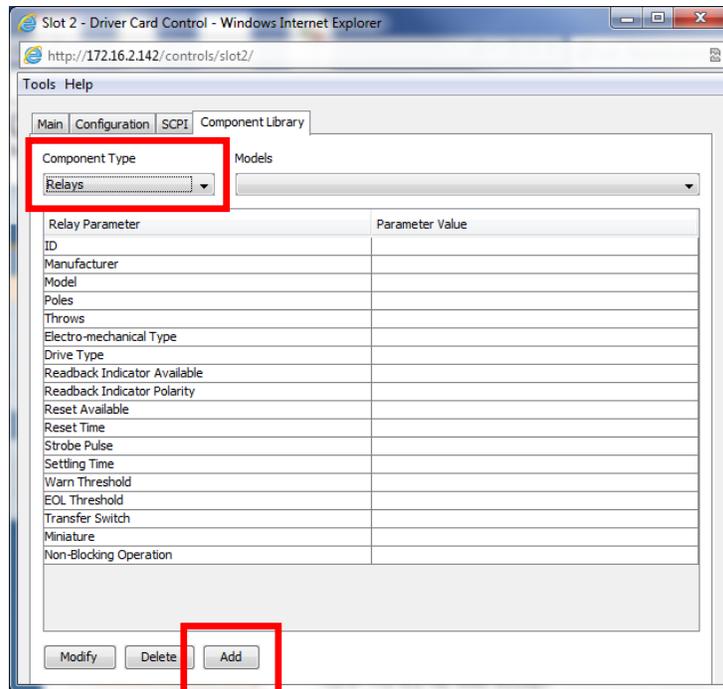
2. The **Edit Relay Parameters** screen appears. Enter information per manufacturer specifications.

3. Click **Submit** to enter the data into the Library. Once data is submitted, the **Edit Relay Parameter** screen still displays. Click **X** or **Cancel** to close the screen.

    You are ready to proceed to the next step to configure the relay to the system.

Configuring the Component to the Adapter Board

This section provides steps to configure your components to the installed adapter boards. After this, you will be able to use the component in your 1257A system.

1. Select the **Configuration** tab. This will show you the **Bank Selection** menu and the available Adapter Boards.
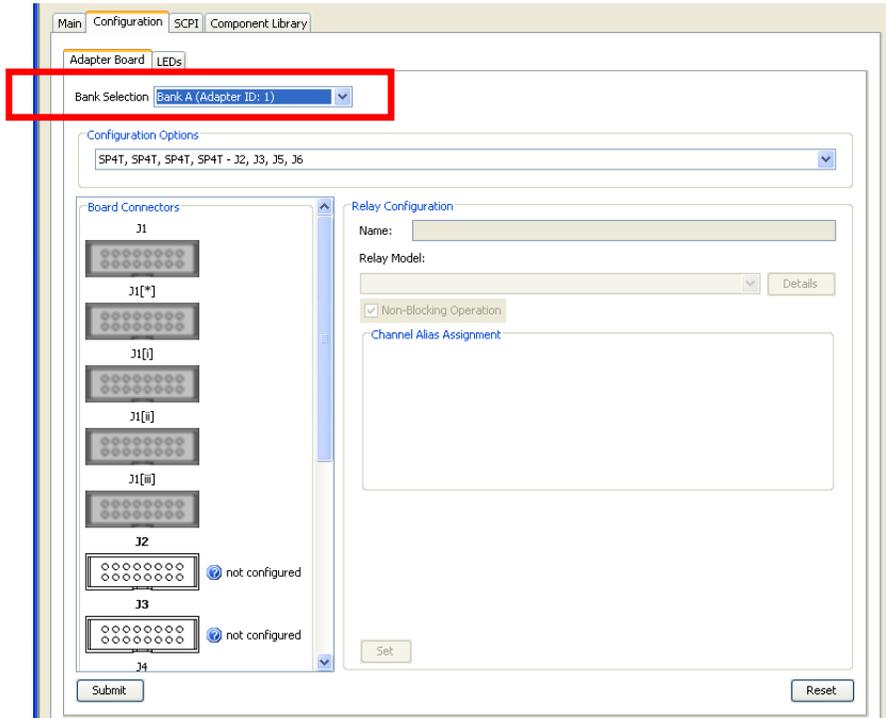
2. From the **Bank Selection** drop-down menu, select the Bank for the adapter board that you wish to configure. In this first example for the SP6T switch, we will select **Bank A**. (Refer to the table at the beginning of this appendix for this information.)
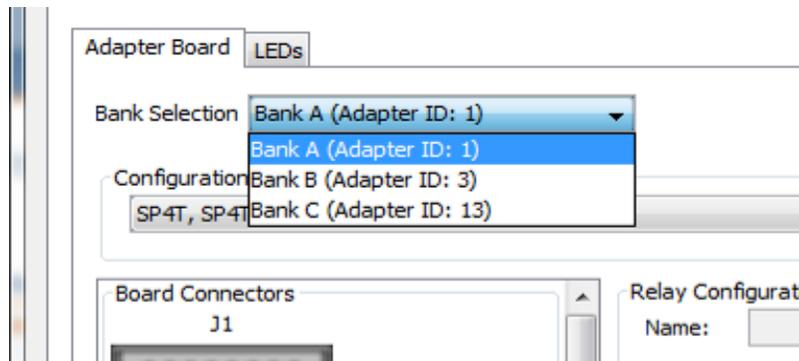
3. From the **Configuration Options** drop-down menu, choose the option that matches the type of component you wish to add. In this example, we will select the **SP6T** row to map to connector J1. *

   * In this case, we choose option SP6T, SP6T, SP4T – J1, J4, J6 so we can map an SP6T to connector J1 or J4, or for an SP4T to connector J6.
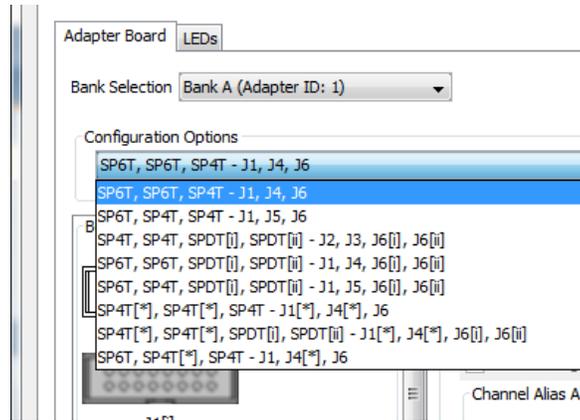
4. Click the **Board Connector** you have connected the relay to. Select connector J1. The connector will turn black when selected.

   If the connector is already grayed out, it is not available for your use. You may need to physically reconnect the relay to another one of the adapter board connectors seen as available on this screen.
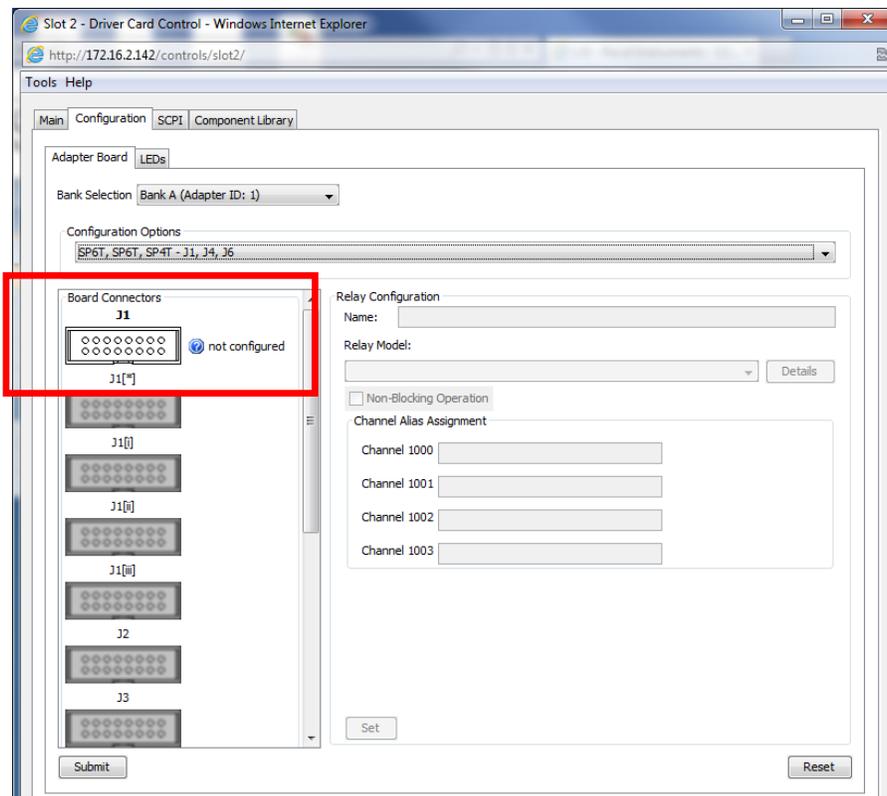
5. Provide an identifying **Name** for the component (i.e. SW1) and then choose the component to be configured by using the **Relay Model** drop-down menu for the SP6T relay in the library. (The dropdown will automatically be populated with components that can connect to the slot, based on the configuration option and connector selected. See steps 3 and 4.)
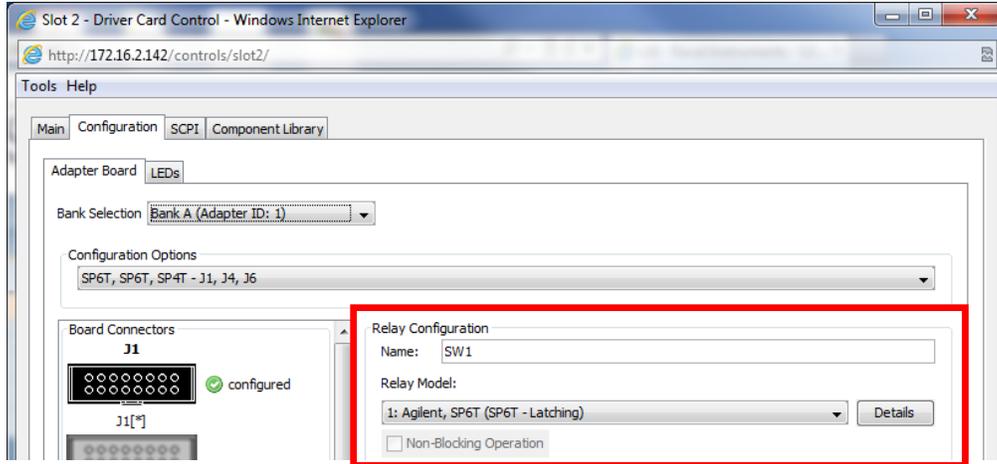
6. If desired, add a **Channel Alias Assignment** name/number.
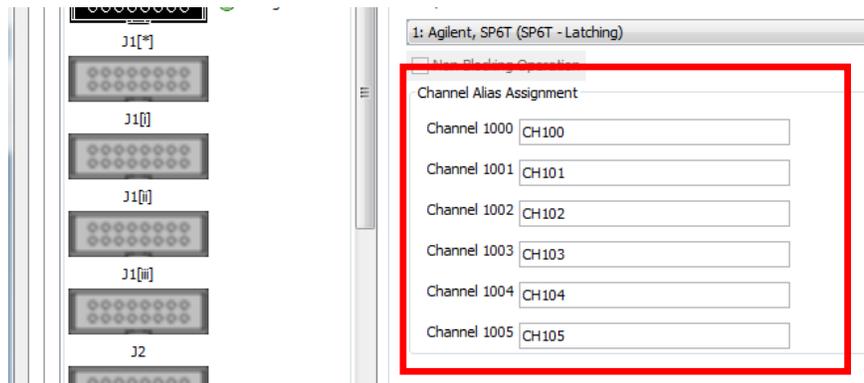
7. Click **Set** to save these details.

8. Now we will add the SP4T relay to Bank B. From the **Bank Selection** drop-down menu, select **Bank B** for the adapter board attached to that relay.
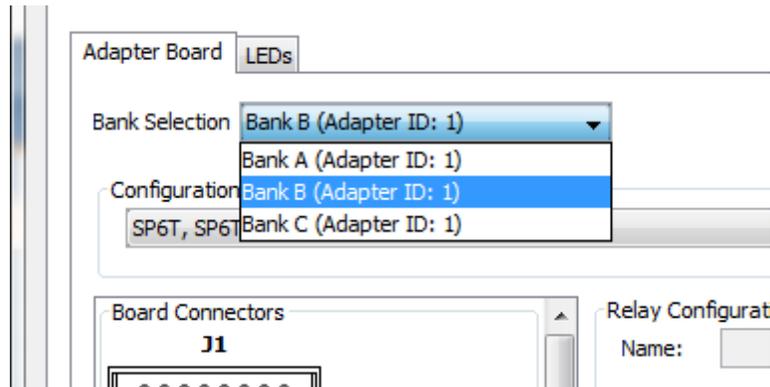
9. From the **Configuration Options** drop-down menu, choose the option that matches the type of component you wish to add. In this example, we will select the **SP4T** row.
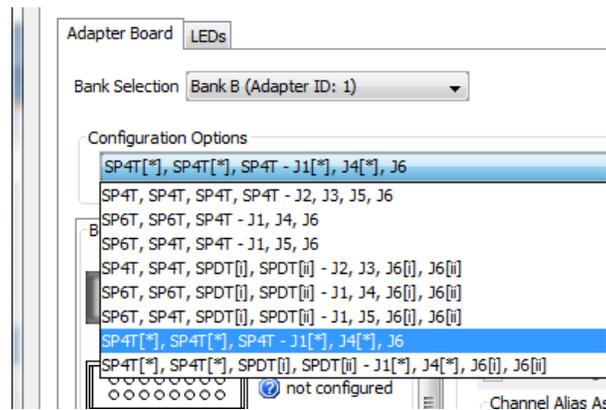
Note: *[*] indicates that the connector is shared between different component types. In this case, connector J1 [*] is used for connecting an SP4T relay while earlier in Bank A this connector was used to connect to an SP6T.*

10. Click the **Board Connector** you have connected the relay to. Select connector J1 [*]. The connector will turn black when selected.

    If the connector is already grayed out, it is not available for your use. You may need to physically reconnect the relay to another one of the adapter board connectors seen as available on this screen.
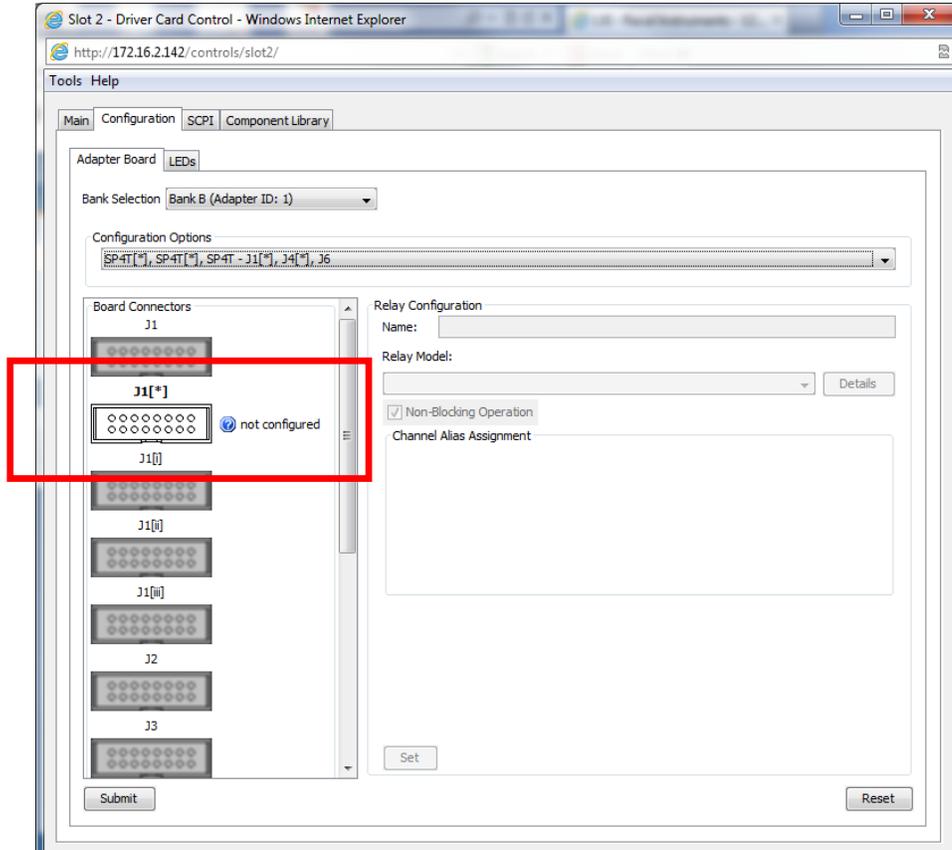
11. Provide an identifying **Name** for the component (i.e. SW2) and then choose the component to be configured by using the **Relay Model** drop-down menu for the SP4T relay in the library.
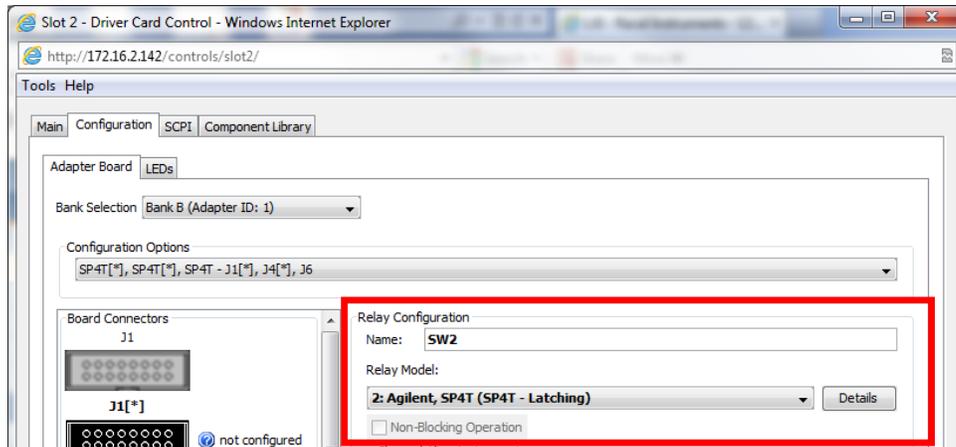
12. If desired, add a **Channel Alias Assignment** name/number.
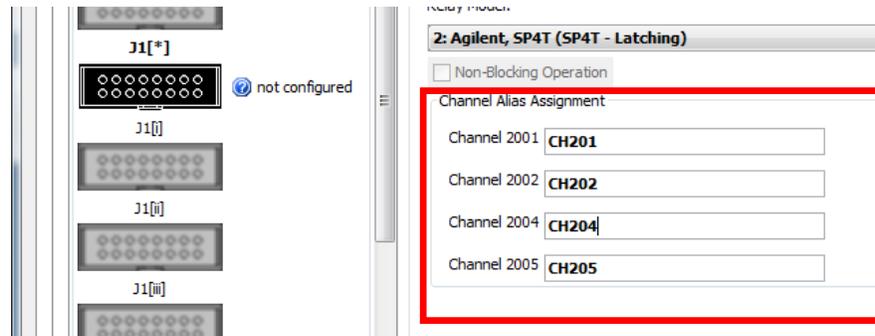
13. Click **Set** to save these details to Bank B.

14. Now we will add the first SPDT relay to Bank C. From the **Bank Selection** drop-down menu, select **Bank C** for the adapter board attached to that relay.
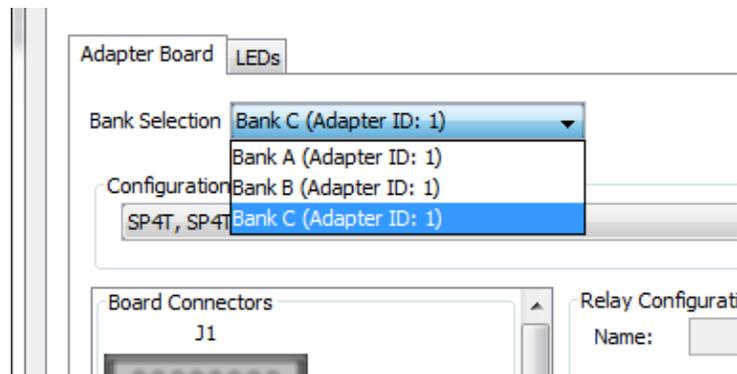
15. From the **Configuration Options** drop-down menu, choose the option that matches the type of component you wish to add. In this example, we will select the **SPDT** row.
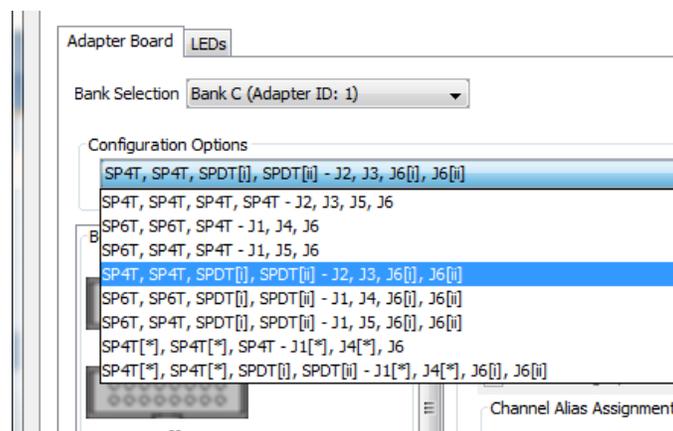
Note: *[i] and [ii] indicate that the connector would provide a split cable to control more than one component. In this case, connector J6 would provide a control cable to connect to 2 SPDTs.*

16. Click the **Board Connector** you have connected the relay to. Select connector J6 [i]. The connector will turn black when selected.

If the connector is already grayed out, it is not available for your use. You may need to physically reconnect the relay to another one of the adapter board connectors seen as available on this screen.

Note that with this option, there are [i] symbols after the SPDT and certain connectors. This indicates that you need to connect the SPDT relay to only those connectors on the adapter board.
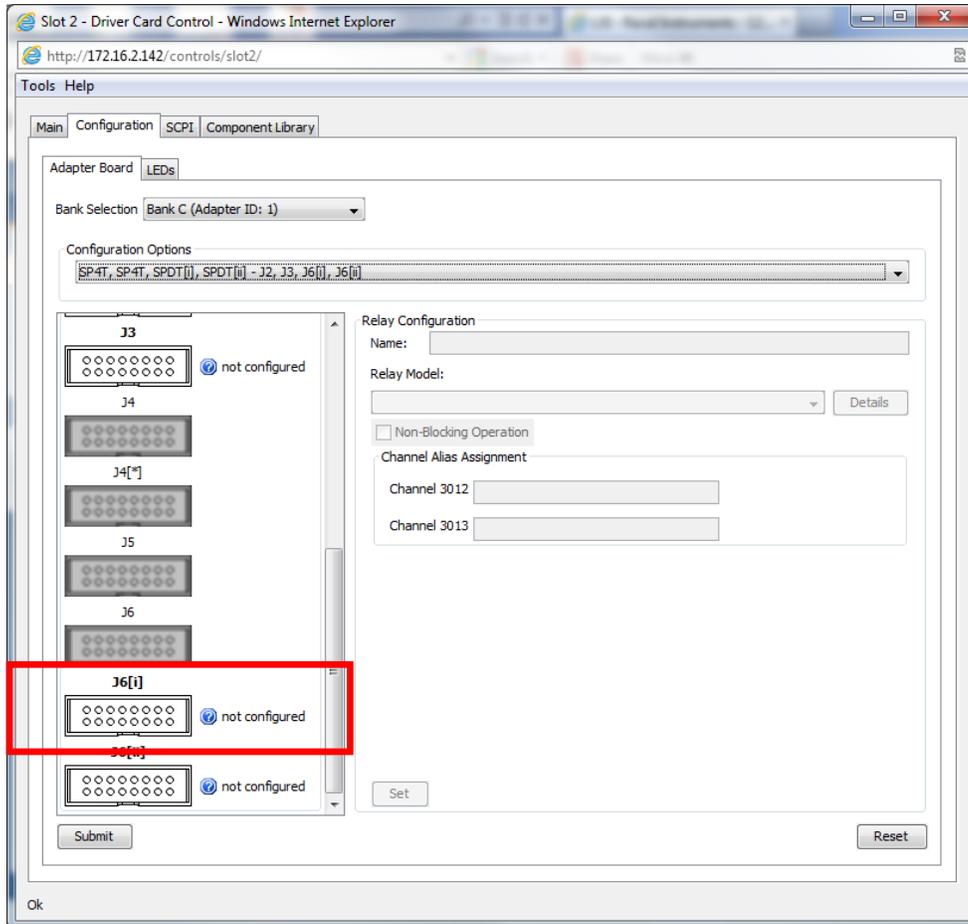
17. Provide an identifying **Name** for the component (i.e. SW3) and then choose the component to be configured by using the **Relay Model** drop-down menu for the SPDT relay in the library.
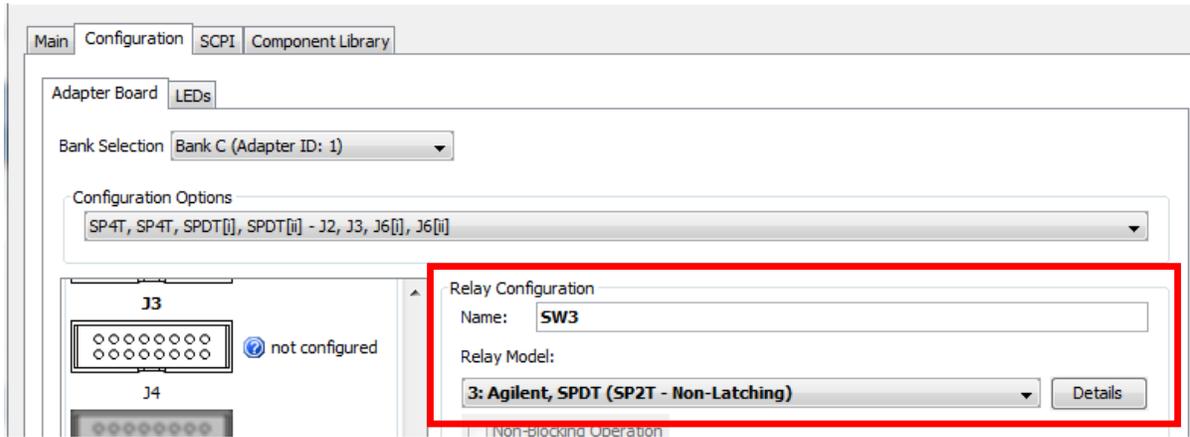
18. If desired, add a **Channel Alias Assignment** name/number.
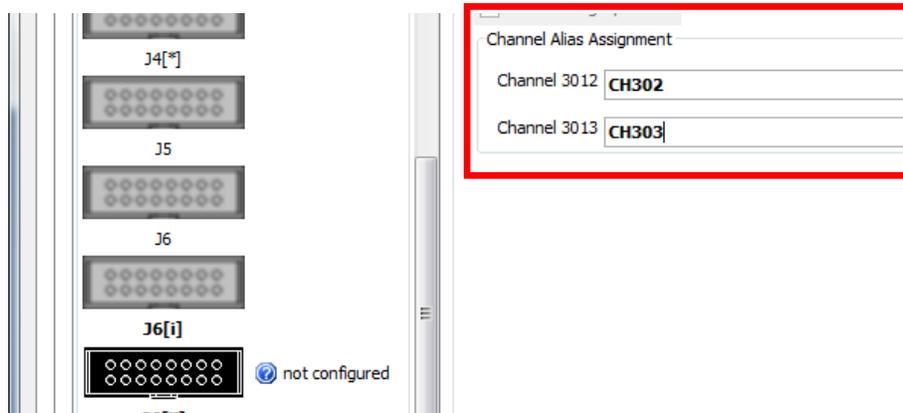
19. Click **Set** to save these details.

20. Finally, we will add the second SPDT relay. From the **Bank Selection** drop-down menu, select **Bank C** for the adapter board attached to that relay.
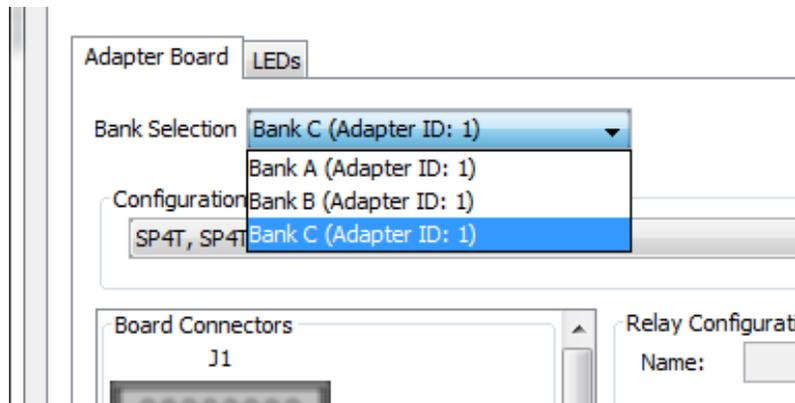
21. From the **Configuration Options** drop-down menu, choose the option that matches the type of component you wish to add. In this example, we will select the **SPDT** row.
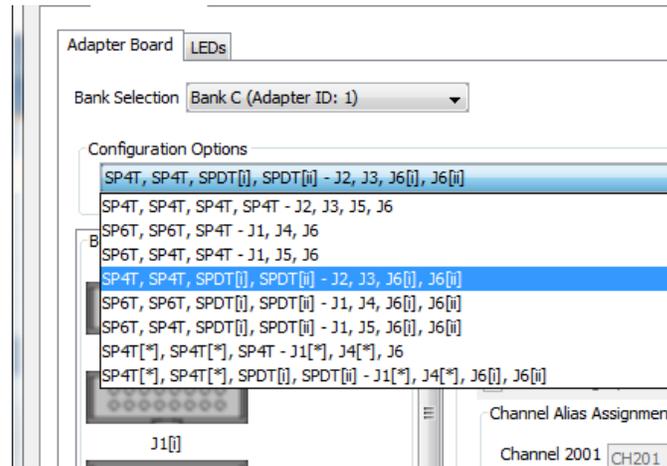
22. Click the **Board Connector** you have connected the relay to. Select connector J6 [ii]. The connector will turn black when selected.

If the connector is already grayed out, it is not available for your use. You may need to physically reconnect the relay to another one of the adapter board connectors seen as available on this screen.

Note that with this option, there are [i] symbols after the SPDT and certain connectors. This indicates that you need to connect the SPDT relay to only those connectors on the adapter board.
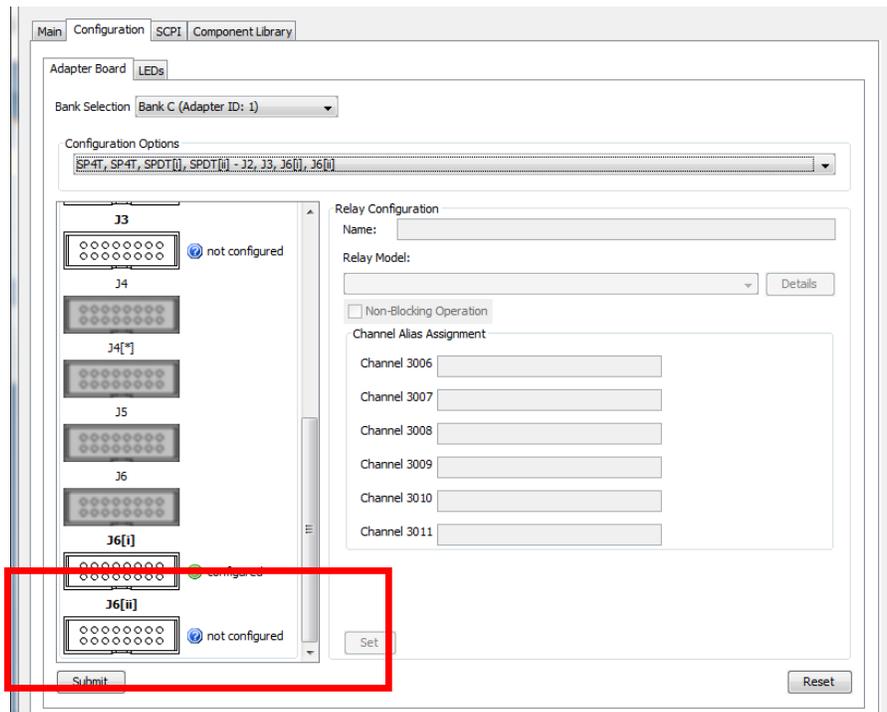
23. Provide an identifying **Name** for the component (i.e. SW4) and then choose the component to be configured by using the **Relay Model** drop-down menu for the SPDT relay in the library.
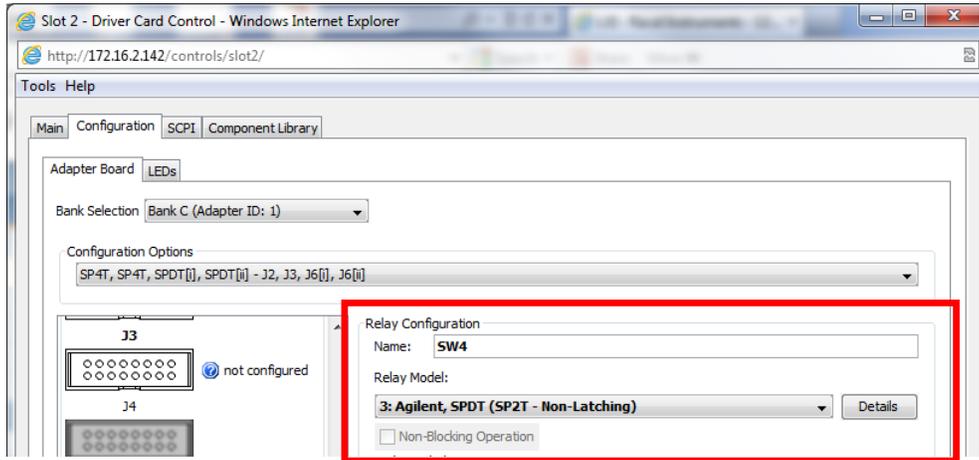
24. If desired, add a **Channel Alias Assignment** name/number.
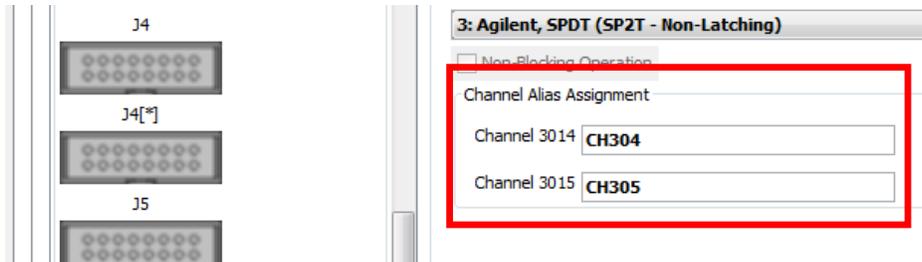
25. Click **Set** to save these details.

26. Since you are done configuring, click **Submit** to save the configuration to the 1257A unit.

27. Click **Yes** on the **Confirmation** screen.
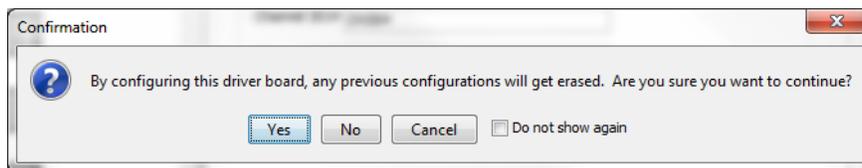
28. When you switch back to the **Main** tab, your updated components and configuration changes should be visible as shown in the next figure. (There might be a momentary delay before the changes are visible.)

Appendix E
Using the Configurator Software Tool

This appendix provides information on installing the Configurator Software Tool (PN 922725) and then to download (copy), modify, and upload the switch configuration of your 1257A.

Note: A requirement to run the Configurator Software Tool is a current version of the VISA software. You can obtain VISA at www.ni.com/visa.

Installing the Software

To install the Configurator, perform the following.

1. Locate the 922725 folder on the CD that came with your 1257A system and double-click on the setup.exe file. The Welcome screen (shown below) appears. Click **Next** to begin the installation process.

2. If satisfied with the settings on the screen, click **Next**. Otherwise, adjust as necessary and then click **Next** to continue.

    *Note: Be sure to identify where you have installed the program so you can easily locate it later. For your convenience, afterwards install a Desktop shortcut for the program file (*1257A_Configurator.msi)*.*
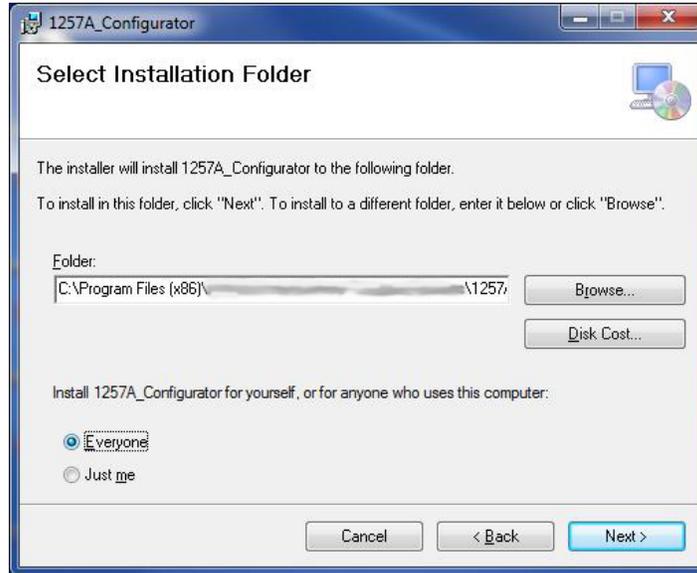
3. Click **Next** to begin the installation. If you receive a message asking if you want to install the program on your computer, click **Yes** or **Continue** to allow the installation.
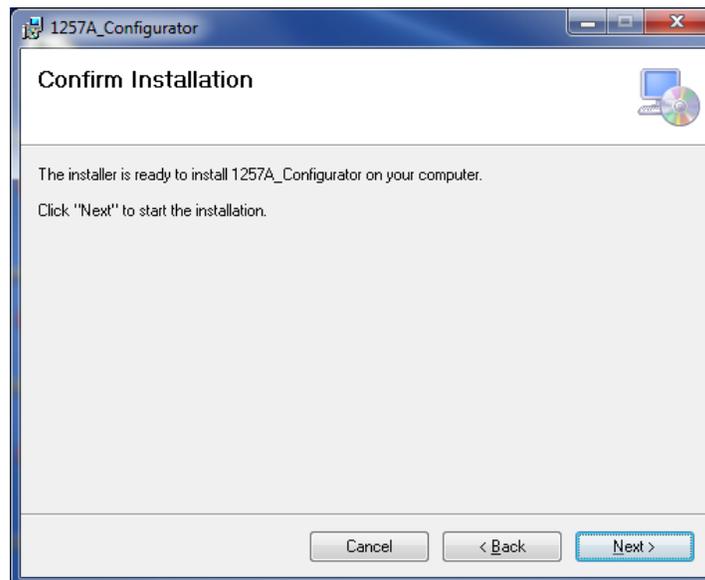
4. Click **Close** when successfully installed.
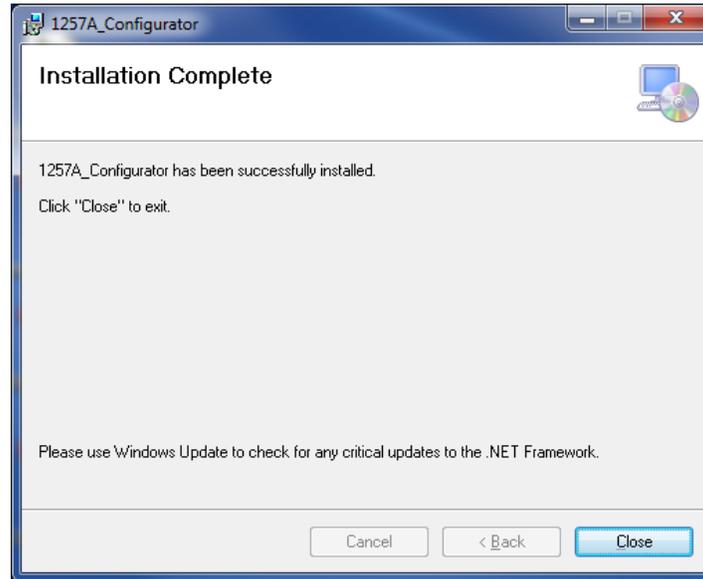
Downloading (Copying) the Configuration Information

To create a file with the current system configuration, perform the following.

1. Create an empty text (.txt) file using Windows Notepad or similar program.
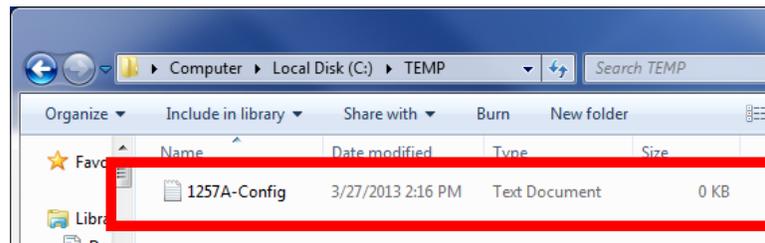
2. Launch the Configurator Tool.
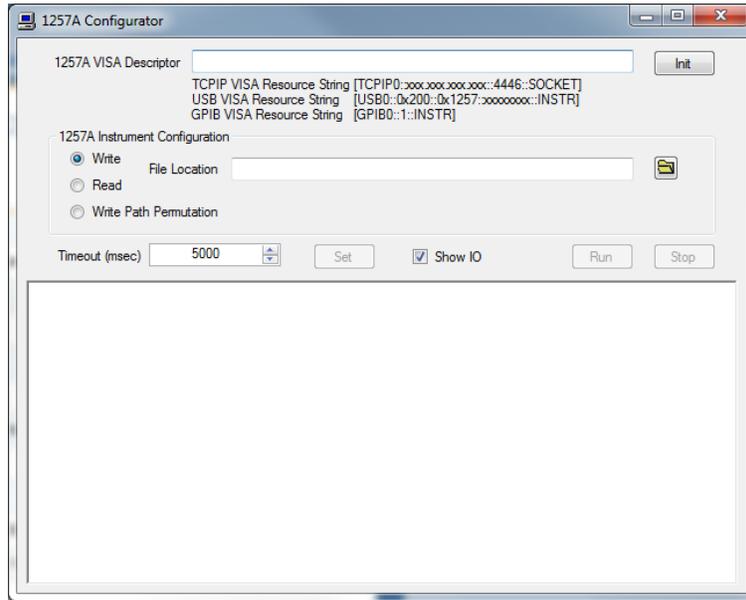
3. Go to the 1257A Home Page and identify the Instrument Address String.
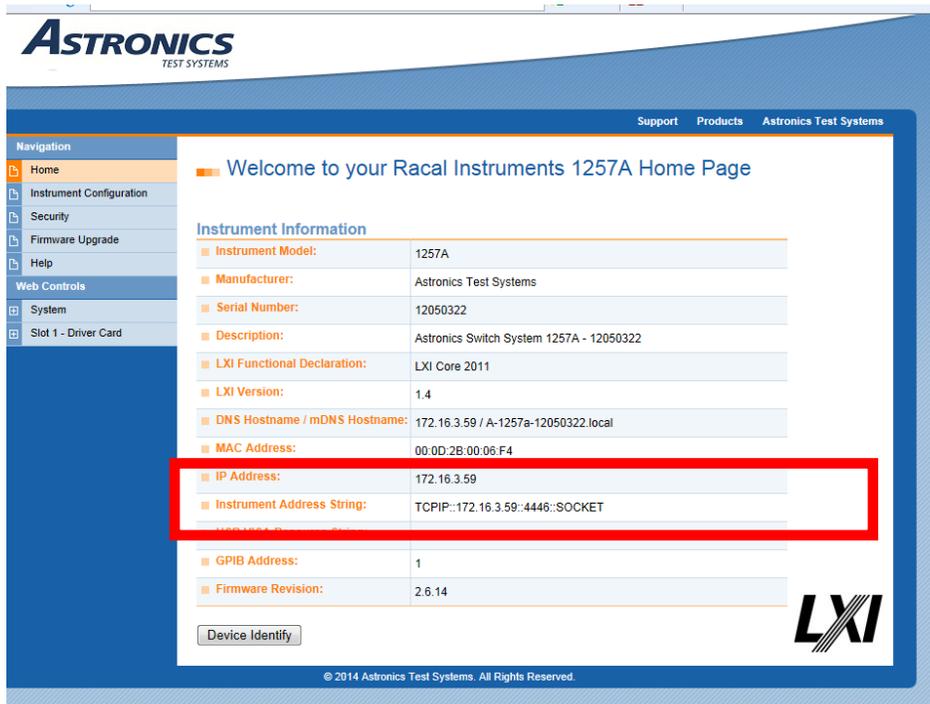
4. Copy the Instrument Address String and enter it into the 1257A VISA Descriptor box of the Configurator.

   Note: If using USB or GPIB for a connection, you can use the USB/GPIB VISA Resource String instead).)
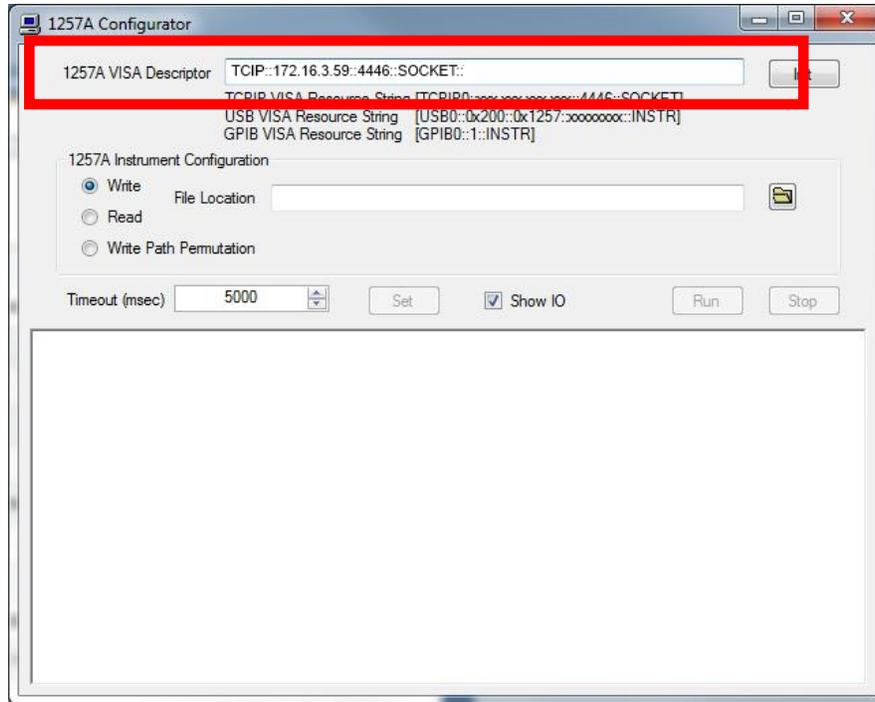
5. Use the **File Location** icon to locate the .txt file created in Step 1. Select the file and click **Open**.

6. Select **Read** (1) under 1257A Configuration. Click **Init** (2) to initialize the connection. Click **Set** (3) to set the timeout. Finally, click **Run** (4).
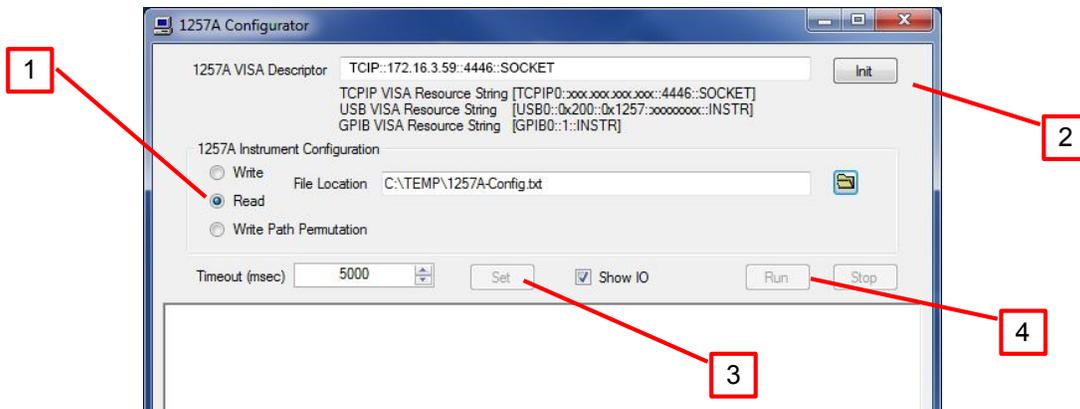
7. The text file will be populated with the current configuration like the example below.

8. Close the Configurator.

Modifying the Configuration File

The configuration file is a simple text (.txt) file that can be easily modified with Windows Notepad or similar program.

For instance, if after you uploaded new firmware to the 1257A you wanted to change the firmware version from the original 2.6.4.3 to 2.6.5, simply change the version information and then save the file.
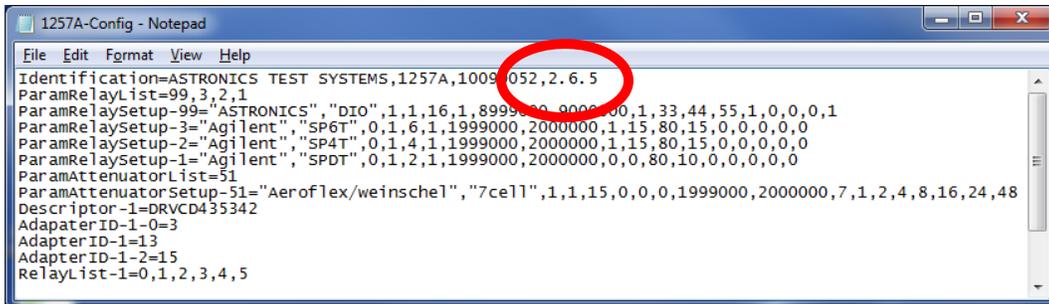
Uploading the Configuration Information

To upload a previously-created text file with a system configuration to your 1257A, perform the following.

1. Launch the Configurator Tool.
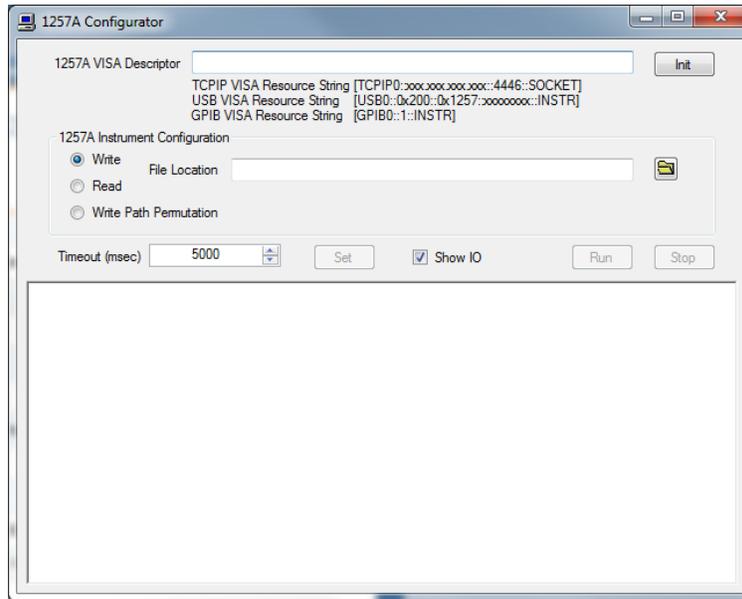
2. Go to the 1257A Home Page.
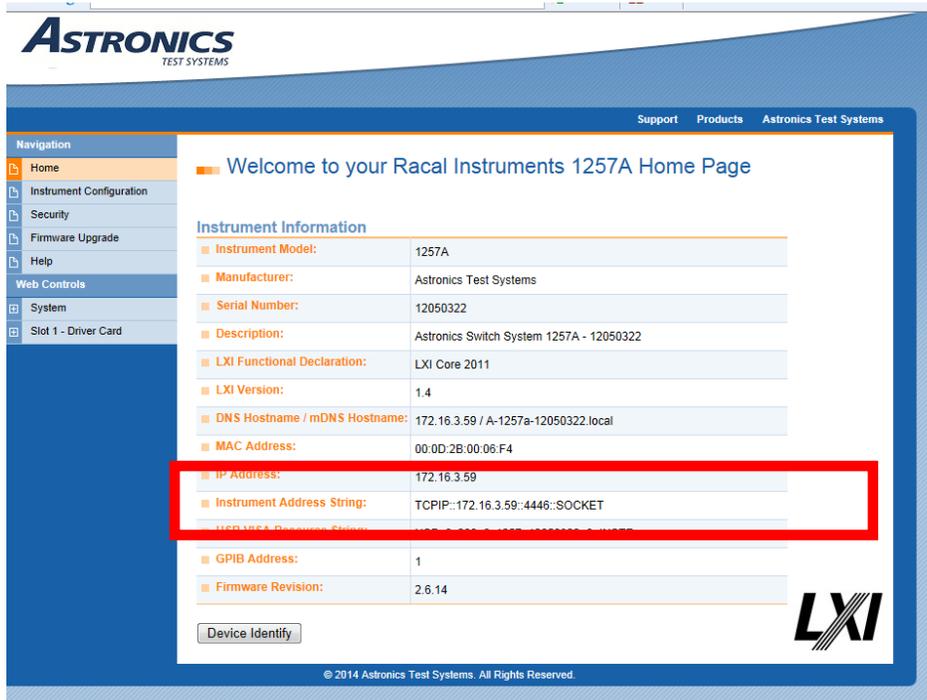
3. Copy the Instrument Address String and enter it into the 1257A VISA Descriptor box of the Configurator.

> *Note: If using USB or GPIB for a connection, you can use the USB/GPIB VISA Resource String instead).)*
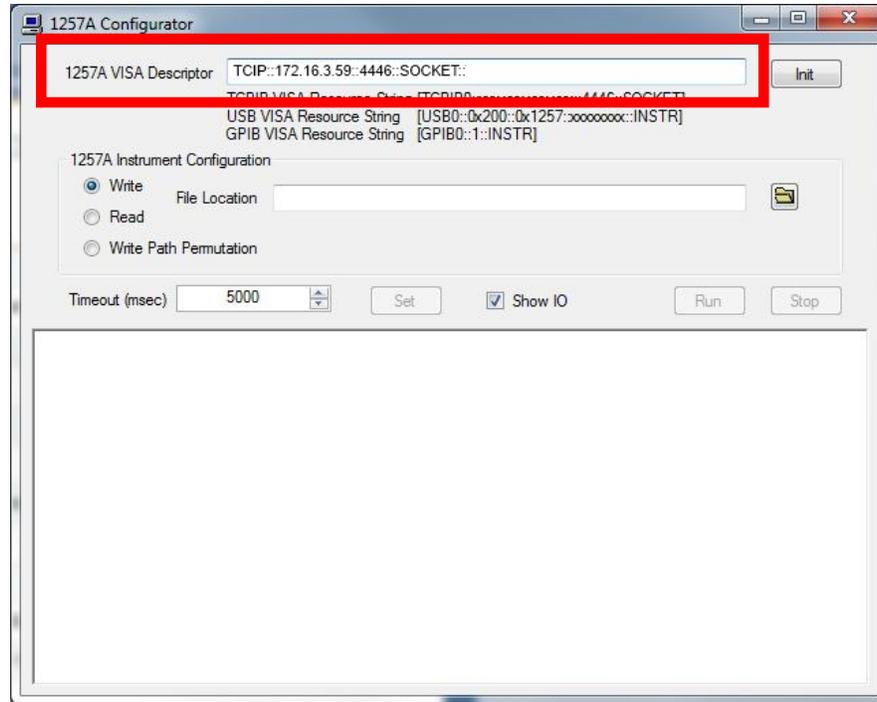
4. Use the **File Location** icon to locate the .txt file with the configuration information. Select the file and click **Open**.

5. Select **Write** (1) under 1257A Configuration. Click **Init** (2) to initialize the connection. Click **Set** (3) to set the timeout. Finally, click **Run** (4).
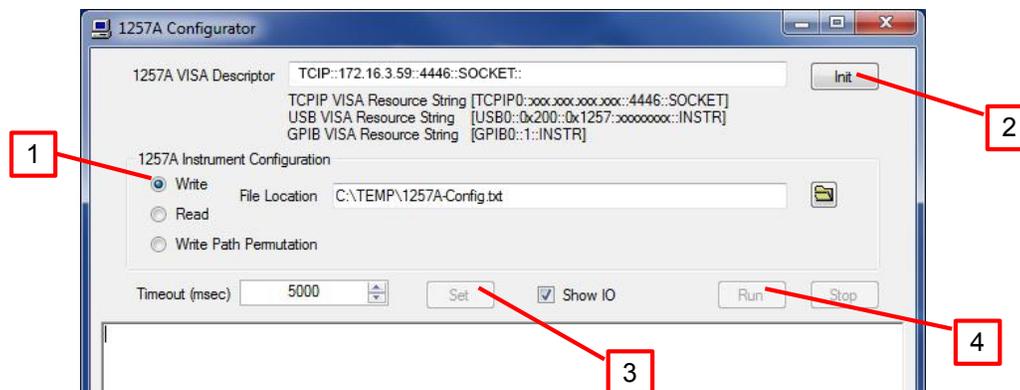
6. The 1257A will be loaded with the revised configuration. Restart the 1257A prior to using it again.

7. Close the Configurator Software Tool.

Appendix F
Rack-Mount Installation

Refer to Table 1-, Orderable Parts, in **Chapter 1, Overview and Features**, for part number information for rack-mount brackets and slides.

Rack-mount slides are only available for the 4U and 6U chassis.

Installing Rack-Mount Brackets (Ears)

Caution

DO NOT attempt to support the 1257A in the rack with the rack-mount brackets only. Right-angle runners or a base plate MUST be used to support the 1257A if slides are not used.

1. If installed, remove the rubber feet on the bottom of the 1257A by removing the screws that secure them.

2. Secure the rack-mount brackets to both sides of the 1257A chassis as shown in **Figure F-1**, using the supplied mounting screws.

Supplied Mounting Screws

Rack-Mount Bracket (Ear)
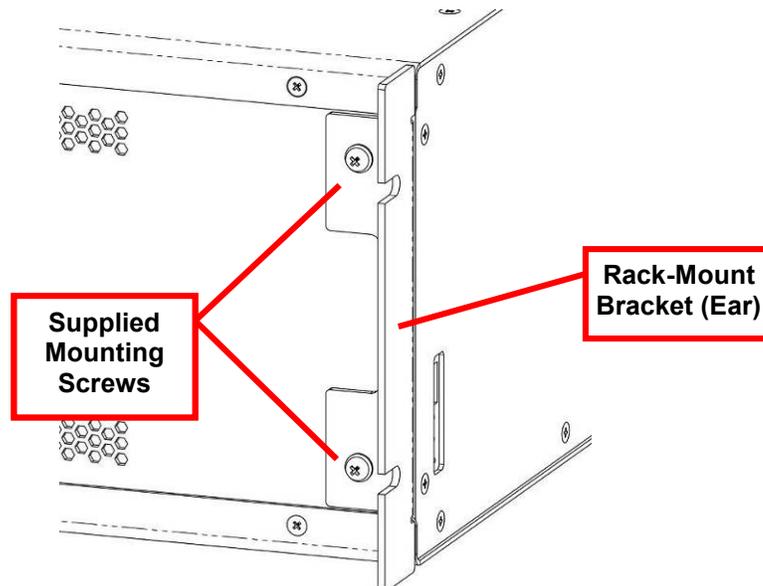
Figure F-1, Attaching Rack Mount Brackets (Appearance Typical)

Rack-Mounting Using Slides

Note

There is no need to remove the covers of the unit to attach the slides.

The slide-mount kit can be used to mount the 1257A into a standard 19-inch equipment rack.

1. Place system on a secure work surface.

2. Extend each slide to its full length and by pressing the release levers on the slide, remove the instrument mounting extension bracket from the rack mounting bracket.

3. Using the hardware found in the slide-mount kit, attach the instrument mounting extension bars to the 1257A.

4. Determine the mounting placement of the 1257A chassis in the rack and where the rack-mounting bracket should be located.

5. Install the rack-mounting bracket to the rack using the hardware provided.

6. Slide the 1257A chassis extension bracket into the rack-mounting bracket (**Figure F-2** – connection to equipment rack not shown in figure).

7. Fasten the 1257A chassis to the rack through the rack-mount brackets added in the previous section. (Screws for mounting to your rack frame are not supplied.)
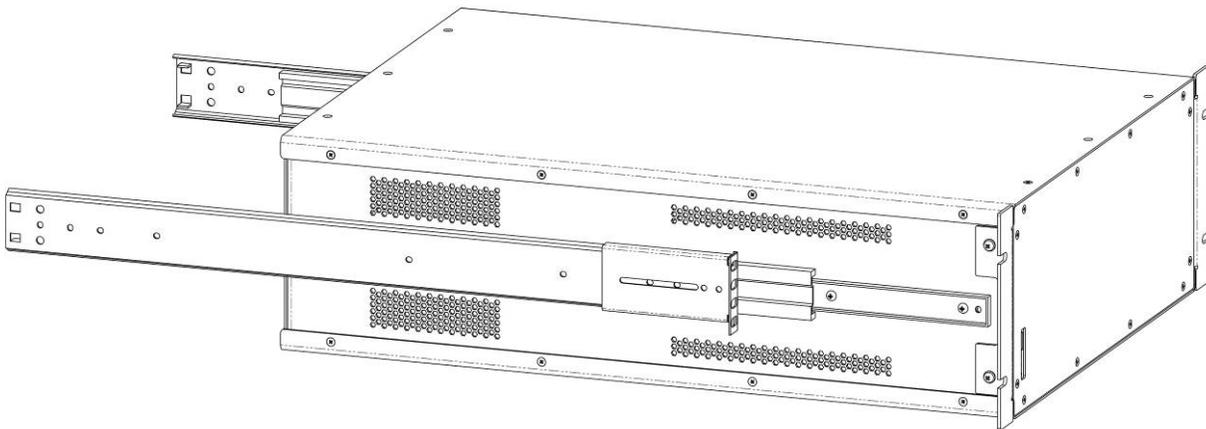

Figure F-2, Rack-Mounting Slides Attached to 1257A (Appearance Typical)