

RACAL INSTRUMENTS™
1260-160B/E
MICROWAVE SPDT
SWITCH PLUG-IN

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FOR YOUR SAFETY

Before undertaking any troubleshooting, maintenance or exploratory procedure, read carefully the **WARNINGS** and **CAUTION** notices.



CAUTION
RISK OF ELECTRICAL SHOCK
DO NOT OPEN



This equipment contains voltage hazardous to human life and safety, and is capable of inflicting personal injury.



If this instrument is to be powered from the AC line (mains) through an autotransformer, ensure the common connector is connected to the neutral (earth pole) of the power supply.



Before operating the unit, ensure the conductor (green wire) is connected to the ground (earth) conductor of the power outlet. Do not use a two-conductor extension cord or a three-prong/two-prong adapter. This will defeat the protective feature of the third conductor in the power cord.



Maintenance and calibration procedures sometimes call for operation of the unit with power applied and protective covers removed. Read the procedures and heed warnings to avoid “live” circuit points.

Before operating this instrument:

1. Ensure the proper fuse is in place for the power source to operate.
2. Ensure all other devices connected to or in proximity to this instrument are properly grounded or connected to the protective third-wire earth ground.

If the instrument:

- fails to operate satisfactorily
- shows visible damage
- has been stored under unfavorable conditions
- has sustained stress

Do not operate until performance is checked by qualified personnel.

EC Declaration of Conformity

We

Astronics Test Systems
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Irvine, CA 92618

declare under sole responsibility that the

**1260-160A,-160B,-160C,160D,160E,160F
RF SPDT Switch Plug In Module
P/N 407766-001,-002,-003,-004,-005,-006**

conforms to the following Product Specifications:

Safety: EN 61010-1

EMC: Immunity: EN61326, Class A, Table 1
Emissions: EN61326, Class A, Table 3

Supplementary Information:

The above specifications are met when the product is installed in an Astronics Test Systems certified mainframe with faceplates installed over all unused slots, as applicable.

The product herewith complies with the requirements of EN61010-1 and EN61326.

Irvine, CA, January 15, 2002


Quality Manager

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DOCUMENT CHANGE HISTORY

Revision	Date	Description of Change
A	6/29/09	Revised per EO 29778 Revised format to current standards. Company name revised throughout manual. Manual now revision letter controlled. Added Document Change History Page v Back of cover sheet. Revised Warranty Statement, Return of Product, Proprietary Notice and Disclaimer to current standards. Removed Reshipment Instructions in (Chap. 2-1) and removed (Chap 4). Information. Now appears in first 2 sheets behind cover sheet. Updated table of contents to reflect changes made. . Added company name to footer at lower corner opposite of Page no's i thru iv.

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Chapter 1

SPECIFICATIONS

Introduction – 1260-160B/E

The 1260-160B and 1260-160E are microwave plug-in switch modules developed for a variety of platforms such as the 1260-100 Adapt-a-Switch™ Carrier and the 1256 Switching System. These are software-controllable 2 and 5 SPDT microwave switches for DC to 18 GHz.

The 1260-160 modules include the following features:

- Standard Adapt-a-Switch™ and 1256 Switching System plug-in design, providing for ease of replacement.
- Data-Driven embedded descriptor, allowing immediate use with any platform compatible with the Adapt-a-Switch standard, regardless of firmware level.

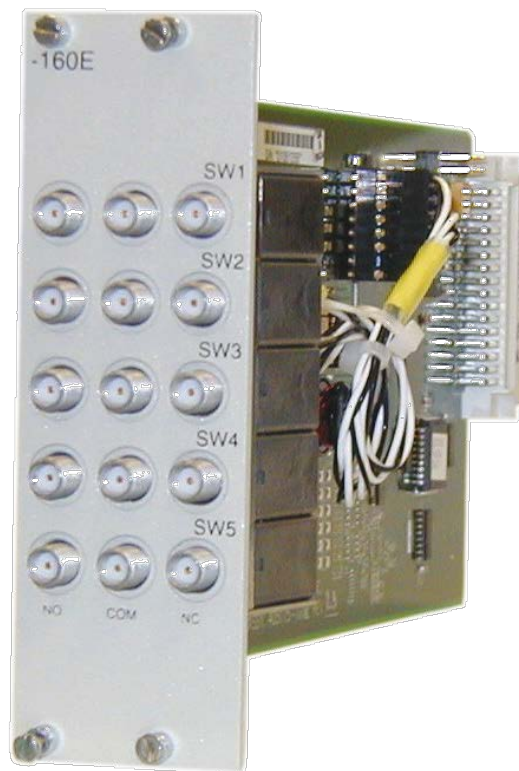


Figure 1-1, 1260-160E

Specifications – 1260-160B/E

Input / Output Specifications

Frequency Range (GHz)	DC-4	4-8	8-12.4	12.4-18
VSWR (Max dB)	1.15:1	1.25:1	1.35:1	1.5:1
Insertion loss (Max dB)	0.2	0.3	0.4	0.5
Isolation (Max dB)	80	70	65	60
RF Input Power	120W 3GHz (VSWR 1.15 or less, no contact switching, 40°C ambient)			
Contact Rating, Max.	30V, 100ma			
Relay Operate Time	15m sec typical			
Switch Contact Lifetime	5 Million cycles per position			
Available I/O Channels	Single SPDT Microwave Switch			
Shock	30g, 11 ms, ½ sine wave			
Vibration	0.013 in. P-P, 5-55 Hz			
Bench Handling	4 in., 45°			
Cooling	See 1260-100 cooling data			
Temperature				
Operating	-20°C to +60°C			
Non-operating	-40°C to +75°C			
Relative Humidity	95 +/-5% RH non condensing; 75+/-5 %RH above 30°C; 45+/-5 %RH above 40°C			
Altitude				
Operating	10,000 feet			
Non-operating	15,000 feet			
Power Requirements +5 VDC Amps Maximum				
1260-160B	0.42 amps			
1260-160E	0.94 amps			
Weight				
1260-160B	6.1 oz	173 gm		
1260-160E	11.3 oz	320 gm		

Mean Time Between Failures (MTBF)	860,000 hrs. Calculated per MIL-HBK-217, ground-benign, 30°C, as design goal excluding relays. (microwave relay MTBF 5,000,000 operations per switch at rated load)
Mean Time to Repair (MTTR)	< 5 minutes

Power Dissipation – 1260-160B/E

The cooling of the Adapt-a-Switch carrier is dependent upon the chassis into which it is installed. The carrier can nominally dissipate approximately 100W. Even with all channels driven to maximum outputs, any combination and quantity of 1260-160 plug-ins may be used together in a 1260-100 without exceeding the maximum allowable power dissipation of the carrier.

If the 1260-160 will be used in conjunction with other cards, the dissipation should be computed and summed with the total worst-case dissipation of the remaining modules.

For example, a 1260-160 module would dissipate the following energy:

Quiescent power dissipation = 0.33 W maximum

Each switch energized = 0.88 W maximum

For example, a 1260-160E module would dissipate the following energy:

Quiescent power dissipation = 0.33 W maximum

With five coil energized = 4.73 W maximum

This is acceptable power dissipation for an individual plug-in module. If three additional module are likewise loaded, then the overall carrier dissipation is approximately 8.36 W for four –160B and 18.92 W for 4 –160E, both of which are well within the cooling available in most commercial VXIbus chassis.

Ordering Information

Listed below are part numbers for both the 1260-160 switch module and available mating connector accessories. Each 1260-160 uses SMA mating connectors.

ITEM	DESCRIPTION	PART #
1260-160B Microwave Switch Module	Switch Module, 2 (SPDT) DC-18 GHz Consists of: P/N 405175-160B PCB Assy P/N 980824-160 Manual	407766-002
1260-160E Microwave Switch Module	Switch Module, 5 (SPDT) DC-18 GHz Consists of: P/N 405175-160E PCB Assy P/N 980824-160 Manual	407766-005
Additional Manual		980824-160

Chapter 2

INSTALLATION INSTRUCTIONS

Unpacking and Inspection

1. Remove the 1260-160B/E module and inspect it for damage. If any damage is apparent, inform the carrier immediately. Retain shipping carton and packing material for the carrier's inspection.
2. Verify that the pieces in the package you received contain the correct 1260-160B/E module option and the 1260-160B/E Users Manual. Notify our Customer Support if the module appears damaged in any way. Do not attempt to install a damaged module into a VXI chassis.
3. The 1260-160B/E module is shipped in an anti-static bag to prevent electrostatic damage to the module. Do not remove the module from the anti-static bag unless it is in a static-controlled area.

Installation

For instructions on installing the 1260-160 into a switching platform, refer to the user manual for that platform, in the “Getting Started” chapter under the “Inserting and Removing Plug-ins” section. Manuals are available at the Astronics Test Systems website: www.astronictestsystems.com.

Module Configuration

The 1260-160 modules are software-selectable multiplexer plug-ins for switching platforms such as Adapt-a-Switch and 1256 System. The 1260-160s are two SPDT microwave switches for the –160B, and five SPDT microwave switches for –160E.

**Front Panel
Connectors 1260-
160B**

The 1260-160B has two front panel microwave relays, labeled SW1 and SW2, with 3 SMA connectors each. See **Figure 2-1** for SMA connector designations. See **Figure 2-2** for the relay diagram and **Figure 2-3** for a block diagram of the 1260-160B.

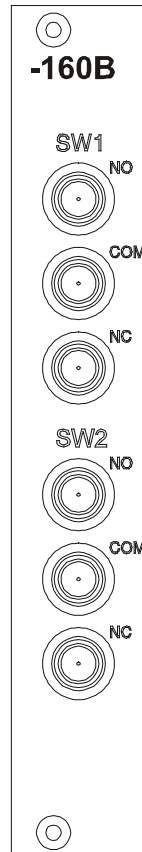


Figure 2-1, 1260-160B SMA Connector Designations

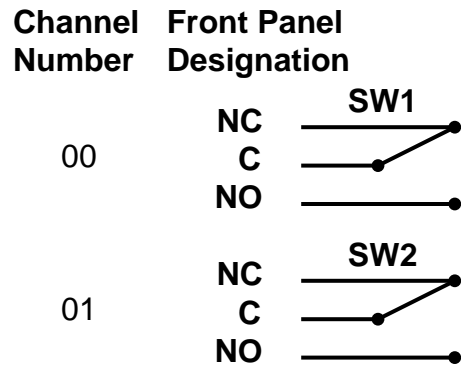


Figure 2-2, 1260-160B Relay Diagram

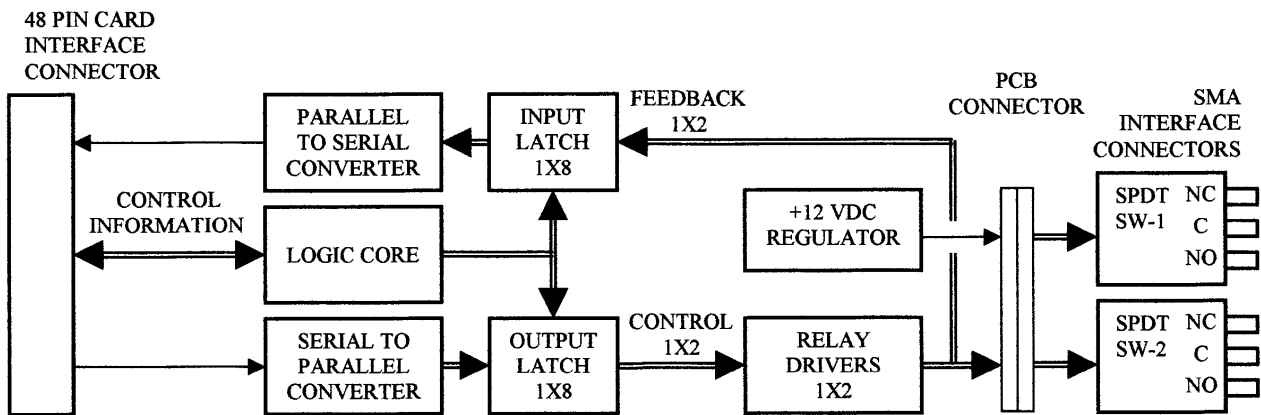


Figure 2-3, 1260-160B Block Diagram

Front Panel Connectors 1260- 160E

The 1260-160E has five front panel microwave relays, labeled SW1 through SW5, with 3 SMA connectors each. See **Figure 2-4** for SMA connector designations. See **Figure 2-5** for the relay diagram and **Figure 2-6** for a block diagram of the 1260-160E.

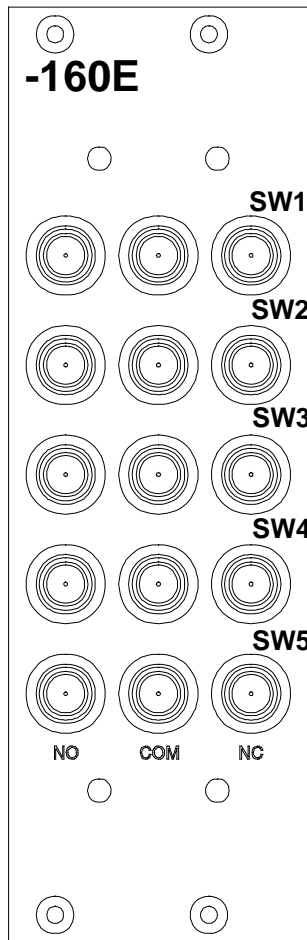


Figure 2-4, 1260-160E SMA Connector Designations

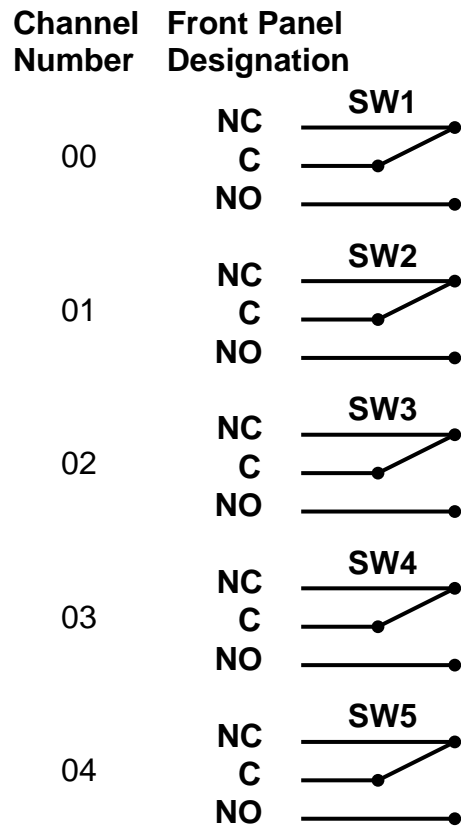


Figure 2-5, 1260-160E Relay Diagram

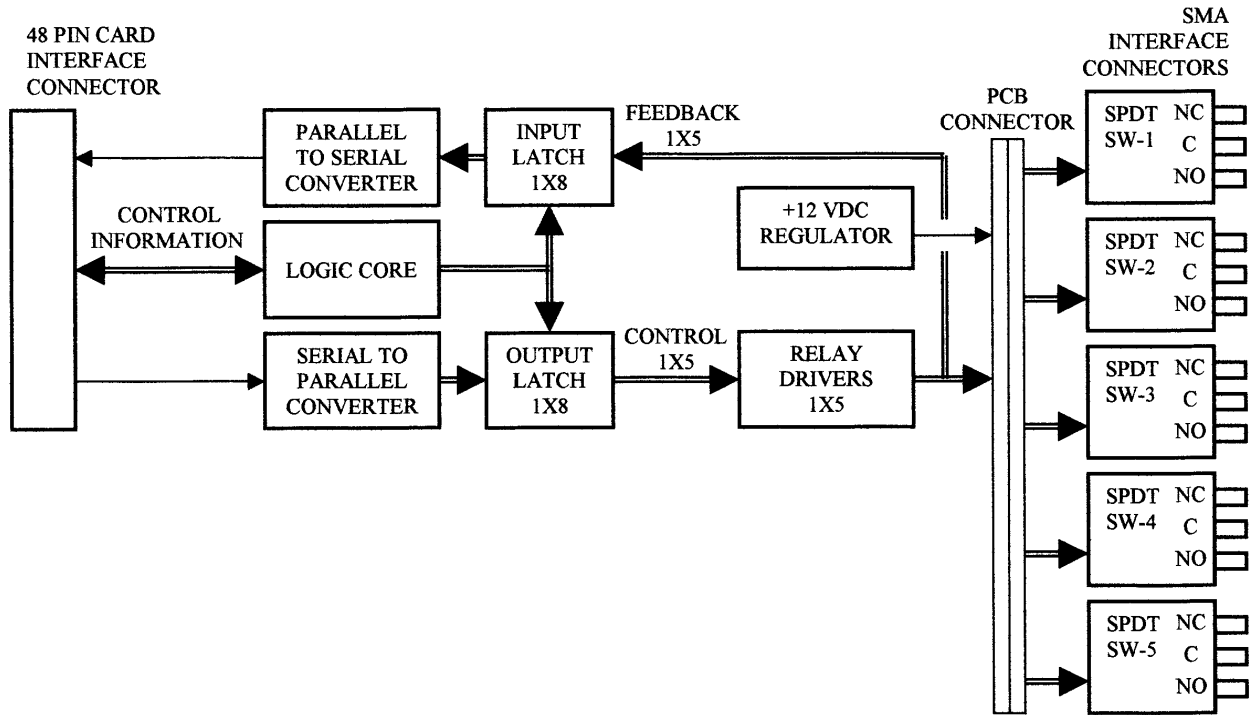


Figure 2-6, 1260-160E Block Diagram

Mating Connectors

Mating connectors are SMA type. Use connectors that are suitable for the type of connecting coax and frequency range to be used. A ¼ inch drive Deep Slotted Socket, P/N 456890, is available for installation and removal of connectors.

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Chapter 3

MODULE OPERATION

Reply to the MOD:LIST? Command

The platform containing the 1260-160 returns a reply to the MOD:LIST? command. This reply is unique for each different 1260 series switch module. The syntax for the reply is:

<module address> : <module-specific identification string>

The value of <module-specific identification string> for the 1260-160 depends on the version (1260-160B and 1260-160E). For the dual SPDT switch (1260-160B), the string value is:

```
1260-160B 2 SPDT RF SWITCHING MODULE
```

For the five SPDT switch (1260-160E), the string value is:

```
1260-160E 5 SPDT RF SWITCHING MODULE
```

Thus, for a 1260-160B whose module address is 2, the reply to this query would be:

```
2 : 1260-160B 2 SPDT RF SWITCHING MODULE
```

Operating in Register-Based Mode

The 1260-160 offers register-based mode when installed in VXI platforms that support it. In register-based mode, the 1260-160 is operated by directly writing and reading to/from ports controlling six relays each. To access the various registers the following details must be assembled to generate an absolute address that can be wrote or read from:

The port and control registers are located in the VXIbus A24 Address Space. The A24 address for a port or control register depends on:

1. The A24 Address Offset assigned to the 1260-01T module by the Resource Manager program. The Resource Manager program is provided by the VXIbus slot-0 controller vendor. The A24 Address Offset is placed into the "Offset Register" of the 1260-01T by the Resource Manager.
2. The <module address> of the 1260-160 module. This is a value in the range from 1 and 12 inclusive.
3. The 1260-160 port or control register to be written to or read from. Each register on the 1260-160 has a unique offset from the base address.

The base A24 address for the 1260-160 module may be calculated by:

$$(A24 \text{ Offset of the } 1260-01T) + (1024 \times \text{Module Address of } 1260-160).$$

The A24 address offset is usually expressed in hexadecimal. A typical value of 204000_{16} is used in the examples that follow.

A 1260-160 with a module address of 7 would have the base A24 address computed as follows:

$$\begin{aligned} \text{Base A24 Address of } 1260-160 &= 204000_{16} + (400_{16} \times 7_{10}) \\ &= 205C00_{16} \end{aligned}$$

The port and control registers for Adapt-a-Switch plug-ins and conventional 1260-Series modules are always on odd-numbered A24 addresses. For port registers, the 1260-160 reads and writes to the same location. For control registers, the 1260-160 writes to one location, but reads back from another. **Table 3-1** provides offsets relative to the base address of the module for all port and control registers of the 1260-160. To obtain the absolute address where data is to be written or read from, the base address is added to the offset:

(Base A24 1260-160 Address) + offset = absolute address

So, for our example base A24 address computed earlier, the following absolute addresses would apply for the operations indicated:

- 205C01 Port A read or written at this location
- 205E01 ID register read at this location

Before explaining the particulars of reading and writing to port and control registers, it is necessary to understand how the registers interact with the 1260-160 relays. **Table 3-2 through 3-4** provide a detailed explanation of each register and how it interacts with the 1260-160 module.

Table 3-1, Register Offset Addresses of the 1260-160 Module

Register Name	Register Offsets to Add to Base Module Address	
	Write Location (hexadecimal)	Read Location (hexadecimal)
Port A	0x01	0x01
ID	Read Only	0x201
EPROM Descriptor	Read Only	0x203

Table 3-2, ID Register Functionality of the 1260-160

Register Table		ID Register
Module Version	Bit	Functionality Description
All	0	Always Reads 0x00 (Read Only)
	1	
	2	
	3	
	4	
	5	
	6	
	7	

Table 3-3, Port A Register Functionality of the 1260-160 Module

Register Table		Port A
Module Version	Bit	Functionality Description
B, E	0	Relay SW1-1 (0: switch NC 1: switch closed)
B,E	1	Relay SW2 (0: switch NC 1: switch closed)
E	2	Relay SW3 (0: switch NC 1: switch closed)
E	3	Relay SW4 (0: switch NC 1: switch closed)
E	4	Relay SW5 (0: switch NC 1: switch closed)
	5	(not used)
	6	(not used)
	7	(not used)

Table 3-4, EPROM Descriptor Functionality of the 1260-160 Module

Register Table		EPROM Descriptor Register
Module Version	Bit	Functionality Description
All	0	Each time this register is read, it advances a memory pointer to the next memory location in the on-board EPROM. To reset this pointer to the beginning, read the ID register. This resets the memory pointer. The descriptor register contains a long string of data, typically used by the Adapt-a-Switch carrier for configuration purposes. Additionally, this data contains the card identification string for the specific type of card (i.e. 1260-160B or 1260-160E). These identification strings are located at EPROM memory locations 0x23 through 0x34.
	1	
	2	
	3	
	4	
	5	
	6	
	7	

Writing to a port location is a straightforward process. Setting a bit high in a port register causes the corresponding relay channel to close.

It is especially important to realize that a single write operation controls eight separate control lines or output devices simultaneously. Therefore if only a single bit change is desired, the following process must be observed.

1. Read the register, inverting the bit pattern.
2. Mask the appropriate bit with an 'AND' operation and a byte mask with all undesired bits set to a '1' and the desired bit set to a '0' or '1' depending on whether the bit is to be set or cleared in the desired register.

3. Write the masked data back into the register.

As simple as this may seem, a number of products reported as faulty and sent back for repair are typically the result of inappropriate register accesses.

Because of the 1260-160 relay driver architecture, registers A and B will read back inverted from what was written to them.

The VISA I/O library may be used to control the module. The VISA function `viOut8()` is used to write a single 8-bit byte to a control register, while `viIn8()` is used to read a single 8-bit byte from the control register. The following code example shows the use of `viOut8()` to update the 1260-160 module.

1260-160 Example Code

```
#include <visa.h>

/* This example shows a 1260-01T at logical address 16 and a VXI/MXI */
/* interface */
#define RI1260_01_DESC      "VXI::16"

/* For a GPIB-VXI interface, and a logical address of 77 */
/* the descriptor would be: "GPIB-VXI::77" */

/* this example shows a 1260-160 with module address 7, port 1,
and write data of 0xAA */
#define MOD_ADDR_160 7
#define PORT_NUMBER 1
#define DATA_ITEM    0xAA

void example_operate_1260_160(void)
{
    ViUInt8 creg_val;
    ViBusAddress portA_addr, offset;
    ViSession hdl1260;    /* VISA handle to the 1260-01T */
    ViSession hdlRM;     /* VISA handle to the resource manager */
    ViStatus error;     /* VISA error code */

    /* open the resource manager */
    /* this must be done once in application program */
    error = viOpenDefaultRM (&hdlRM);

    if (error < 0) {
        /* error handling code goes here */
    }

    /* get a handle for the 1260-01T */
    error = viOpen (hdlRM, RI1260_01_DESC, VI_NULL,VI_NULL, &hdl1260);
    if (error < 0) {
        /* error handling code goes here */
    }
}
```

```
/* form the offset for control register 0 */
/* note that the base A24 Address for the 1260-01T */
/* is already accounted for by VISA calls viIn8() and */
/* viOut8() */

/* module address shifted 10 places = module address x 1024 */
portA_addr = (MOD_ADDR_160 << 10) + 1;
offset = portA_addr + (PORT_NUMBER << 1);
error = viOut8 (vi, VI_A24_SPACE, offset, DATA_ITEM);

if (error < 0)

    return( error );

/* close the VISA session */
error = viClose( hdl1260 );
if (error < 0) {
    /* error handling code goes here */
}
}
```

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